

NC7SB3157P6X-F40 Datasheet



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DiGi Electronics Part Number NC7SB

NC7SB3157P6X-F40-DG

Manufacturer onsemi

Manufacturer Product Number NC7SB3157P6X-F40

Description IC SWITCH SPDT X 1 150HM SC88

Detailed Description 1 Circuit IC Switch 2:1 150hm SC-88 (SC-70-6)



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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
NC7SB3157P6X-F40	onsemi
Series:	Product Status:
	Obsolete
Switch Circuit:	Multiplexer/Demultiplexer Circuit:
SPDT	2:1
Number of Circuits:	On-State Resistance (Max):
1	150hm
Channel-to-Channel Matching (ΔRon):	Voltage - Supply, Single (V+):
150mOhm	1.65V ~ 5.5V
Voltage - Supply, Dual (V±):	Switch Time (Ton, Toff) (Max):
	5.2ns, 3.5ns
-3db Bandwidth:	Charge Injection:
250MHz	7pC
Channel Capacitance (CS(off), CD(off)):	Current - Leakage (IS(off)) (Max):
6.5pF	100nA
Crosstalk:	Operating Temperature:
-54dB @ 10MHz	-40°C ~ 85°C (TA)
Mounting Type:	Package / Case:
Surface Mount	6-TSSOP, SC-88, SOT-363
Supplier Device Package:	Base Product Number:
SC-88 (SC-70-6)	NC7SB3157

Environmental & Export classification

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
EAR99	8542.39.0001



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March 2009

NC7SB3157, FSA3157 Low-Voltage SPDT Analog Switch or 2:1Multiplexer / De-multiplexer Bus Switch

Features

- Useful in Both Analog and Digital Applications
- Space-Saving, SC70 6-Lead Surface Mount Package
- Ultra-Small, MicroPak™ Leadless Package
- Low On Resistance: <10Ω on Typical at 3.3V V_{CC}
- Broad V_{CC} Operating Range: 1.65V to 5.5V
- Rail-to-Rail Signal Handling
- Power-Down, High-Impedance Control Input
- Over-Voltage Tolerance of Control Input to 7.0V
- Break-Before-Make Enable Circuitry
- 250MHz, 3dB Bandwidth

Description

The NC7SB3157 / FSA3157 is a high-performance, single-pole / double-throw (SPDT) analog switch or 2:1 multiplexer / de-multiplexer bus switch.

The device is fabricated with advanced sub-micron CMOS technology to achieve high-speed enable and disable times and low on resistance. The break-before-make select circuitry prevents disruption of signals on the B Port due to both switches temporarily being enabled during select pin switching. The device is specified to operate over the 1.65 to 5.5V $V_{\rm CC}$ operating range. The control input tolerates voltages up to 5.5V, independent of the $V_{\rm CC}$ operating range.

Ordering Information

Part Number	Top Mark	Eco Status	Package Description	Packing Method
NC7SB3157P6X	В7А	RoHS	6-Lead, SC70, EIAJ SC88, 1.25mm Wide Package	3000 Units on Tape and Reel
NC7SB3157L6X	ВВ	RoHS	6-Lead, MicroPak 1.0mm Wide Package	5000 Units on Tape and Reel
FSA3157P6X	В7А	RoHS	6-Lead, SC70, EIAJ SC88, 1.25mm Wide Package	3000 Units on Tape and Reel
FSA3157L6X	ВВ	RoHS	6-Lead, MicroPak 1.0mm Wide Package	5000 Units on Tape and Reel



For Fairchild's definition of "green" Eco Status, please visit: http://www.fairchildsemi.com/company/green/rohs_green.html.

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Logic Symbol

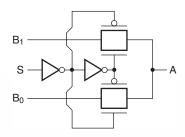


Figure 1. Logic Symbol

Analog Symbol

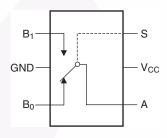


Figure 3. Analog Symbol

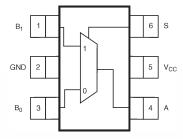
Function Table

Input (S)	Function
Logic Level Low	B ₀ Connected to A
Logic Level High	B ₁ Connected to A

Pin Descriptions

Pin Names Description			
A, B ₀ , B ₁	Data Ports		
S	Control Input		

Connection Diagrams



2. Pin Assignments SC70

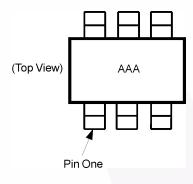


Figure 4. Pin One Orientation

Note:

Orientation of top mark determines pin one location. Read the top product code mark left to right and pin one is the lower left pin (see Figure 4).

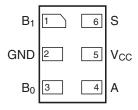


Figure 5. Pad Assignments for MicroPak™

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Min.	Max.	Unit
V _{CC}	Supply Voltage	-0.5	+7.0	V
V _S	DC Switch Voltage ⁽¹⁾	-0.5	V _{CC} +0.5	V
V _{IN}	DC Input Voltage ⁽¹⁾	-0.5	+7.0	V
I _{IK}	DC Input Diode Current at V _{IN} < 0V		– 50	mA
I _{OUT}	DC Output Current		128	mA
I _{CC} /I _{GND}	DC V _{CC} or Ground Current		±100	mA
T _{STG}	Storage Temperature Range	– 65	+150	°C
T _J	Junction Temperature Under Bias		+150	°C
TL	Junction Lead Temperature (Soldering, 10 seconds)		+260	°C
MSL	Moisture Sensitivity Level (JEDEC J-STD-020A)		1	Level
P _D	Power Dissipation at +85°C		180	mW
ESD	Human Body Model, JESD22-A114		4000	V

Note:

 The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. Fairchild does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Para	Parameter			Unit
V _{CC}	Supply Voltage Operating		1.65	5.50	V
V _{IN}	Control Input Voltage ⁽²⁾		0	V _{CC}	V
V _{IN}	Switch Input Voltage ⁽²⁾	Switch Input Voltage ⁽²⁾			V
V _{OUT}	Output Voltage ⁽²⁾	0	V _{CC}	V	
T _A	Operating Temperature		-40	+85	°C
	Input Rise and Fall Time	Control Input V _{CC} = 2.3V–3.6V	0	10	ns/V
t _r , t _f	t _r , t _f imput Nise and Fair fille	Control Input V _{CC} = 4.5V–5.5V	0	5	ns/V
θ_{JA}	Thermal Resistance, SC7		270	°C/W	

Note:

2. Control input must be held HIGH or LOW; it must not float.

DC Electrical Characteristics

Symbol	Parameter	Conditions	V _{CC} (V)	TA	= +25	°C		40°C to 5°C	Units
				Min.	Тур.	Max.	Min.	Max.	
\ <u>'</u>	High Level		1.65 – 1.95	0.75 V _{CC}			0.75 V _{CC}		V
V _{IH}	Input Voltage		2.3 – 5.5	0.7 V _{CC}			0.7 V _{CC}		V
\ <u>'</u>	Low Level		1.65 – 1.95			0.25 V _{CC}		0.25 V _{CC}	V
V_{IL}	Input Voltage		2.3 – 5.5			0.3 V _{CC}		0.3 V _{CC}	v
I _{IN}	Input Leakage Current	$0 \le V_{IN} \le 5.5V$	0 – 5.5		±0.05	±0.1		±1	μΑ
I _{OFF}	Off State Leakage Current	0 ≤ A, B ≤ V _{CC}	1.65 – 5.5		±0.05	±0.1		±1	μΑ
		V _{IN} = 0V, I _O = 30mA	4.5		3.0	7.0		7.0	
		V _{IN} = 2.4V, I _O = -30mA			5.0	12.0		12.0	
		$V_{IN} = 4.5V, I_{O} = -30mA$			7.0	15.0		15.0	
		V _{IN} = 0V, I _O = 24mA	3.0		4.0	9.0		9.0	
R _{ON}	Switch On Resistance ⁽³⁾	$V_{IN} = 3V, I_{O} = -24mA$			10.0	20.0		20.0	Ω
	rtoolotarioo	V _{IN} = 0V, I _O = 8mA	2.3		5.0	12.0		12.0	
		$V_{IN} = 2.3V, I_{O} = -8mA$			13.0	30.0		30.0	
		V _{IN} = 0V, I _O = 4mA	1.65		6.5	20.0		20.0	
		V _{IN} = 1.65V, I _O = -4mA			17.0	50.0		50.0	
I _{CC}	Quiescent Supply Current; All Channels On or Off	V _{IN} = V _{CC} or GND I _{OUT} = 0	5.5			1		10	μА
	Analog Signal Range		V _{CC}	0		V _{CC}	0	V _{CC}	٧
		$I_A = -30 \text{mA}, \ 0 \le V_{Bn} \le V_{CC}$	4.5					25.0	
	On Resistance Over	$I_A = -24mA$, $0 \le V_{Bn} \le V_{CC}$	3.0					50.0	
R _{RANGE}	Signal Range (3, 7)	$I_A = -8mA$, $0 \le V_{Bn} \le V_{CC}$	2.3					100	Ω
		$I_A = -4mA$, $0 \le V_{Bn} \le V_{CC}$	1.65					300	
		I _A = -30mA, V _{Bn} = 3.15	4.5		0.15				
_ \	On Resistance	I _A = -24mA, V _{Bn} 2.1	3.0		0.2	4			
ΔR _{ON}	ΔR _{ON} Match Between- Channels ^(3, 4, 5)	I _A = -8mA, V _{Bn} = 1.6	2.3		0.5				Ω
		I _A = -4mA, V _{Bn} = 1.15	1.65		0.50			7	
		$I_A = -30 \text{mA}, \ 0 \le V_{Bn} \le V_{CC}$	5.0		6.0				
_	On Resistance	$I_A = -24$ mA, $0 \le V_{Bn} \le V_{CC}$	3.3		12.0				
R _{flat}	Flatness ^(3, 4, 6)	$I_A = -8mA$, $0 \le V_{Bn} \le V_{CC}$	2.5		28.0				Ω
		$I_A = -4mA$, $0 \le V_{Bn} \le V_{CC}$	1.8		125				1

Notes:

- 3. Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B Ports).
- 4. Parameter is characterized, but not tested in production.
- 5. $\Delta R_{ON} = R_{ON} \max R_{ON} \min$ minimum measured at identical V_{CC} , temperature, and voltage levels.
- 6. Flatness is defined as the difference between the maximum and minimum value of on resistance over the specified range of conditions.
- 7. Guaranteed by design.

AC Electrical Characteristics

Symbol	Parameter Conditions		litions +85°C		°C		Units	Figure Number			
			(V)	Min.	Тур.	Max.	Min.	Max.		Number	
			1.65 – 1.95			3.5		3.5			
t _{PHL} ,	t _{PHL} , Propagation Delay	V _I = OPEN	2.3 – 2.7			1.2		1.2	1	Figure 12	
t_{PLH}	Bus-to-Bus ⁽⁸⁾	VI - OPEN	3.0 – 3.6			0.8		0.8	ns	Figure 13	
			4.5 – 5.5			0.3		0.3			
			1.65 – 1.95	7.0		23.0	7.0	24.0			
t_{PZL} ,	Output Enable Time Turn-On Time	$V_I = 2 \times V_{CC}$ for t_{PZL}	2.3 – 2.7	3.5		13.0	3.5	14.0	ns	Figure 12	
t_{PZH}	(A to B _n)	$V_I = 0V$ for t_{PZH}	3.0 – 3.6	2.5		6.9	2.5	7.6	115	Figure 13	
			4.5 – 5.5	1.7		5.2	1.7	5.7			
	A		1.65 – 1.95	3.0		12.5	3.0	13.0			
t _{PLZ} ,	t _{PLZ} , t _{PHZ} Output Disable Time Turn-Off Time (A Port to B Port)	$V_I = 2 \times V_{CC}$ for t_{PLZ} $V_I = 0V$ for t_{PHZ}	2.3 – 2.7	2.0		7.0	2.0	7.5	ns	Figure 12 Figure 13	
t_{PHZ}			3.0 – 3.6	1.5		5.0	1.5	5.3			
			4.5 – 5.5	0.8		3.5	0.8	3.8			
			1.65 –1.95	0.5			0.5				
	Break-Before-Make		2.3 – 2.7	0.5			0.5			Figure 44	
t _{B-M}	Time ⁽⁹⁾		3.0 – 3.6	0.5			0.5		ns	Figure 14	
			4.5 – 5.5	0.5			0.5				
Q	Charge Injection ⁽⁹⁾	$C_L = 0.1 nF, V_{GEN} = 0V,$	5.0		7.0				рС	Figure 15	
Q	Charge injection.	$R_{GEN} = 0\Omega$	3.3		3.0				ρC	l igule 15	
OIRR	Off Isolation ⁽¹⁰⁾	$R_L = 50\Omega$, $f = 10MHz$	1.65 – 5.5		-57.0				dB	Figure 16	
Xtalk	Crosstalk	$R_L = 50\Omega$, $f = 10MHz$	1.65 – 5.5		-54.0				dB	Figure 17	
BW	-3dB Bandwidth	$R_L = 50\Omega$	1.65 – 5.5		250				MHz	Figure 20	
THD	Total Harmonic Distortion ⁽⁹⁾	R _L = 600Ω, 0.5 V _{PP} , f = 600 Hz to 20 KHz	5.0		.011				%		

Notes:

- 8. This parameter is guaranteed by design but not tested. The bus switch contributes no propagation delay other than the RC delay of the on resistance of the switch and the 50pF load capacitance, when driven by an ideal voltage source (zero output impedance).
- 9. Guaranteed by design.
- 10. Off Isolation = $20 \log_{10} [V_A / V_{Bn}]$.

Capacitance

 T_A = +25°C, f = 1MHz. Capacitance is characterized, but not tested in production.

Symbol	Parameter	Conditions	Тур.	Max.	Units	Figure Number
C _{IN}	Control Pin Input Capacitance	V _{CC} = 0V	2.3		pF	$V \rightarrow$
C _{IO-B}	B Port Off Capacitance	V _{CC} = 5.0V	6.5		pF	Figure 18
C _{IOA-ON}	A Port Capacitance When Switch Is Enabled	V _{CC} = 5.0V	18.5		pF	Figure 19

Typical Characteristics

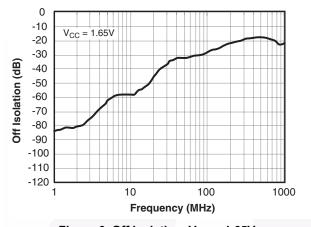


Figure 6. Off Isolation, $V_{CC} = 1.65V$

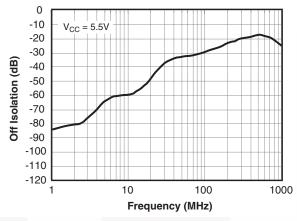


Figure 7. Off Isolation, $V_{CC} = 5.5V$

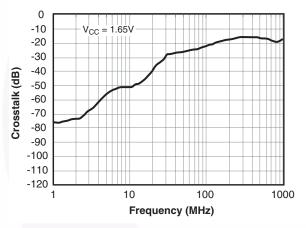


Figure 8. Crosstalk, V_{CC} = 1.65V

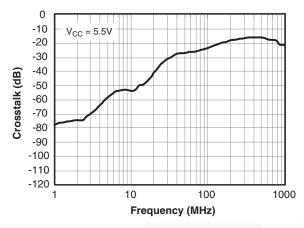


Figure 9. Crosstalk, $V_{CC} = 5.5V$

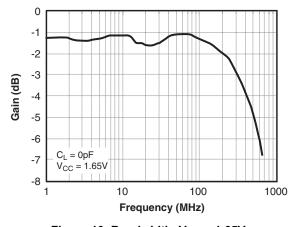


Figure 10. Bandwidth, V_{CC} = 1.65V

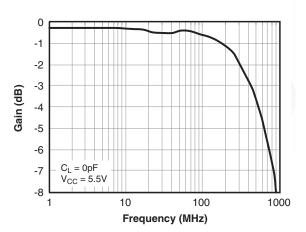
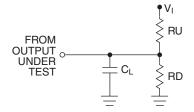


Figure 11. Bandwidth, $V_{CC} = 5.5V$

AC Loading and Waveforms



Notes:

Input driven by 50Ω source terminated in 50Ω C_L includes load and stray capacitance Input PRR = 1.0 MHz; t_W = 500 ns

Figure 12. AC Test Circuit

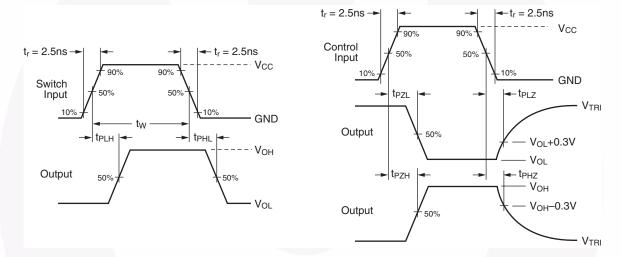


Figure 13. AC Waveforms

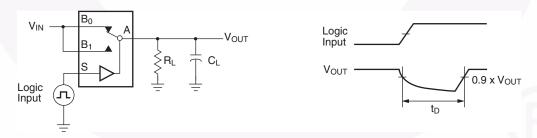


Figure 14. Break-Before-Make Interval Timing

AC Loading and Waveforms (continued)

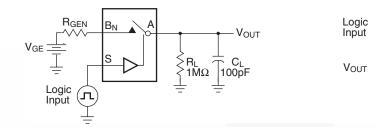


Figure 15. Charge Injection Test

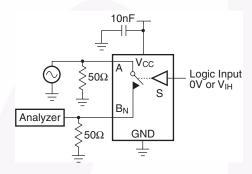


Figure 16. Off Isolation

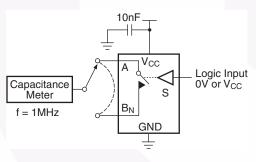
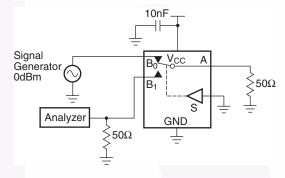


Figure 18. Channel Off Capacitance



ON

 $Q = (\Delta V_{OUT})(C_L)$

OFF

↓ <u>Δ</u>V_{OUT}

OFF

Figure 17. Crosstalk

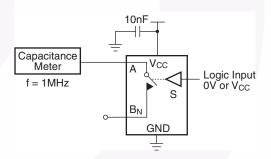


Figure 19. Channel On Capacitance

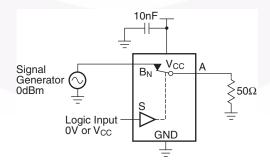


Figure 20. Bandwidth

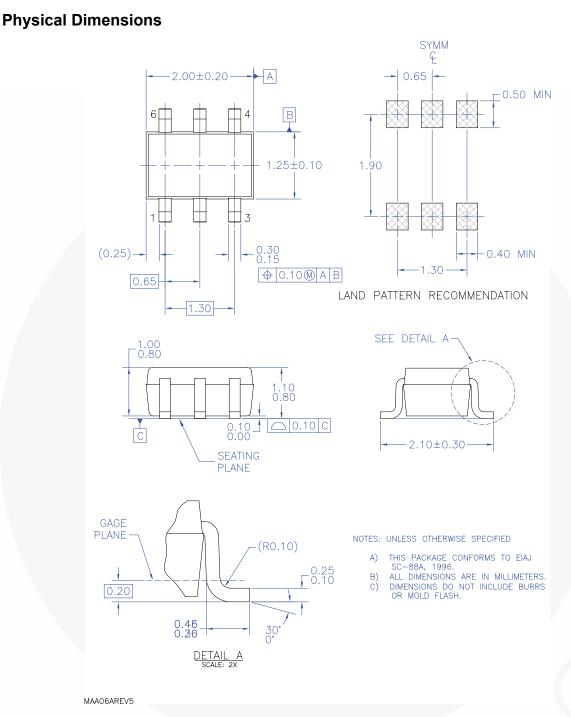


Figure 21. 6-Lead, SC70, EIAJ SC88, 1.25mm Wide Package

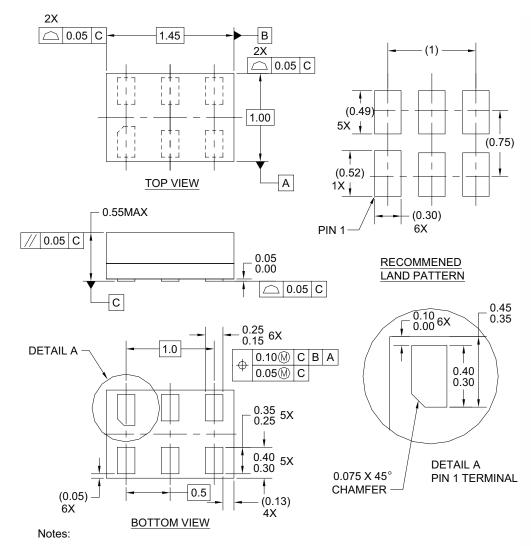
Note: click here for tape and reel specifications, available at: http://www.fairchildsemi.com/products/analog/pdf/sc70-6 tr.pdf

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Physical Dimensions



- 1. CONFORMS TO JEDEC STANDARD M0-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-1994

MAC06AREVC

6-Lead, MicroPak™ 1.0mm Wide Package

Note: click here for tape and reel specifcations, available at:

http://www.fairchildsemi.com/products/logic/pdf/micropak_tr.pdf

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