

## NC7SP86L6X Datasheet



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DiGi Electronics Part Number NC7SP86L6X-DG

Manufacturer onsemi

Manufacturer Product Number NC7SP86L6X

Description IC GATE XOR 1CH 2-INP 6MICROPAK

Detailed Description XOR (Exclusive OR) IC 1 Channel 6-MicroPak



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## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
NC7SP86L6X	onsemi
Series:	Product Status:
7SP	Obsolete
Logic Type:	Number of Circuits:
XOR (Exclusive OR)	1
Number of Inputs:	Features:
2	
Voltage - Supply:	Current - Quiescent (Max):
0.9V ~ 3.6V	900 nA
Current - Output High, Low:	Input Logic Level - Low:
2.6mA, 2.6mA	0.7V ~ 0.9V
Input Logic Level - High:	Max Propagation Delay @ V, Max CL:
1.6V ~ 2.1V	8ns @ 3.3V, 30pF
Operating Temperature:	Mounting Type:
-40°C ~ 85°C	Surface Mount
Supplier Device Package:	Package / Case:
6-MicroPak	6-UFDFN
Base Product Number:	
7SP86	

## **Environmental & Export classification**

8542.39.0001

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



# TinyLogic ULP-A Exclusive-OR Gate

### NC7SP86

The NC7SP86 is a single 2-input exclusive-OR gate in tiny footprint packages. The device is designed to operate for  $V_{\rm CC}$  = 0.9 V to 3.6 V.

#### **Features**

- Designed for 0.9 V to 3.6 V V<sub>CC</sub> Operation
- 2.2 ns t<sub>PD</sub> at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I<sub>OFF</sub> Supports Partial Power Down Protection
- Source/Sink 2.6 mA at 3.3 V
- Available in SC−88A and MicroPak<sup>™</sup> Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

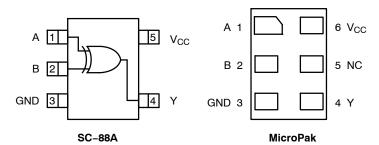


Figure 1. Pinout Diagrams (Top Views)

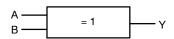


Figure 2. Logic Symbol

#### MARKING DIAGRAMS



SIP6 1.45X1.0 MicroPak CASE 127EB



CC = Specific Device Code

KK = 2-Digit Lot Run Traceability Code

XY = 2-Digit Date Code Z = Assembly Plant Code





XXX = Specific Device Code

M = Date Code

= Pb-Free Package

#### **ORDERING INFORMATION**

See detailed ordering, marking and shipping information on page 6 of this data sheet.

#### **PIN ASSIGNMENT**

Pin	SC88A	MicroPak
1	Α	А
2	В	В
3	GND	GND
4	Υ	Υ
5	V <sub>CC</sub>	N.C.
6	-	$V_{CC}$

N.C. = No Connect

#### **FUNCTION TABLE**

1

Inp	Output	
Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

#### **MAXIMUM RATINGS**

Symbol	Characteristics		Value	Unit
V <sub>CC</sub>	DC Supply Voltage		-0.5 to +4.3	V
V <sub>IN</sub>	DC Input Voltage		-0.5 to +4.3	V
V <sub>OUT</sub>	DC Output Voltage Active	-0.5 to V <sub>CC</sub> + 0.5 -0.5 to +4.3 -0.5 to +4.3	V	
I <sub>IK</sub>	DC Input Diode Current	V <sub>IN</sub> < GND	-50	mA
l <sub>ok</sub>	DC Output Diode Current	V <sub>OUT</sub> < GND	-50	mA
l <sub>OUT</sub>	DC Output Source/Sink Current		±50	mA
I <sub>CC</sub> or I <sub>GND</sub>	DC Supply Current per Supply Pin or Ground Pin		±50	mA
T <sub>STG</sub>	Storage Temperature Range	-65 to +150	°C	
TL	Lead Temperature, 1 mm from Case for 10 Seconds		260	°C
TJ	Junction Temperature Under Bias		+150	°C
θЈА	Thermal Resistance (Note 2)	SC-88A MicroPak	377 154	°C/W
P <sub>D</sub>	Power Dissipation in Still Air	SC-88A MicroPak	332 812	mW
MSL	Moisture Sensitivity		Level 1	-
F <sub>R</sub>	Flammability Rating	Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V <sub>ESD</sub>	ESD Withstand Voltage (Note 3)	Human Body Model Charged Device Model	2000 1000	V
I <sub>Latchup</sub>	Latchup Performance (Note 4)		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Applicable to devices with outputs that may be tri-stated.
   Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
   HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.
- 4. Tested to EIA/JESD78 Class II.

#### RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter			Max	Unit
V <sub>CC</sub>	Positive DC Supply Voltage		0.9	3.6	V
V <sub>IN</sub>	DC Input Voltage		0	3.6	V
V <sub>OUT</sub>	DC Output Voltage	Active–Mode (High or Low State) Tri–State Mode (Note 1) Power–Down Mode ( $V_{CC} = 0 \text{ V}$ )	0 0 0	V <sub>CC</sub> 3.6 3.6	
T <sub>A</sub>	Operating Temperature Range		-40	+85	°C
t <sub>r</sub> , t <sub>f</sub>	Input Transition Rise and Fall Time	$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$	0	10	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

#### DC ELECTRICAL CHARACTERISTICS

				T	A = 25°	С	T <sub>A</sub> = -40°0	C to +85°C	
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
$V_{IH}$	High-Level Input		0.9	_	0.5	_	-	-	V
	Voltage		1.1 to 1.3	0.65 x V <sub>CC</sub>	_	-	0.65 x V <sub>CC</sub>	-	
			1.4 to 1.6	0.65 x V <sub>CC</sub>	_	-	0.65 x V <sub>CC</sub>	-	
			1.65 to 1.95	0.65 x V <sub>CC</sub>	_	-	0.65 x V <sub>CC</sub>	-	
			2.3 to 2.7	1.6	_	-	1.6	-	
			3.0 to 3.6	2.1	_	-	2.1	-	
V <sub>IL</sub>	Low-Level Input		0.9	-	0.5	-	-	-	V
	Voltage		1.1 to 1.3	-	-	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>	
			1.4 to 1.6	-	_	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>	
			1.65 to 1.95	-	_	0.35 x V <sub>CC</sub>	-	0.35 x V <sub>CC</sub>	
			2.3 to 2.7	-	_	0.7	-	0.7	
			3.0 to 3.6	-	_	0.9	-	0.9	
V <sub>OH</sub>	High-Level Output	$V_{IN} = V_{IH}$ or $V_{IL}$							V
	Voltage	I <sub>OH</sub> = -20 μA	0.9	-	V <sub>CC</sub> - 0.1	-	-	-	
			1.1 to 1.3	V <sub>CC</sub> - 0.1	_	-	V <sub>CC</sub> - 0.1	-	
			1.4 to 1.6	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> - 0.1	-	
			1.65 to 1.95	V <sub>CC</sub> - 0.1	-	-	V <sub>CC</sub> - 0.1	-	
			2.3 to 2.7	V <sub>CC</sub> - 0.1	_	-	V <sub>CC</sub> - 0.1	-	
			3.0 to 3.6	V <sub>CC</sub> - 0.1	_	_	V <sub>CC</sub> - 0.1	-	
		$I_{OH} = -0.5 \text{ mA}$	1.1 to 1.3	0.75 x V <sub>CC</sub>	-	-	0.70 x V <sub>CC</sub>	-	
		I <sub>OH</sub> = -1 mA	1.4 to 1.6	1.07	_	-	0.99	-	
		I <sub>OH</sub> = -1.5 mA	1.65 to 1.95	1.24	-	-	1.22	-	
		I <sub>OH</sub> = -2.1 mA	2.3 to 2.7	1.95	_	_	1.87	-	
		$I_{OH} = -2.6 \text{ mA}$	3.0 to 3.6	2.61	-	-	2.55	-	
$V_{OL}$	Low-Level Output	$V_{IN} = V_{IH}$ or $V_{IL}$							V
	Voltage	I <sub>OL</sub> = 20 μA	0.9	_	0.1	_	-	-	
			1.1 to 1.3	-	-	0.1	-	0.1	
			1.4 to 1.6	-	_	0.1	-	0.1	
			1.65 to 1.95	-	_	0.1	-	0.1	
			2.3 to 2.7	-	-	0.1	-	0.1	
			3.0 to 3.6	_	_	0.1	-	0.1	
		I <sub>OL</sub> = 0.5 mA	1.1 to 1.3	-	_	0.3 x V <sub>CC</sub>	-	0.3 x V <sub>CC</sub>	
		I <sub>OL</sub> = 1 mA	1.4 to 1.6	-	-	0.31	-	0.37	
		I <sub>OL</sub> = 1.5 mA	1.65 to 1.95	_	_	0.31	-	0.35	
		I <sub>OL</sub> = 2.1 mA	2.3 to 2.7	-	-	0.31	-	0.33	
		I <sub>OL</sub> = 2.6 mA	3.0 to 3.6	-	-	0.31	-	0.33	
I <sub>IN</sub>	Input Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V	0.9 to 3.6	-	-	±0.1	-	±0.5	μΑ
I <sub>OFF</sub>	Power Off Leakage Current	V <sub>IN</sub> = 0 V to 3.6 V or V <sub>OUT</sub> = 0 V to 3.6 V	0	-	_	0.5	-	0.5	μΑ
I <sub>CC</sub>	Quiescent Supply Current	V <sub>IN</sub> = V <sub>CC</sub> or GND	0.9 to 3.6	-	_	0.9	-	0.9	μΑ

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

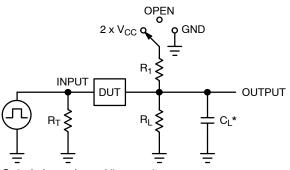
#### **AC ELECTRICAL CHARACTERISTICS**

			$T_A = 25^{\circ}C$ $T_A = -40^{\circ}C \text{ to } +85^{\circ}C$		T <sub>A</sub> = 25°C T <sub>A</sub> =		C to +85°C		
Symbol	Parameter	Condition	V <sub>CC</sub> (V)	Min	Тур	Max	Min	Max	Unit
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay,	$R_L = 1 \text{ M}\Omega$ , $C_L = 10 \text{ pF}$	0.9	-	44.8	-	-	-	ns
	(A or B) to Y (Figures 3 and 4)		1.10 to 1.30	-	12.3	24.7	-	32.9	
	,		1.40 to 1.60	-	6.3	13.9	_	15.6	
			1.65 to 1.95	-	4.3	11.1	-	13.6	
			2.3 to 2.7	-	2.9	8.0	-	9.0	
			3.0 to 3.6	-	2.2	6.7	_	7.9	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay, (A or B) to Y (Figures 3 and 4)	(A or B) to Y	0.9	-	46.3	-	-	-	ns
			1.10 to 1.30	-	12.8	26.0	_	35.9	
		,	1.40 to 1.60	-	6.7	14.8	-	17.5	
			1.65 to 1.95	-	4.7	11.8	-	14.1	
			2.3 to 2.7	-	3.2	8.4	-	9.2	
			3.0 to 3.6	_	2.4	7.1	-	8.0	
t <sub>PLH</sub> , t <sub>PHL</sub>	Propagation Delay,	$R_L = 1 \text{ M}\Omega$ , $C_L = 30 \text{ pF}$	0.9	_	50.6	-	-	-	ns
	(A or B) to Y (Figures 3 and 4)		1.10 to 1.30	_	14.4	30.0	-	45.0	
			1.40 to 1.60	-	8.0	17.5	-	19.5	
			1.65 to 1.95	-	5.8	13.5	-	15.5	
			2.3 to 2.7	-	3.9	10.0	-	11.0	
			3.0 to 3.6	-	3.0	8.0	_	9.9	

#### **CAPACITIVE CHARACTERISTICS**

Symbol	Parameter	Parameter Test Condition 1		Unit
C <sub>IN</sub>	Input Capacitance	V <sub>CC</sub> = 0 V	2.0	pF
C <sub>OUT</sub>	Output Capacitance	V <sub>CC</sub> = 0 V	4.0	pF
C <sub>PD</sub>	Power Dissipation Capacitance (Note 5)	$f = 10$ MHz, $V_{CC} = 0.9$ to 3.6 V, $V_{IN} = 0$ V or $V_{CC}$	6.0	pF

<sup>5.</sup> C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation I<sub>CC(OPR)</sub> = C<sub>PD</sub> • V<sub>CC</sub> • f<sub>in</sub> + I<sub>CC</sub>. C<sub>PD</sub> is used to determine the no–load dynamic power consumption: P<sub>D</sub> = C<sub>PD</sub> • V<sub>CC</sub><sup>2</sup> • f<sub>in</sub> + I<sub>CC</sub> • V<sub>CC</sub>.



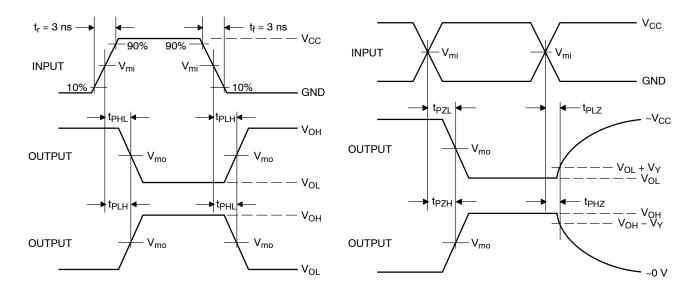
Test	Switch Position
t <sub>PLH</sub> / t <sub>PHL</sub>	Open
t <sub>PLZ</sub> / t <sub>PZL</sub>	2 x V <sub>CC</sub>
t <sub>PHZ</sub> / t <sub>PZH</sub>	GND

 $C_{\mbox{\scriptsize L}}$  includes probe and jig capacitance

 $R_T$  is  $Z_{OUT}$  of pulse generator (typically 50  $\Omega$ )

f = 1 MHz

Figure 3. Test Circuit



V <sub>CC</sub> , V	V <sub>mi</sub> , V	V <sub>mo</sub> , V	V <sub>Y</sub> , V
0.9	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.1
1.1 to 1.3	V <sub>CC</sub> /2	V <sub>CC</sub> /2	0.1
1.4 to 1.6	V <sub>CC</sub> / 2	V <sub>CC</sub> /2	0.1
1.65 to 1.95	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
2.3 to 2.7	V <sub>CC</sub> / 2	V <sub>CC</sub> / 2	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 4. Switching Waveforms

#### **ORDERING INFORMATION**

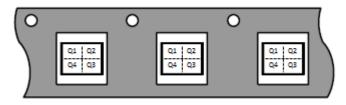
Device	Package	Marking	Pin 1 Orientation (See below)	Shipping <sup>†</sup>
NC7SP86P5X	SC-88A	P86	Q4	3000 / Tape & Reel
NC7SP86L6X	MicroPak	L4	Q4	5000 / Tape & Reel

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### Pin 1 Orientation in Tape and Reel

#### Direction of Feed

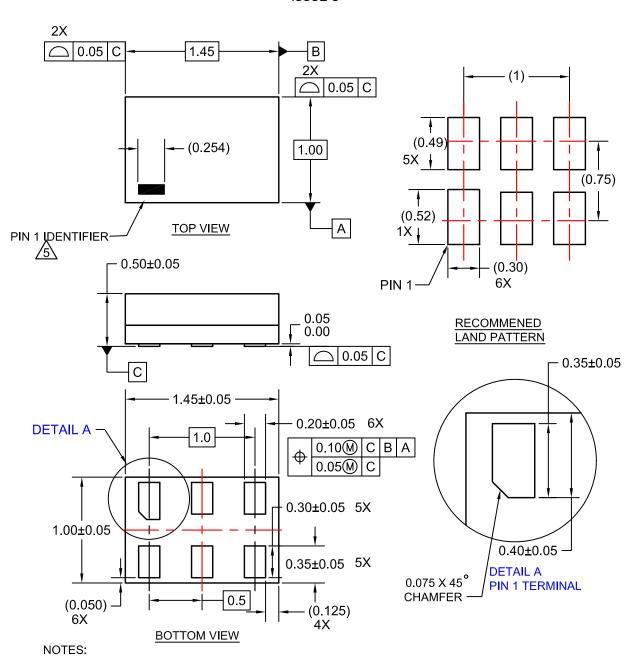




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#### **PACKAGE DIMENSIONS**

SIP6 1.45X1.0 CASE 127EB ISSUE O

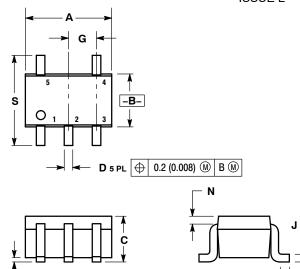


- 1, CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
- 2. DIMENSIONS ARE IN MILLIMETERS
- 3. DRAWING CONFORMS TO ASME Y14.5M-2009
  4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY

OTHER LINE IN THE MARK CODE LAYOUT.

#### PACKAGE DIMENSIONS

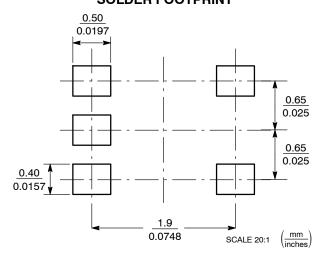
#### SC-88A (SC-70-5/SOT-353) CASE 419A-02 **ISSUE L**



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
   CONTROLLING DIMENSION: INCH.
- 419A-01 OBSOLETE. NEW STANDARD 419A-02
- DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.071	0.087	1.80	2.20
В	0.045	0.053	1.15	1.35
С	0.031	0.043	0.80	1.10
D	0.004	0.012	0.10	0.30
G	0.026 BSC		0.65 BSC	
Н		0.004		0.10
J	0.004	0.010	0.10	0.25
K	0.004	0.012	0.10	0.30
N	0.008 REF		0.20 REF	
S	0.079	0.087	2 00	2 20

#### **SOLDER FOOTPRINT**



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Phone: 00421 33 790 2910

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