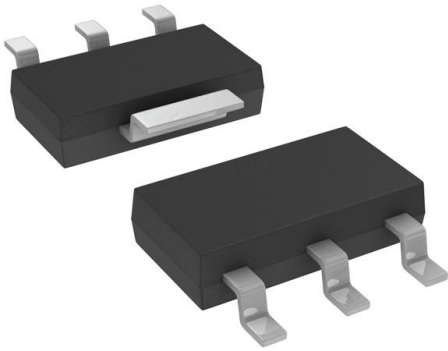


# NCV8402ASTT3G Datasheet

[www.digi-electronics.com](http://www.digi-electronics.com)



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	NCV8402ASTT3G-DG
Manufacturer	<a href="#">onsemi</a>
Manufacturer Product Number	NCV8402ASTT3G
Description	IC PWR DRIVER N-CHAN 1:1 SOT223
Detailed Description	Power Switch/Driver 1:1 N-Channel 2A SOT-223 (TO-261)



Tel: +00 852-30501935

RFQ Email: [Info@DiGi-Electronics.com](mailto:Info@DiGi-Electronics.com)

DiGi is a global authorized distributor of electronic components.

## Purchase and inquiry

Manufacturer Product Number:

NCV8402ASTT3G

Series:

-

Switch Type:

General Purpose

Ratio - Input:Output:

1:1

Output Type:

N-Channel

Voltage - Load:

42V (Max)

Current - Output (Max):

2A

Input Type:

Non-Inverting

Fault Protection:

Current Limiting (Fixed), Over Temperature, Over Voltage

Grade:

Automotive

Mounting Type:

Surface Mount

Package / Case:

TO-261-4, TO-261AA

Manufacturer:

onsemi

Product Status:

Active

Number of Outputs:

1

Output Configuration:

Low Side

Interface:

On/Off

Voltage - Supply (Vcc/Vdd):

Not Required

Rds On (Typ):

165mOhm

Features:

Auto Restart

Operating Temperature:

-40°C ~ 150°C (TJ)

Qualification:

AEC-Q100

Supplier Device Package:

SOT-223 (TO-261)

Base Product Number:

NCV8402

## Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8542.39.0001

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

# Self-Protected Low Side Driver with Temperature and Current Limit

## NCV8402, NCV8402A

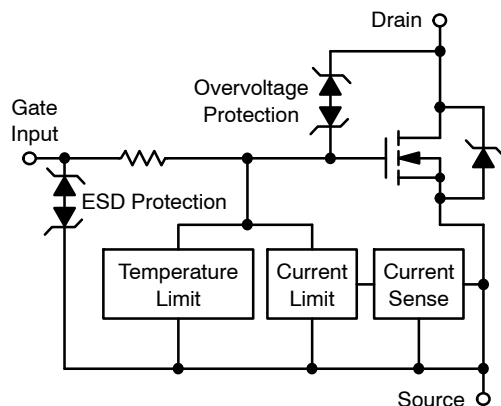
NCV8402/A is a three terminal protected Low-Side Smart Discrete device. The protection features include overcurrent, overtemperature, ESD and integrated Drain-to-Gate clamping for overvoltage protection. This device offers protection and is suitable for harsh automotive environments.

### Features

- Short-Circuit Protection
- Thermal Shutdown with Automatic Restart
- Overvoltage Protection
- Integrated Clamp for Inductive Switching
- ESD Protection
- NCV8402AMNWT1G – Wettable Flanks Product
- dV/dt Robustness
- Analog Drive Capability (Logic Level Input)
- NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

### Typical Applications

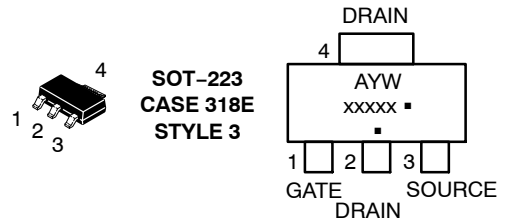
- Switch a Variety of Resistive, Inductive and Capacitive Loads
- Can Replace Electromechanical Relays and Discrete Circuits
- Automotive / Industrial



$V_{(BR)DSS}$ (Clamped)	$R_{DS(ON)}$ TYP	$I_D$ MAX
42 V	165 mΩ @ 10 V	2.0 A*

\*Max current limit value is dependent on input condition.

### MARKING DIAGRAMS



**SOT-223**  
**CASE 318E**  
**STYLE 3**



**DFN6**  
**CASE 506AX**

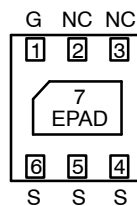


**DFN6 (WF)**  
**CASE 506DK**

A = Assembly Location  
 Y = Year  
 W or WW = Work Week  
 xxxxx = V8402 or 8402A  
 ■ = Pb-Free Package

(Note: Microdot may be in either location)

### DFN6 PACKAGE PIN DESCRIPTION



Pin #	Symbol	Description
1	G	Gate Input
2	NC	No Connect
3	NC	No Connect
4	S*	Source
5	S*	Source
6	S*	Source
7	EPAD	Drain

\*Pins 4, 5, 6 are internally shorted together. It is recommended to short these pins externally.

### ORDERING INFORMATION

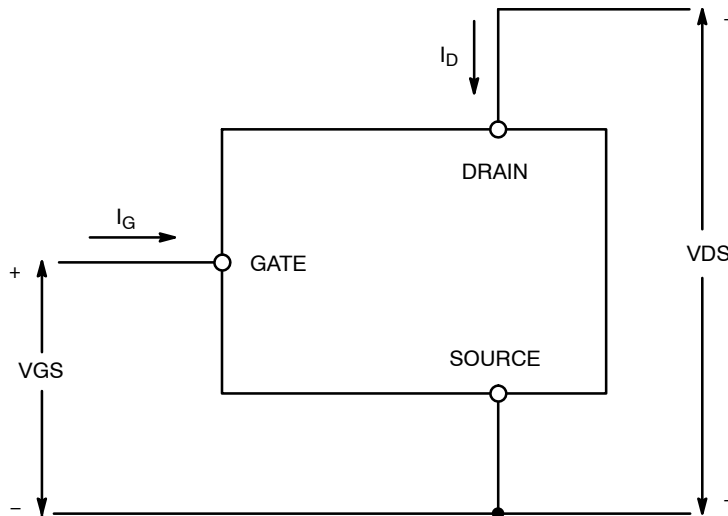
See detailed ordering and shipping information on page 11 of this data sheet.

**NCV8402, NCV8402A****MAXIMUM RATINGS** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage Internally Clamped	$V_{DSS}$	42	V
Drain-to-Gate Voltage Internally Clamped ( $R_G = 1.0\text{ M}\Omega$ )	$V_{DGR}$	42	V
Gate-to-Source Voltage	$V_{GS}$	$\pm 14$	V
Continuous Drain Current	$I_D$	Internally Limited	
Total Power Dissipation – SOT-223 Version @ $T_A = 25^\circ\text{C}$ (Note 1) @ $T_A = 25^\circ\text{C}$ (Note 2) @ $T_S = 25^\circ\text{C}$	$P_D$	1.1 1.74 8.9	W
Total Power Dissipation – DFN Version @ $T_A = 25^\circ\text{C}$ (Note 1) @ $T_A = 25^\circ\text{C}$ (Note 2) @ $T_S = 25^\circ\text{C}$	$P_D$	0.76 1.78 8.9	W
Maximum Continuous Drain Current – SOT-223 Version @ $T_A = 25^\circ\text{C}$ (Note 1) @ $T_A = 25^\circ\text{C}$ (Note 2) @ $T_S = 25^\circ\text{C}$	$I_D$	1.54 1.94 6.75	A
Maximum Continuous Drain Current – DFN Version @ $T_A = 25^\circ\text{C}$ (Note 1) @ $T_A = 25^\circ\text{C}$ (Note 2) @ $T_S = 25^\circ\text{C}$	$I_D$	1.28 1.97 6.75	A
Thermal Resistance SOT223 Junction-to-Ambient Steady State (Note 1) SOT223 Junction-to-Ambient Steady State (Note 2) SOT223 Junction-to-Soldering Point Steady State  DFN Junction-to-Ambient Steady State (Note 1) DFN Junction-to-Ambient Steady State (Note 2) DFN Junction-to-Soldering Point Steady State	$R_{\theta JA}$ $R_{\theta JA}$ $R_{\theta JS}$  $R_{\theta JA}$ $R_{\theta JA}$ $R_{\theta JS}$	114 72 14  163 70 14	$^\circ\text{C/W}$
Single Pulse Drain-to-Source Avalanche Energy ( $V_{DD} = 32\text{ V}$ , $V_G = 5.0\text{ V}$ , $I_{PK} = 1.0\text{ A}$ , $L = 300\text{ mH}$ , $R_{G(ext)} = 25\ \Omega$ )	$E_{AS}$	150	mJ
Load Dump Voltage ( $V_{GS} = 0$ and $10\text{ V}$ , $R_I = 2.0\ \Omega$ , $R_L = 9.0\ \Omega$ , $t_d = 400\text{ ms}$ )	$V_{LD}$	55	V
Operating Junction Temperature	$T_J$	-40 to 150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-55 to 150	$^\circ\text{C}$

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

- Surface-mounted onto min pad FR4 PCB, (2 oz. Cu, 0.06" thick).
- Surface-mounted onto 2" sq. FR4 board (1" sq., 1 oz. Cu, 0.06" thick).



**Figure 1. Voltage and Current Convention**

**NCV8402, NCV8402A****ELECTRICAL CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Test Condition	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>						
Drain-to-Source Breakdown Voltage (Note 3)	$V_{GS} = 0\text{ V}, I_D = 10\text{ mA}, T_J = 25^\circ\text{C}$	$V_{(BR)DSS}$	42	46	55	V
	$V_{GS} = 0\text{ V}, I_D = 10\text{ mA}, T_J = 150^\circ\text{C}$ (Note 5)		40	45	55	
Zero Gate Voltage Drain Current	$V_{GS} = 0\text{ V}, V_{DS} = 32\text{ V}, T_J = 25^\circ\text{C}$	$I_{DSS}$		0.25	4.0	$\mu\text{A}$
Zero Gate Voltage Drain Current	$V_{GS} = 0\text{ V}, V_{DS} = 32\text{ V}, T_J = 150^\circ\text{C}$ (Note 5)	$I_{DSS}$		1.1	20	$\mu\text{A}$
Gate Input Current	$V_{DS} = 0\text{ V}, V_{GS} = 5.0\text{ V}$	$I_{GSSF}$		50	100	$\mu\text{A}$

**ON CHARACTERISTICS** (Note 3)

Gate Threshold Voltage	$V_{GS} = V_{DS}, I_D = 150\text{ }\mu\text{A}$	$V_{GS(th)}$	1.3	1.8	2.2	V
Gate Threshold Temperature Coefficient		$V_{GS(th)}/T_J$		4.0		$-\text{mV}/^\circ\text{C}$
Static Drain-to-Source On-Resistance	$V_{GS} = 10\text{ V}, I_D = 1.7\text{ A}, T_J = 25^\circ\text{C}$	$R_{DS(on)}$		165	200	$\text{m}\Omega$
	$V_{GS} = 10\text{ V}, I_D = 1.7\text{ A}, T_J = 150^\circ\text{C}$ (Note 5)			305	400	
	$V_{GS} = 5.0\text{ V}, I_D = 1.7\text{ A}, T_J = 25^\circ\text{C}$			195	230	
	$V_{GS} = 5.0\text{ V}, I_D = 1.7\text{ A}, T_J = 150^\circ\text{C}$ (Note 5)			360	460	
	$V_{GS} = 5.0\text{ V}, I_D = 0.5\text{ A}, T_J = 25^\circ\text{C}$			190	230	
	$V_{GS} = 5.0\text{ V}, I_D = 0.5\text{ A}, T_J = 150^\circ\text{C}$ (Note 5)			350	460	
Source-Drain Forward On Voltage	$V_{GS} = 0\text{ V}, I_S = 7.0\text{ A}$	$V_{SD}$		1.0		V

**SWITCHING CHARACTERISTICS** (Note 5)

Turn-On Time (10% $V_{IN}$ to 90% $I_D$ )	$V_{GS} = 10\text{ V}, V_{DD} = 12\text{ V}, I_D = 2.5\text{ A}, R_L = 4.7\text{ }\Omega$	$t_{on}$		25	30	$\mu\text{s}$
Turn-Off Time (90% $V_{IN}$ to 10% $I_D$ )		$t_{off}$		120	200	$\mu\text{s}$
Turn-On Rise Time (10% $I_D$ to 90% $I_D$ )		$t_{rise}$		20	25	$\mu\text{s}$
Turn-Off Fall Time (90% $I_D$ to 10% $I_D$ )		$t_{fall}$		50	70	$\mu\text{s}$
Slew-Rate ON (70% to 50% $V_{DD}$ )		$-dV_{DS}/dt_{ON}$		0.8	1.2	$\text{V}/\mu\text{s}$
Slew-Rate OFF (50% to 70% $V_{DD}$ )		$dV_{DS}/dt_{OFF}$		0.3	0.5	$\text{V}/\mu\text{s}$

**SELF PROTECTION CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise noted) (Note 4)

Current Limit	$V_{DS} = 10\text{ V}, V_{GS} = 5.0\text{ V}, T_J = 25^\circ\text{C}$ (Note 6)	$I_{LIM}$	3.7	4.3	5.0	A
	$V_{DS} = 10\text{ V}, V_{GS} = 5.0\text{ V}, T_J = 150^\circ\text{C}$ (Notes 5, 6)		2.3	3.0	3.7	
	$V_{DS} = 10\text{ V}, V_{GS} = 10\text{ V}, T_J = 25^\circ\text{C}$ (Note 6)		4.2	4.8	5.4	
	$V_{DS} = 10\text{ V}, V_{GS} = 10\text{ V}, T_J = 150^\circ\text{C}$ (Notes 5, 6)		2.7	3.6	4.5	
Temperature Limit (Turn-off)	$V_{GS} = 5.0\text{ V}$ (Notes 5, 6)	$T_{LIM(off)}$	150	175	200	$^\circ\text{C}$
Thermal Hysteresis	$V_{GS} = 5.0\text{ V}$	$\Delta T_{LIM(on)}$		15		
Temperature Limit (Turn-off)	$V_{GS} = 10\text{ V}$ (Notes 5, 6)	$T_{LIM(off)}$	150	165	185	
Thermal Hysteresis	$V_{GS} = 10\text{ V}$	$\Delta T_{LIM(on)}$		15		

**GATE INPUT CHARACTERISTICS** (Note 5)

Device ON Gate Input Current	$V_{GS} = 5\text{ V}, I_D = 1.0\text{ A}$	$I_{GON}$		50		$\mu\text{A}$
	$V_{GS} = 10\text{ V}, I_D = 1.0\text{ A}$			400		

3. Pulse Test: Pulse Width  $\leq 300\text{ }\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .

4. Fault conditions are viewed as beyond the normal operating range of the part.

5. Not subject to production testing.

6. Refer to Application Note AND8202/D for dependence of protection features on gate voltage.

**NCV8402, NCV8402A****ELECTRICAL CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Test Condition	Symbol	Min	Typ	Max	Unit
-----------	----------------	--------	-----	-----	-----	------

**GATE INPUT CHARACTERISTICS** (Note 5)

Current Limit Gate Input Current	$V_{GS} = 5\text{ V}, V_{DS} = 10\text{ V}$	$I_{GCL}$		0.05		mA
	$V_{GS} = 10\text{ V}, V_{DS} = 10\text{ V}$			0.4		
Thermal Limit Fault Gate Input Current	$V_{GS} = 5\text{ V}, V_{DS} = 10\text{ V}$	$I_{GTL}$		0.15		mA
	$V_{GS} = 10\text{ V}, V_{DS} = 10\text{ V}$			0.7		

**ESD ELECTRICAL CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise noted) (Note 5)

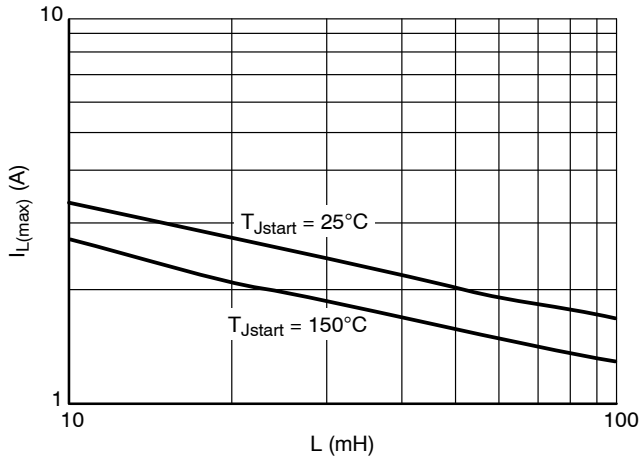
Electro-Static Discharge Capability	Human Body Model (HBM)	ESD	4000			V
	Machine Model (MM)		400			

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

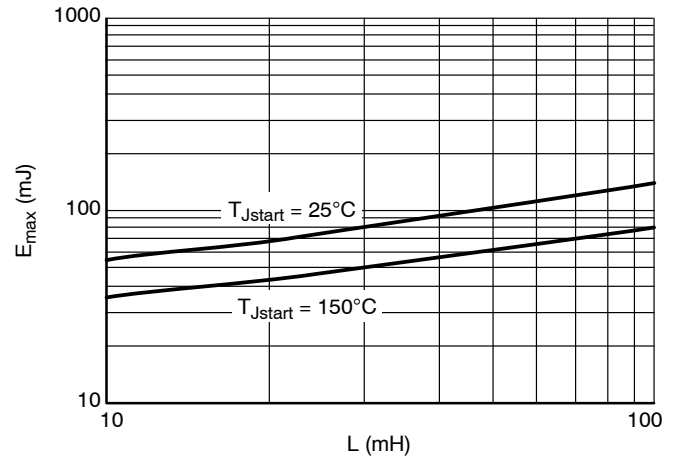
- Pulse Test: Pulse Width  $\leq 300\ \mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
- Fault conditions are viewed as beyond the normal operating range of the part.
- Not subject to production testing.
- Refer to Application Note AND8202/D for dependence of protection features on gate voltage.

# NCV8402, NCV8402A

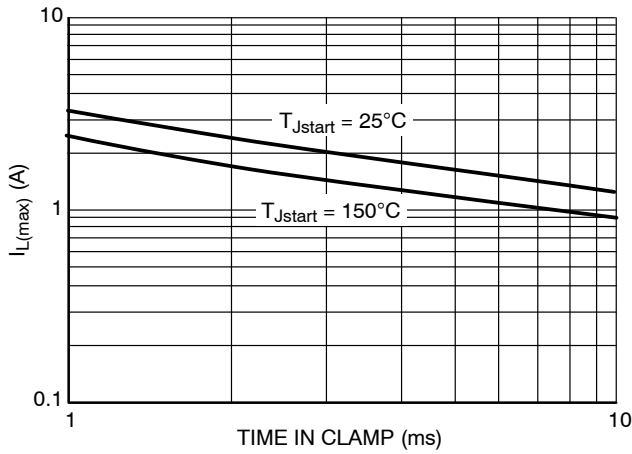
## TYPICAL PERFORMANCE CURVES



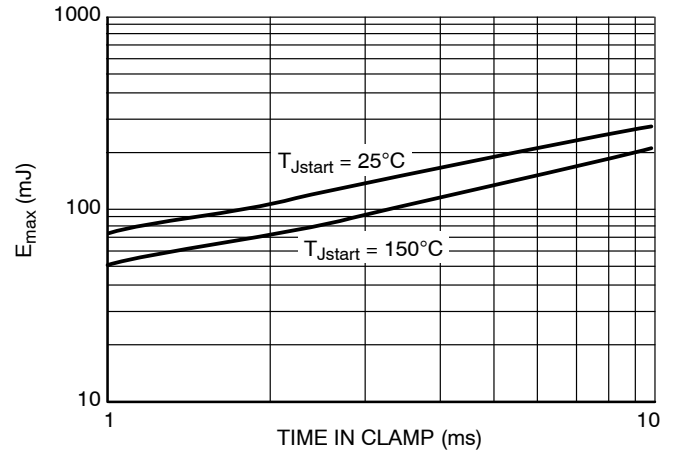
**Figure 2. Single Pulse Maximum Switch-off Current vs. Load Inductance**



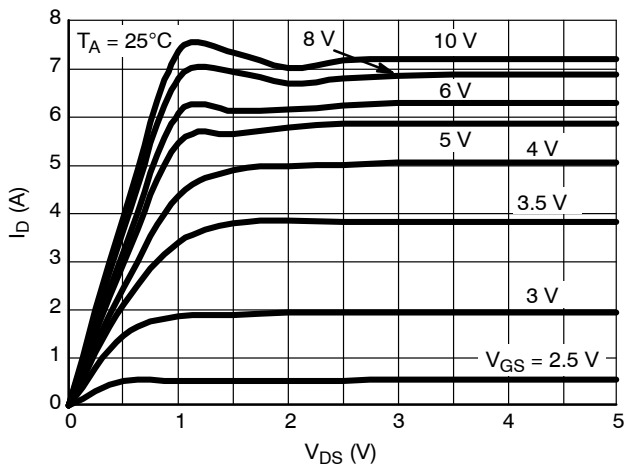
**Figure 3. Single Pulse Maximum Switching Energy vs. Load Inductance**



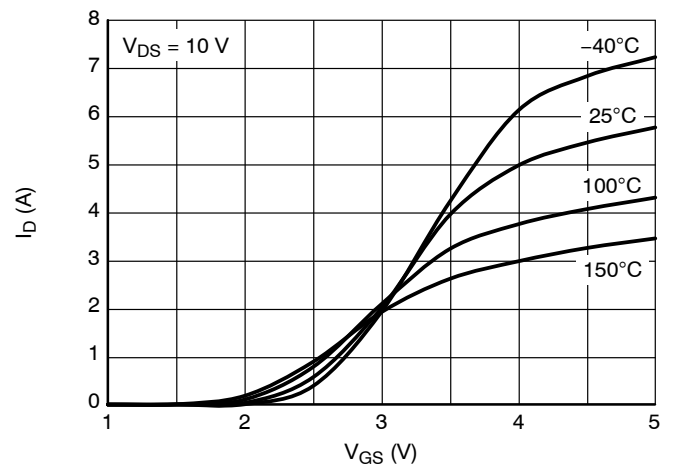
**Figure 4. Single Pulse Maximum Inductive Switch-off Current vs. Time in Clamp**



**Figure 5. Single Pulse Maximum Inductive Switching Energy vs. Time in Clamp**



**Figure 6. On-state Output Characteristics**



**Figure 7. Transfer Characteristics**

# NCV8402, NCV8402A

## TYPICAL PERFORMANCE CURVES

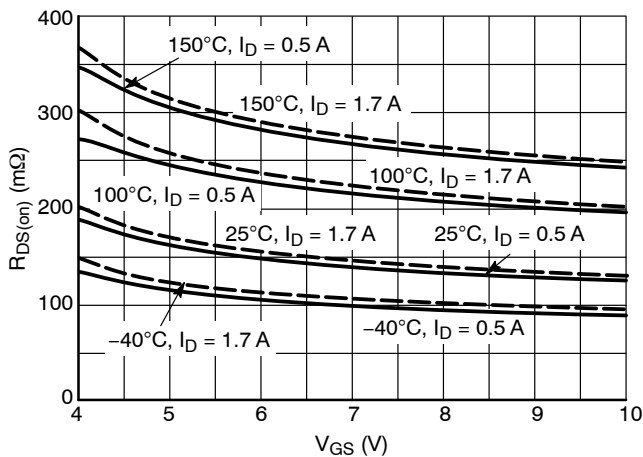


Figure 8.  $R_{DS(on)}$  vs. Gate-Source Voltage

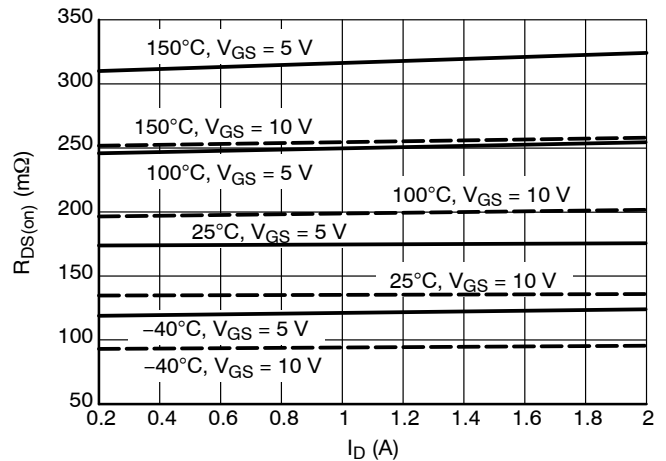


Figure 9.  $R_{DS(on)}$  vs. Drain Current

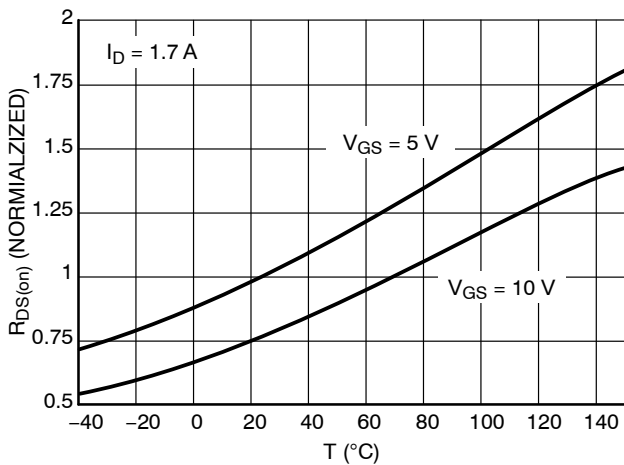


Figure 10. Normalized  $R_{DS(on)}$  vs. Temperature

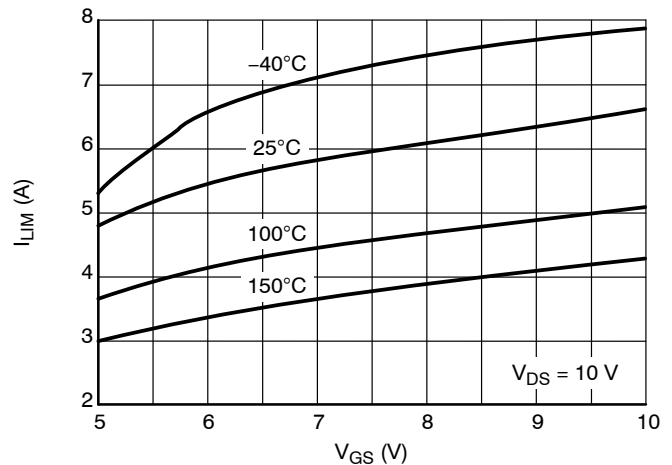


Figure 11. Current Limit vs. Gate-Source Voltage

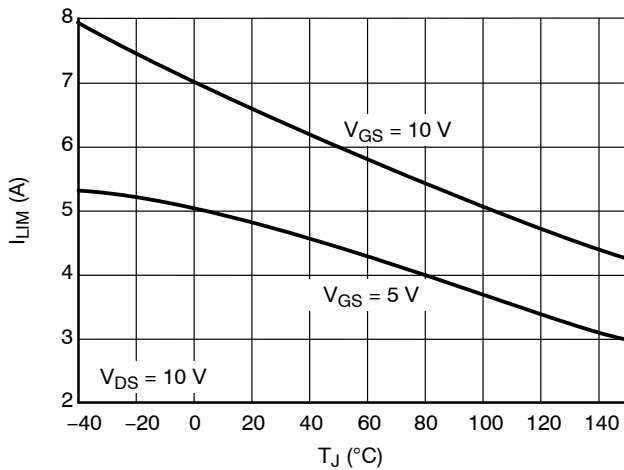


Figure 12. Current Limit vs. Junction Temperature

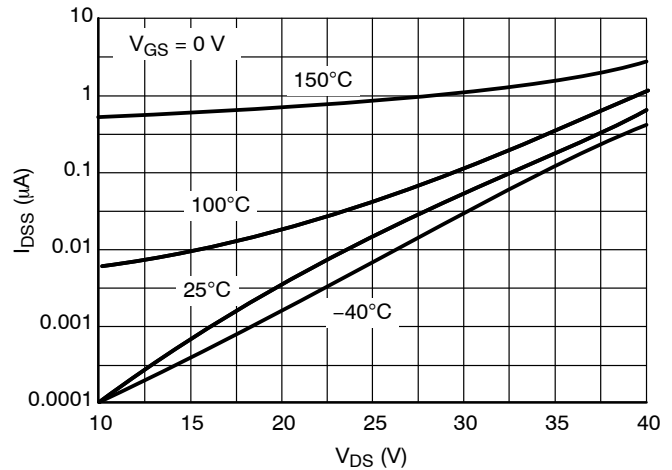


Figure 13. Drain-to-Source Leakage Current



# NCV8402, NCV8402A

## TYPICAL PERFORMANCE CURVES

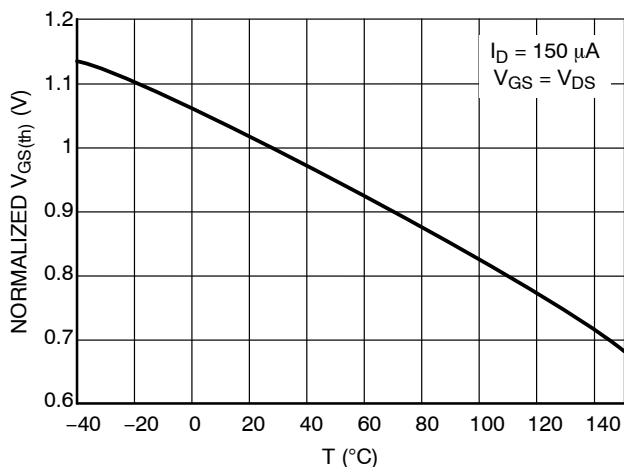


Figure 14. Normalized Threshold Voltage vs. Temperature

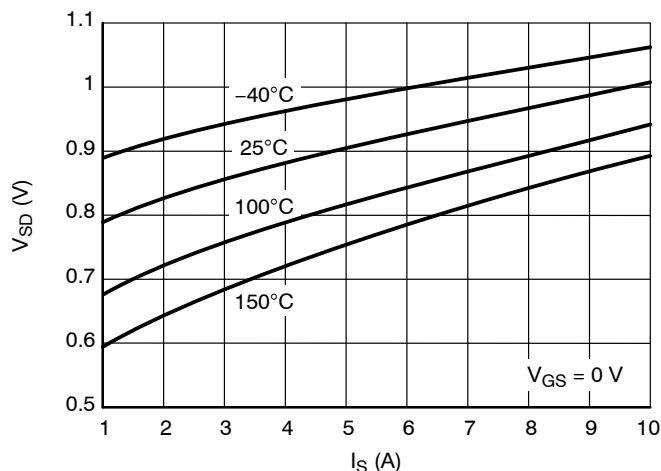


Figure 15. Source-Drain Diode Forward Characteristics

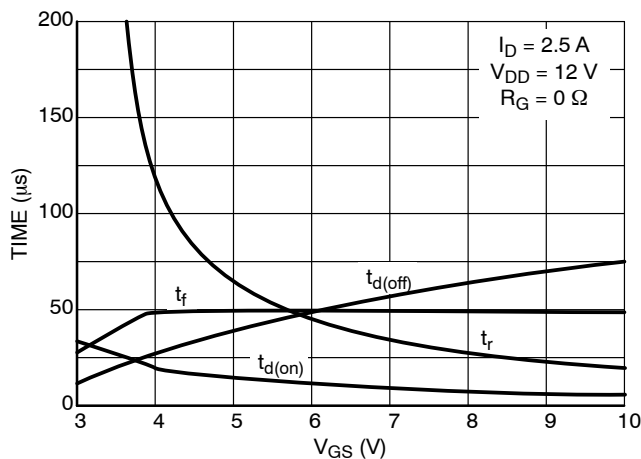


Figure 16. Resistive Load Switching Time vs. Gate-Source Voltage

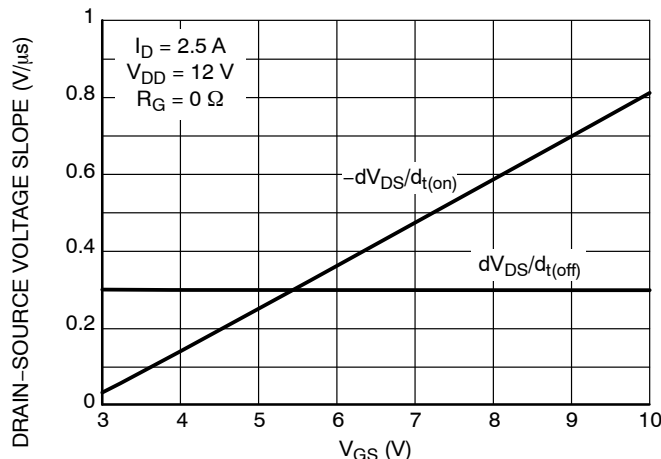


Figure 17. Resistive Load Switching Drain-Source Voltage Slope vs. Gate-Source Voltage

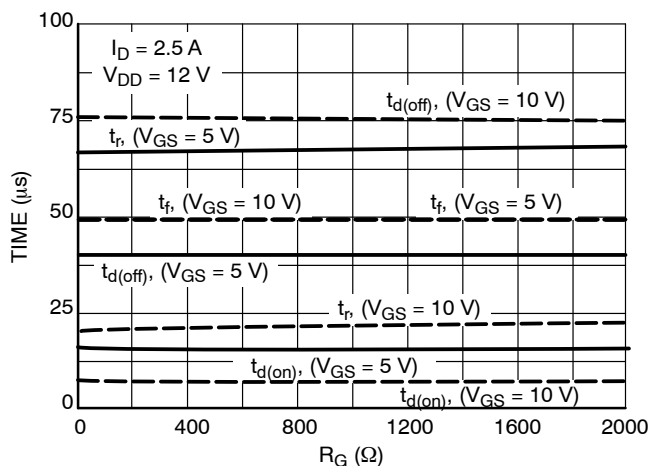


Figure 18. Resistive Load Switching Time vs. Gate Resistance

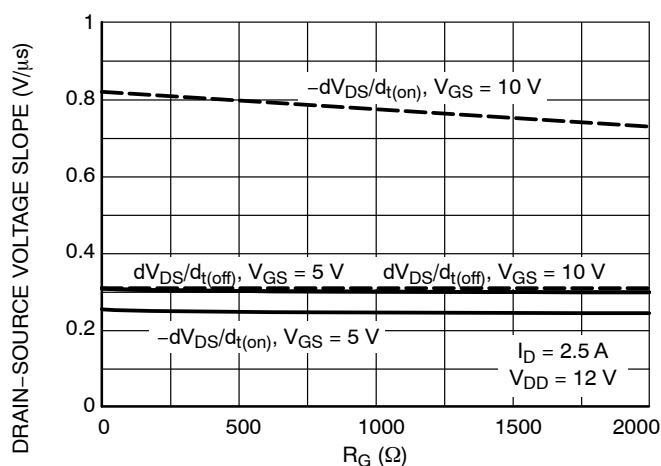


Figure 19. Drain-Source Voltage Slope during Turn On and Turn Off vs. Gate Resistance

## NCV8402, NCV8402A

### TYPICAL PERFORMANCE CURVES

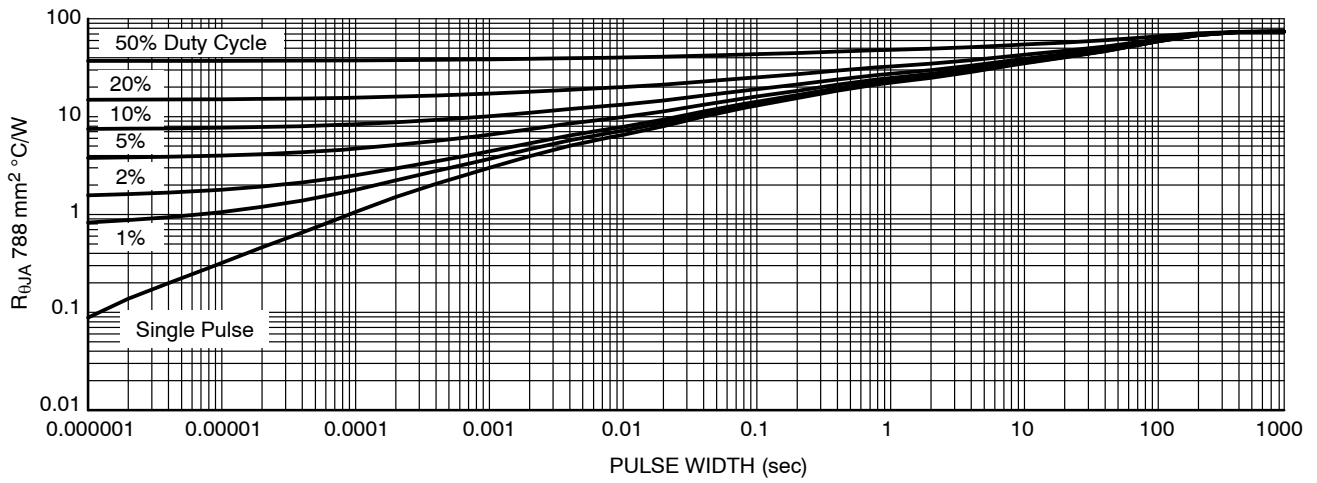


Figure 20. Transient Thermal Resistance – SOT-223 Package

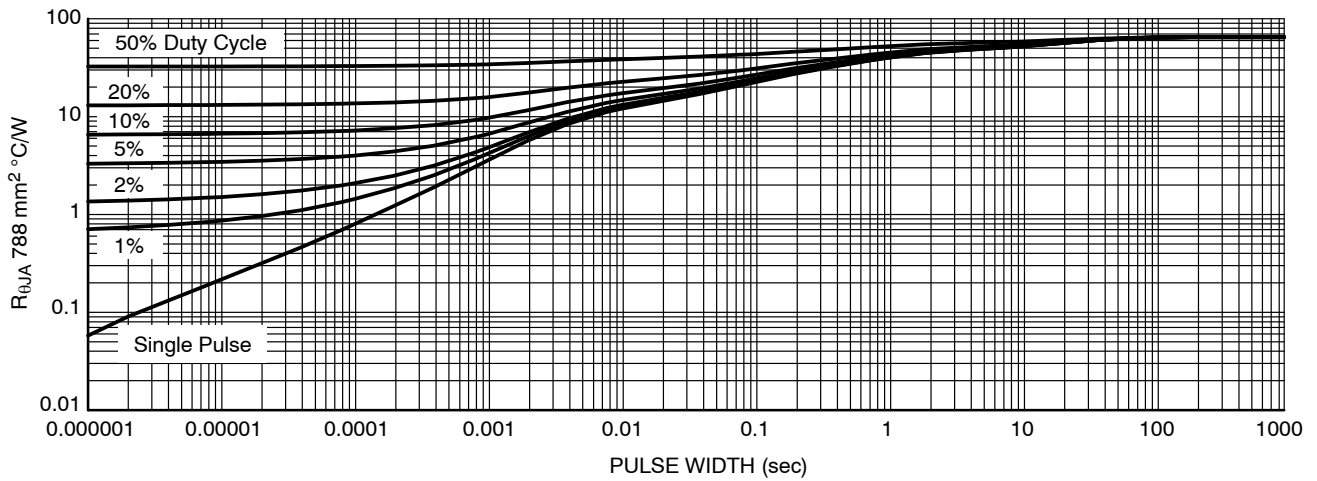


Figure 21. Transient Thermal Resistance – DFN Package

## NCV8402, NCV8402A

### TEST CIRCUITS AND WAVEFORMS

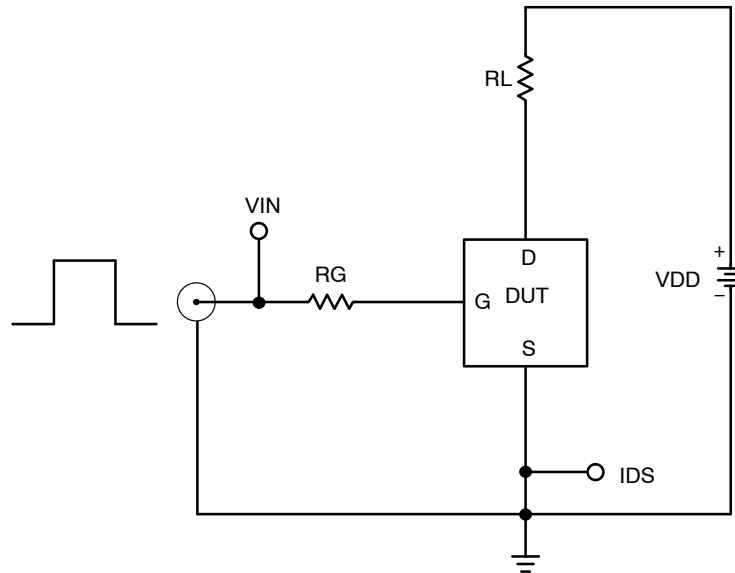


Figure 22. Resistive Load Switching Test Circuit

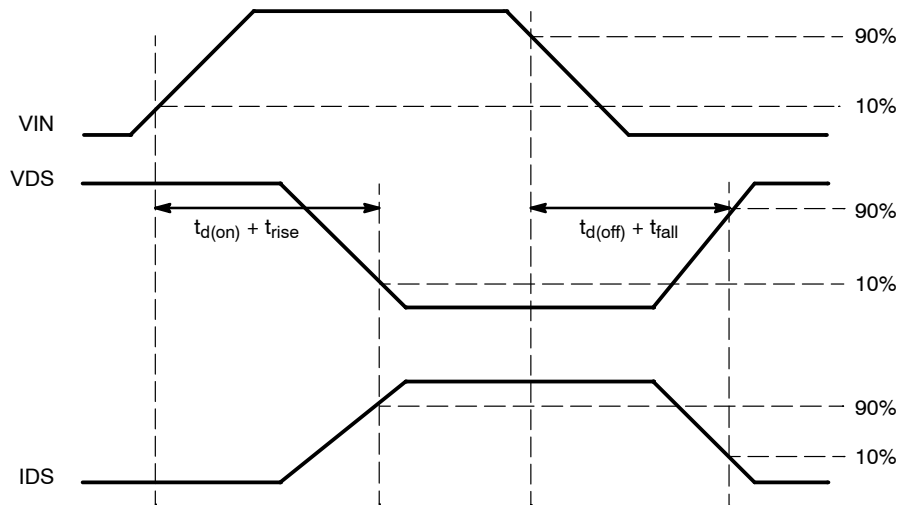
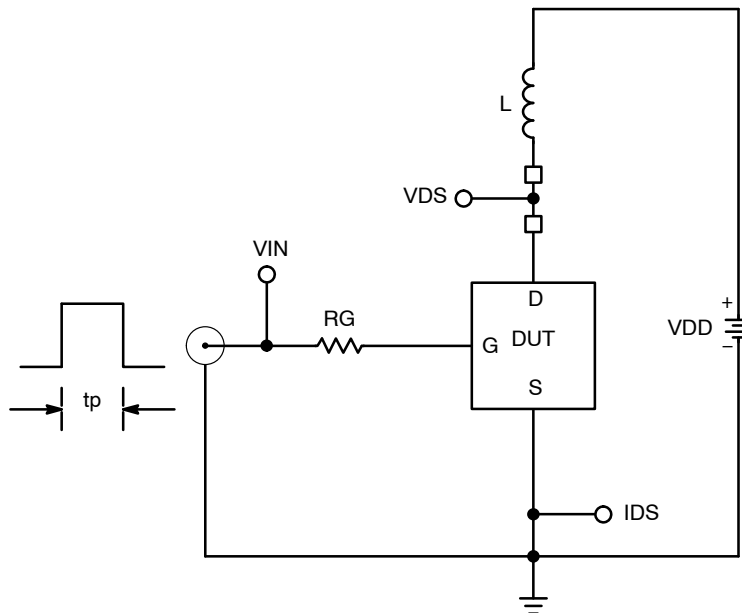


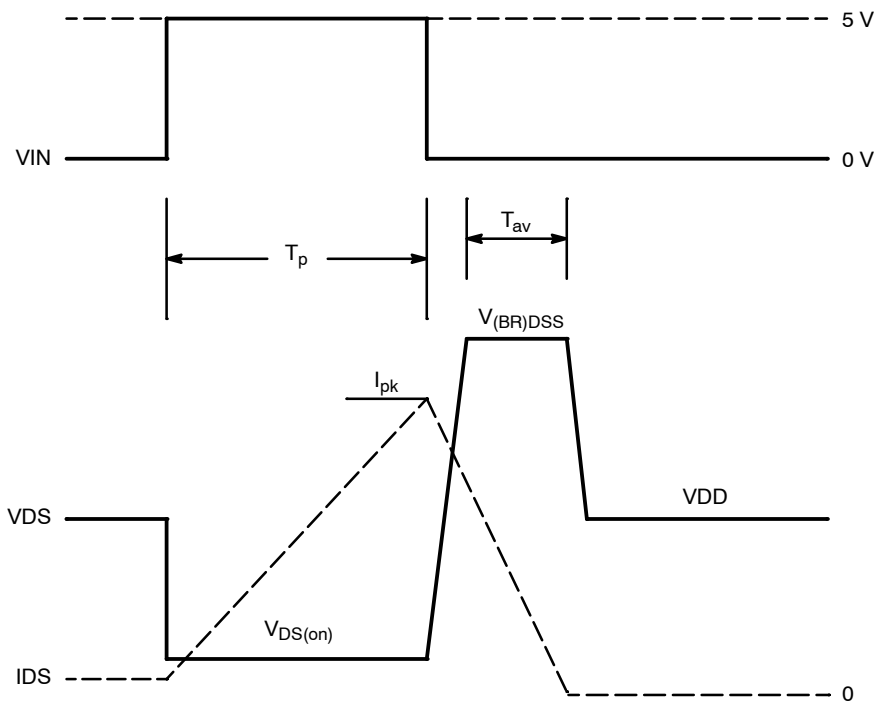
Figure 23. Resistive Load Switching Waveforms

**NCV8402, NCV8402A**

**TEST CIRCUITS AND WAVEFORMS**



**Figure 24. Inductive Load Switching Test Circuit**



**Figure 25. Inductive Load Switching Waveforms**

**NCV8402, NCV8402A**

**ORDERING INFORMATION**

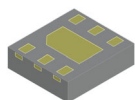
Device*	Package	Shipping†
NCV8402STT1G	SOT-223 (Pb-Free)	1000 / Tape & Reel
NCV8402ASTT1G		
NCV8402STT3G	SOT-223 (Pb-Free)	4000 / Tape & Reel
NCV8402ASTT3G		
NCV8402AMNT2G	DFN6 (Pb-Free)	2000 / Tape & Reel
NCV8402AMNWT1G	DFN6 (Pb-Free, Wettable Flank)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

\*NCV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

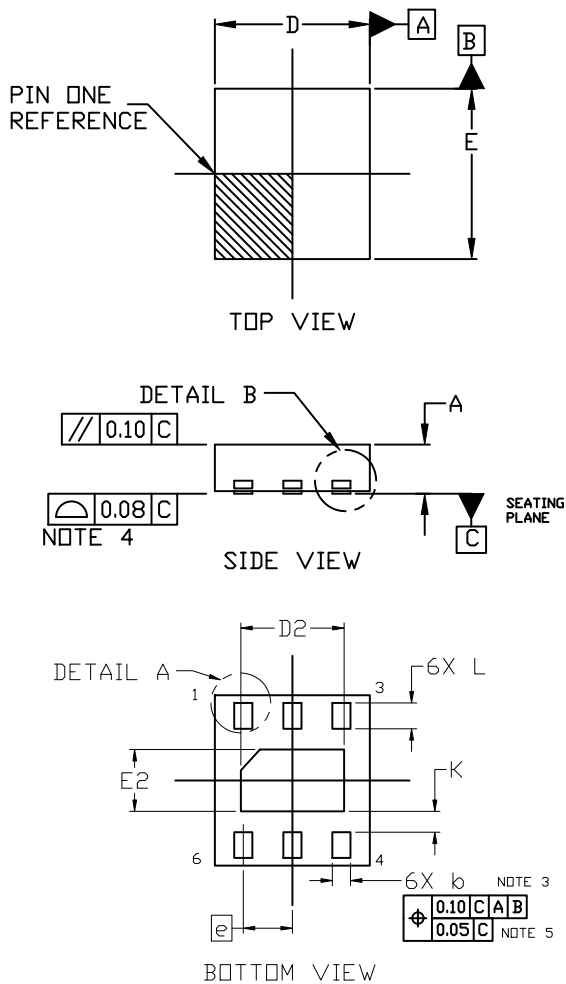
# NCV8402, NCV8402A

## PACKAGE DIMENSIONS

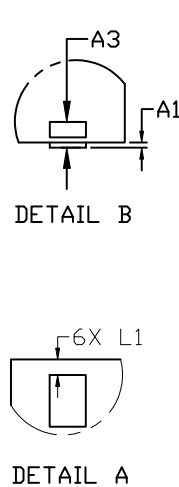


**DFN6 3.0x3.3, 0.95P**  
CASE 506AX  
ISSUE A

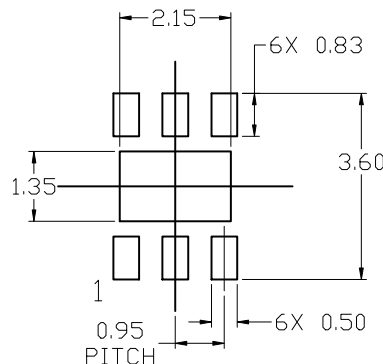
DATE 22 SEP 2020



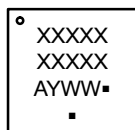
- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
  2. CONTROLLING DIMENSION: MILLIMETERS
  3. DIMENSION *b* APPLIES TO PLATED TERMINALS AND IS MEASURED BETWEEN 0.15 AND 0.30MM FROM THE TERMINAL TIP.
  4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.
  5. POSITIONAL TOLERANCE APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.



DIM	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.80	0.90	1.00
A1	0.00	---	0.05
b	0.30	0.35	0.40
D	2.90	3.00	3.10
D2	1.90	2.00	2.10
E	3.20	3.30	3.40
E2	1.10	1.20	1.30
e	0.95 BSC		
K	0.40 REF		
L	0.40	0.50	0.60
L1	0.00	---	0.15



### GENERIC MARKING DIAGRAM\*



- XXXX = Specific Device Code
- A = Assembly Location
- Y = Year
- WW = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

\* For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.



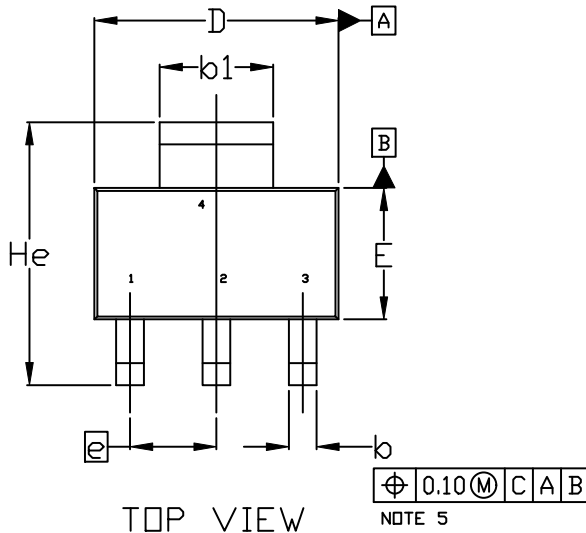
**MECHANICAL CASE OUTLINE**  
**PACKAGE DIMENSIONS**



SCALE 1:1

**SOT-223 (TO-261)**  
**CASE 318E-04**  
**ISSUE R**

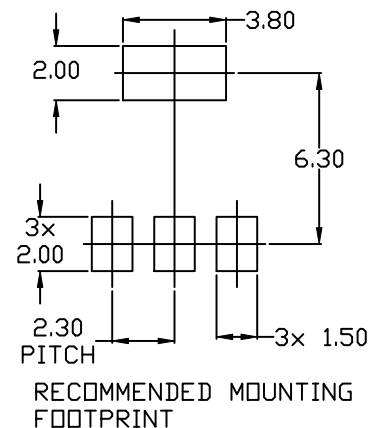
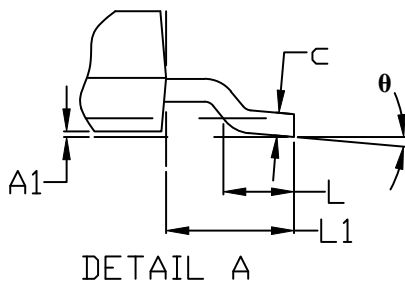
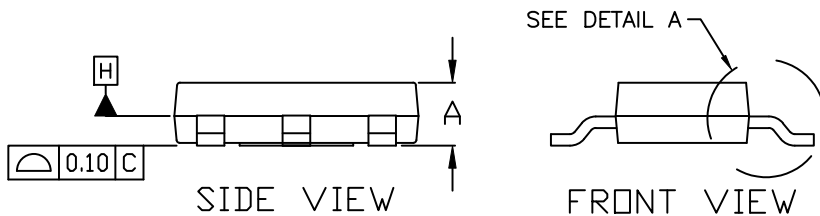
DATE 02 OCT 2018



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
4. DATUMS A AND B ARE DETERMINED AT DATUM H.
5. A1 IS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
6. POSITIONAL TOLERANCE APPLIES TO DIMENSIONS b AND b1.

MILLIMETERS			
DIM	MIN.	NOM.	MAX.
A	1.50	1.63	1.75
A1	0.02	0.06	0.10
b	0.60	0.75	0.89
b1	2.90	3.06	3.20
c	0.24	0.29	0.35
D	6.30	6.50	6.70
E	3.30	3.50	3.70
e	2.30 BSC		
L	0.20	---	---
L1	1.50	1.75	2.00
He	6.70	7.00	7.30
θ	0°	---	10°



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<b>DESCRIPTION:</b> SOT-223 (TO-261)	<b>PAGE 1 OF 2</b>

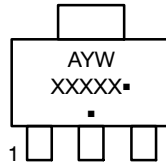
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**SOT-223 (TO-261)**  
**CASE 318E-04**  
**ISSUE R**

DATE 02 OCT 2018

- |  |   |   |   |   |
|--|---|---|---|---|
| <b>STYLE 1:</b><br>PIN 1. BASE<br>2. COLLECTOR<br>3. EMITTER<br>4. COLLECTOR | <b>STYLE 2:</b><br>PIN 1. ANODE<br>2. CATHODE<br>3. NC<br>4. CATHODE        | <b>STYLE 3:</b><br>PIN 1. GATE<br>2. DRAIN<br>3. SOURCE<br>4. DRAIN           | <b>STYLE 4:</b><br>PIN 1. SOURCE<br>2. DRAIN<br>3. GATE<br>4. DRAIN   | <b>STYLE 5:</b><br>PIN 1. DRAIN<br>2. GATE<br>3. SOURCE<br>4. GATE    |
| <b>STYLE 6:</b><br>PIN 1. RETURN<br>2. INPUT<br>3. OUTPUT<br>4. INPUT        | <b>STYLE 7:</b><br>PIN 1. ANODE 1<br>2. CATHODE<br>3. ANODE 2<br>4. CATHODE | <b>STYLE 8:</b><br>CANCELLED  | <b>STYLE 9:</b><br>PIN 1. INPUT<br>2. GROUND<br>3. LOGIC<br>4. GROUND | <b>STYLE 10:</b><br>PIN 1. CATHODE<br>2. ANODE<br>3. GATE<br>4. ANODE |
| <b>STYLE 11:</b><br>PIN 1. MT 1<br>2. MT 2<br>3. GATE<br>4. MT 2             | <b>STYLE 12:</b><br>PIN 1. INPUT<br>2. OUTPUT<br>3. NC<br>4. OUTPUT         | <b>STYLE 13:</b><br>PIN 1. GATE<br>2. COLLECTOR<br>3. EMITTER<br>4. COLLECTOR |   |   |

**GENERIC MARKING DIAGRAM\***



- A = Assembly Location
- Y = Year
- W = Work Week
- XXXXX = Specific Device Code
- = Pb-Free Package

(Note: Microdot may be in either location)  
 \*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

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<b>DESCRIPTION:</b>	<b>SOT-223 (TO-261)</b>	<b>PAGE 2 OF 2</b>

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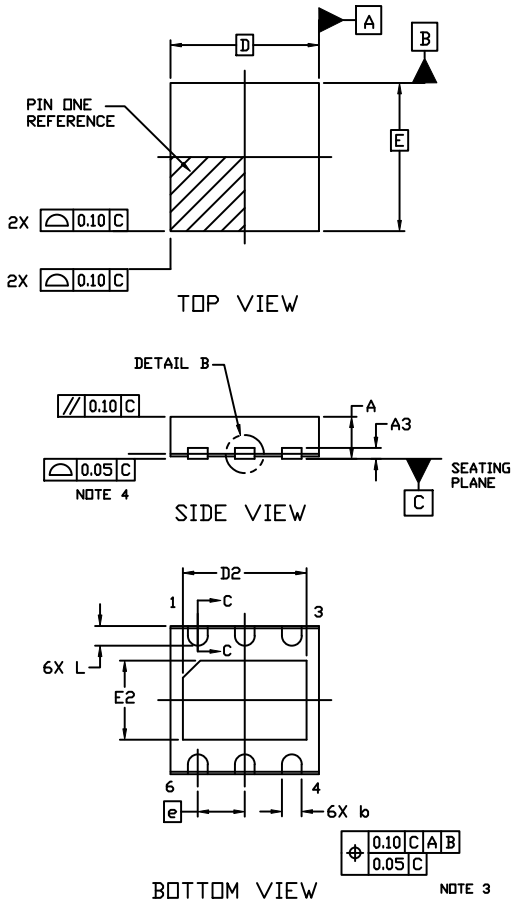
**MECHANICAL CASE OUTLINE  
PACKAGE DIMENSIONS**



SCALE 2:1

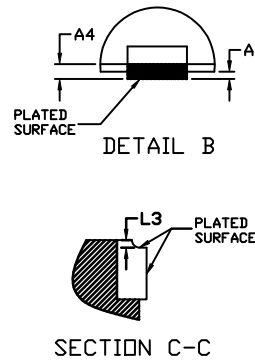
**DFNW6 3x3, 0.95P  
CASE 506DK  
ISSUE A**

DATE 07 MAY 2021

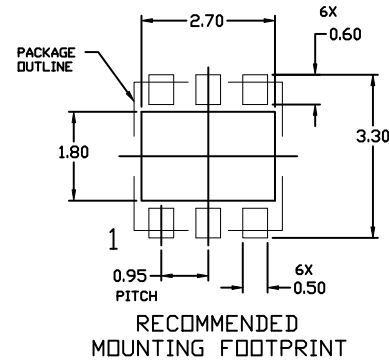


NOTES:

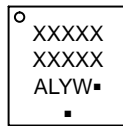
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS
3. DIMENSION b APPLIES TO THE PLATED TERMINALS AND IS MEASURED BETWEEN 0.10 AND 0.20mm FROM THE TERMINAL TIP.
4. PROFILE APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.



DIM	MILLIMETERS		
	MIN.	MAX.	MAX.
A	0.75	0.85	0.95
A1	0.00	---	0.05
A3	0.20 REF		
A4	0.10	---	---
b	0.35	0.40	0.45
D	3.00 BSC		
D2	2.40	2.50	2.60
E	3.00 BSC		
E2	1.50	1.60	1.70
e	0.95 BSC		
L	0.30	0.40	0.50
L3	0.00	0.05	0.10



**GENERIC MARKING DIAGRAM\***



- XXXXX = Specific Device Code
- A = Assembly Location
- L = Wafer Lot
- Y = Year
- W = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present. Some products may not follow the Generic Marking.

\* For additional information on our Pb-Free strategy and soldering details, please download the DN Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERM/D.

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