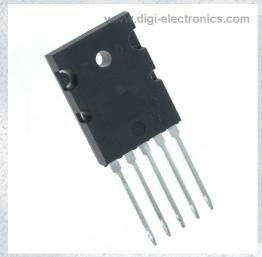


# NJL21193DG Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number NJL21193DG-DG

Manufacturer onsemi

Manufacturer Product Number NJL21193DG

Description TRANS PNP 250V 16A TO264

Detailed Description Bipolar (BJT) Transistor PNP 250 V 16 A 4MHz 200 W

Through Hole TO-264



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
NJL21193DG	onsemi
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
PNP	16 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
250 V	4V @ 3.2A, 16A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
100μΑ	25 @ 8A, 5V
Power - Max:	Frequency - Transition:
200 W	4MHz
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Through Hole
Package / Case:	Supplier Device Package:
TO-264-5	TO-264
Base Product Number:	
NJL2119	

# **Environmental & Export classification**

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
FAR99	8541 29 0095

# Complementary ThermalTrak<sup>TM</sup> Transistors

The ThermalTrak family of devices has been designed to eliminate thermal equilibrium lag time and bias trimming in audio amplifier applications. They can also be used in other applications as transistor die protection devices.

#### **Features**

- Thermally Matched Bias Diode
- Instant Thermal Bias Tracking
- Absolute Thermal Integrity
- Medium Frequency Device with Extended Safe Operating Area
- These are Pb-Free Devices

#### **Benefits**

- Eliminates Thermal Equilibrium Lag Time and Bias Trimming
- Superior Sound Quality Through Improved Dynamic Temperature Response
- Significantly Improved Bias Stability
- Simplified Assembly
  - Reduced Labor Costs
  - Reduced Component Count
- High Reliability

#### **Applications**

- High-End Consumer Audio Products
  - Home Amplifiers
  - Home Receivers
- Professional Audio Amplifiers
  - Theater and Stadium Sound Systems
  - ◆ Public Address Systems (PAs)



ON Semiconductor®

http://onsemi.com

BIPOLAR POWER TRANSISTORS 16 A, 250 V, 200 W

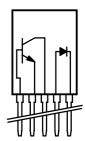


TO-264, 5 LEAD CASE 340AA STYLE 1

## **MARKING DIAGRAM**

### **SCHEMATIC**





xxxx = Specific Device Code
G = Pb-Free Device
A = Assembly Location

YY = Year WW = Work Week

#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

<sup>\*</sup>For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

## **MAXIMUM RATINGS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V <sub>CEO</sub>	250	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	400	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	5	Vdc
Collector–Emitter Voltage – 1.5 V	V <sub>CEX</sub>	400	Vdc
Collector Current – Continuous – Peak (Note 1)	I <sub>C</sub>	16 30	Adc
Base Current – Continuous	I <sub>B</sub>	5.0	Adc
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate Above 25°C	P <sub>D</sub>	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	- 65 to +150	°C
DC Blocking Voltage	V <sub>R</sub>	200	V
Average Rectified Forward Current	I <sub>F(AV)</sub>	1.0	A

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{ hetaJC}$	0.625	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

#### **ATTRIBUTES**

Characteristic		Value	
ESD Protection	Human Body Model Machine Model	>8000 V > 400 V	
Flammability Rating		UL 94 V-0 @ 0.125 in	

## **ORDERING INFORMATION**

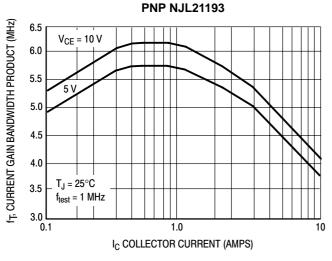
Device	Package	Shipping
NJL21193DG	TO-264 (Pb-Free)	25 Units / Rail
NJL21194DG	TO-264 (Pb-Free)	25 Units / Rail

<sup>1.</sup> Pulse Test: Pulse Width = 5 ms, Duty Cycle < 10%.

## **ELECTRICAL CHARACTERISTICS** (T<sub>C</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector–Emitter Sustaining Voltage (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 0)	V <sub>CEO(sus)</sub>	250	_	Vdc
Collector Cutoff Current (V <sub>CE</sub> = 200 Vdc, I <sub>B</sub> = 0)	I <sub>CEO</sub>	-	100	μAdc
Emitter Cutoff Current (V <sub>CE</sub> = 5 Vdc, I <sub>C</sub> = 0)	I <sub>EBO</sub>	_	100	μAdc
Collector Cutoff Current (V <sub>CE</sub> = 250 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc)	I <sub>CEX</sub>	-	100	μAdc
SECOND BREAKDOWN				
Second Breakdown Collector Current with Base Forward Biased (V <sub>CE</sub> = 50 Vdc, t = 1 s (non–repetitive) (V <sub>CE</sub> = 80 Vdc, t = 1 s (non–repetitive)	I <sub>S/b</sub>	4.0 2.25	- -	Adc
ON CHARACTERISTICS				
DC Current Gain ( $I_C = 8$ Adc, $V_{CE} = 5$ Vdc) ( $I_C = 16$ Adc, $I_B = 5$ Adc)	h <sub>FE</sub>	25 8	75 -	
Base–Emitter On Voltage (I <sub>C</sub> = 8 Adc, V <sub>CE</sub> = 5 Vdc)	V <sub>BE(on)</sub>	-	2.2	Vdc
Collector–Emitter Saturation Voltage ( $I_C = 8$ Adc, $I_B = 0.8$ Adc) ( $I_C = 16$ Adc, $I_B = 3.2$ Adc)	V <sub>CE(sat)</sub>	- -	1.4 4	Vdc
DYNAMIC CHARACTERISTICS				
Total Harmonic Distortion at the Output $V_{RMS} = 28.3 \text{ V}, f = 1 \text{ kHz}, P_{LOAD} = 100 \text{ W}_{RMS}$ $h_{FE}$ unmatched (Matched pair $h_{FE} = 50 \ @ 5 \text{ A/5 V}$ ) $h_{FE}$ matched	T <sub>HD</sub>	-	-	%
Current Gain Bandwidth Product ( $I_C = 1$ Adc, $V_{CE} = 10$ Vdc, $f_{test} = 1$ MHz)	f <sub>T</sub>	4	_	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f <sub>test</sub> = 1 MHz)	C <sub>ob</sub>	-	500	pF
Maximum Instantaneous Forward Voltage (Note 2)	VF	1.1 0.93		V
Maximum Instantaneous Reverse Current (Note 2) (Rated dc Voltage, $T_J = 25^{\circ}C$ ) (Rated dc Voltage, $T_J = 150^{\circ}C$ )	i <sub>R</sub> 10 100		μΑ	
Maximum Reverse Recovery Time $(i_F = 1.0 \text{ A}, \text{ di/dt} = 50 \text{ A/}\mu\text{s})$	t <sub>rr</sub> 100		ns	

<sup>2.</sup> Diode Pulse Test: Pulse Width = 300  $\mu$ s, Duty Cycle  $\leq$  2.0%.



NPN NJL21194  $f_{\mathrm{T}}$ , CURRENT GAIN BANDWIDTH PRODUCT (MHz) 8.0 7.0 10 V 6.0 5.0  $V_{CE} = 5 V$ 4.0 3.0 2.0  $T_J=25^{\circ}C$ 1.0  $f_{test} = 1 \text{ MHz}$ <sub>0</sub> L 1.0 10 I<sub>C</sub> COLLECTOR CURRENT (AMPS)

Figure 1. Typical Current Gain Bandwidth Product

**Figure 2. Typical Current Gain Bandwidth Product** 

#### **TYPICAL CHARACTERISTICS**

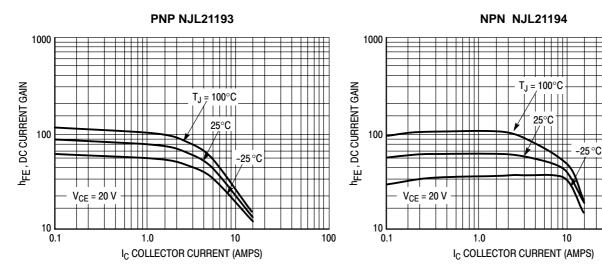


Figure 3. DC Current Gain, V<sub>CE</sub> = 20 V

Figure 4. DC Current Gain, V<sub>CE</sub> = 20 V

100

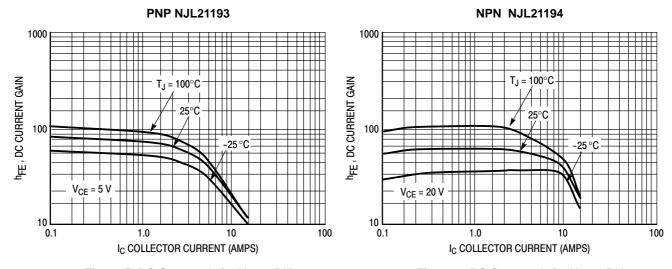
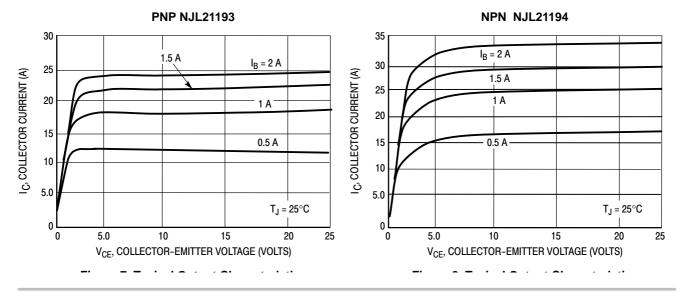


Figure 5. DC Current Gain,  $V_{CE} = 5 \text{ V}$ 

Figure 6. DC Current Gain,  $V_{CE} = 5 \text{ V}$ 



## **TYPICAL CHARACTERISTICS**

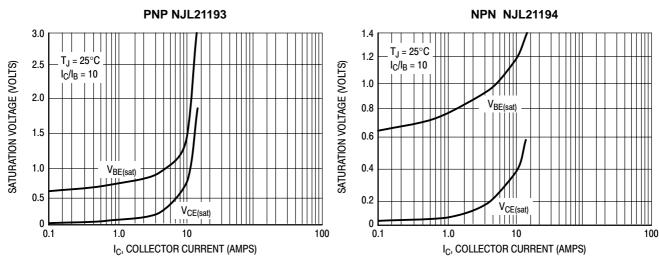


Figure 9. Typical Saturation Voltages

Figure 10. Typical Saturation Voltages

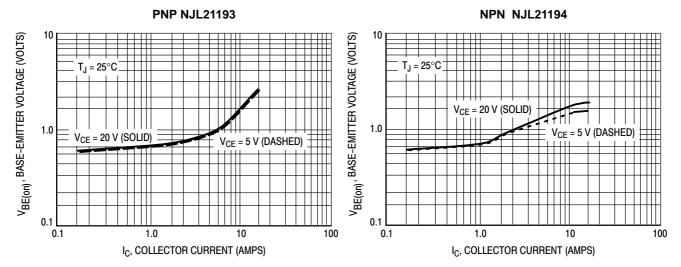


Figure 11. Typical Base-Emitter Voltage

Figure 12. Typical Base–Emitter Voltage

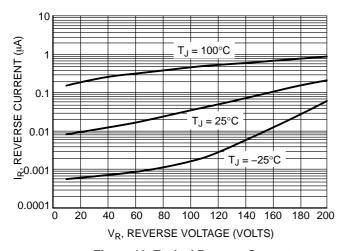


Figure 13. Typical Reverse Current

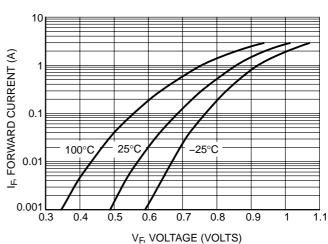


Figure 14. Typical Forward Voltage

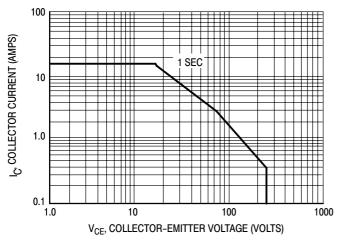


Figure 15. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate  $I_C - V_{CE}$  limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

The data of Figure 15 is based on  $T_{J(pk)} = 150^{\circ} C$ ;  $T_{C}$  is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.

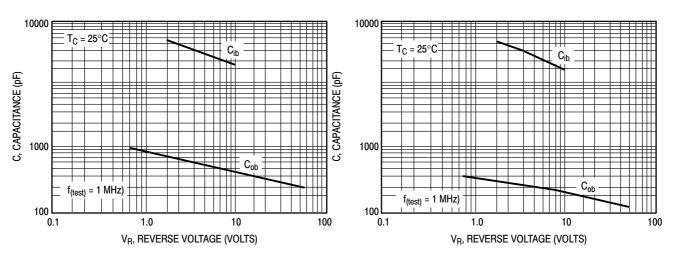


Figure 16. NJL21193 Typical Capacitance

Figure 17. NJL21194 Typical Capacitance

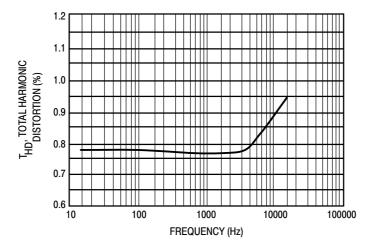


Figure 18. Typical Total Harmonic Distortion

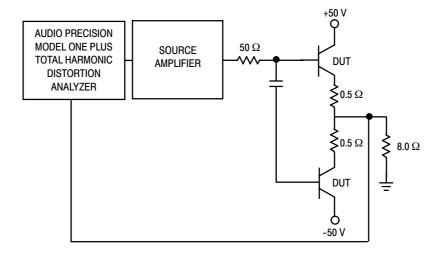


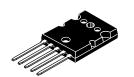
Figure 19. Total Harmonic Distortion Test Circuit

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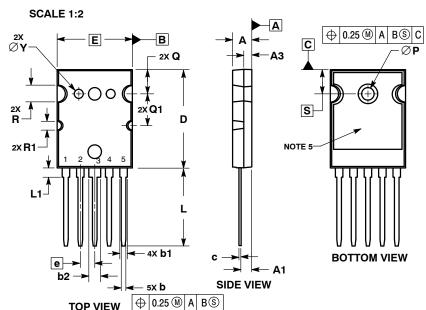
# **MECHANICAL CASE OUTLINE**

# PACKAGE DIMENSIONS



TO-264, 5-LEAD CASE 340AA **ISSUE A** 

**DATE 04 FEB 2013** 



#### NOTES

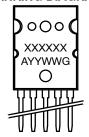
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION b APPLIES BETWEEN 2.50 AND 3.81 FROM THE LEAD TIP.
- FROM THE LEAD TIP.
  4. DIMENSION S APPLIES TO THE MOUNTING HOLE
  (ØP). DIMENSION Q APPLIES TO THE NOTCHES
  (2X R).
  5. THERMAL PAD SIZE AND SHAPE MAY VARY WITHIN THE AREA DEFINED BY DIMENSIONS D AND E.

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.31	
A1	2.50	3.10	
А3	2.00	REF	
b	1.10	1.50	
b1	2.00		
b2	3.00	REF	
С	0.43	0.74	
D	25.58	26.59	
E	19.30	20.29	
е	3.81	BSC	
L	19.79 21.3		
L1	2.10	2.30	
Р	3.00 3.51		
Q	5.80	6.20	
Q1	8.80	9.20	
R	4.00 REF		
R1	2.00 REF		
S	9.00 BSC		
Υ	1.80 REF		

- STYLE 1: PIN 1. BASE 2. EMITTER

  - 3. COLLECTOR 4. ANODE
  - 5. CATHODE

#### **GENERIC MARKING DIAGRAM\***



XXXXXX = Specific Device Code

= Assembly Location Α

YY = Year

WW = Work Week

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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DESCRIPTION:	TO-264, 5-LEAD		PAGE 1 OF 1

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