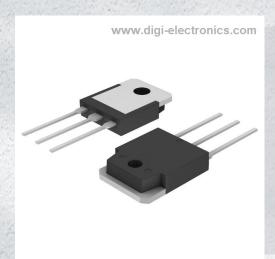


NJW3281G Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number NJW3281G-DG

Manufacturer onsemi

Manufacturer Product Number NJW3281G

Description TRANS NPN 250V 15A TO3P-3L

Detailed Description Bipolar (BJT) Transistor NPN 250 V 15 A 30MHz 200

W Through Hole TO-3P-3L



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
NJW3281G	onsemi
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN	15 A
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
250 V	600mV @ 800mA, 8A
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ Ic, Vce:
50μA (ICBO)	75 @ 3A, 5V
Power - Max:	Frequency - Transition:
200 W	30MHz
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Through Hole
Package / Case:	Supplier Device Package:
TO-3P-3, SC-65-3	TO-3P-3L
Base Product Number:	
NII\N/2 701	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):		
ROHS3 Compliant	Not Applicable		
REACH Status:	ECCN:		
REACH Unaffected	EAR99		
HTSUS:			
8541.29.0075			

Complementary NPN-PNP Silicon Power Bipolar Transistors

The NJW3281G and NJW1302G are power transistors for high power audio, disk head positioners and other linear applications.

Features

- Exceptional Safe Operating Area
- NPN/PNP Gain Matching within 10% from 50 mA to 5 A
- Excellent Gain Linearity
- High BVCEO
- High Frequency
- These Devices are Pb-Free and are RoHS Compliant

- Reliable Performance at Higher Powers
- Symmetrical Characteristics in Complementary Configurations
- Accurate Reproduction of Input Signal
- Greater Dynamic Range
- High Amplifier Bandwidth

Applications

- High-End Consumer Audio Products
 - Home Amplifiers
 - Home Receivers
- Professional Audio Amplifiers
 - Theater and Stadium Sound Systems
 - Public Address Systems (PAs)

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CEO}	250	Vdc
Collector-Base Voltage	V _{CBO}	250	Vdc
Emitter-Base Voltage	V _{EBO}	5.0	Vdc
Collector-Emitter Voltage - 1.5 V	V _{CEX}	250	Vdc
Collector Current - Continuous	I _C	15	Adc
Collector Current - Peak (Note 1)	I _{CM}	30	Adc
Base Current - Continuous	I _B	1.6	Adc
Total Power Dissipation @ T _C = 25°C Derate Above 25°C	P _D	200 1.43	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	- 65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.625	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	°C/W

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

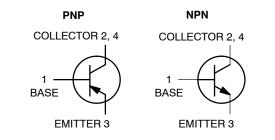
1. Pulse Test: Pulse Width = 5 ms, Duty Cycle < 10%.

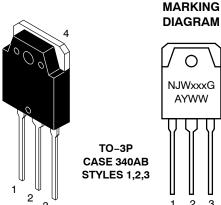


ON Semiconductor®

http://onsemi.com

15 AMPERES **COMPLEMENTARY** SILICON POWER TRANSISTORS **250 VOLTS 200 WATTS**





AYWW

XXXX = 0281 or 0302 G = Pb-Free Package = Assembly Location Α = Year = Work Week ww

ORDERING INFORMATION

Device	Package	Shipping
NJW3281G	TO-3P (Pb-Free)	30 Units/Rail
NJW1302G	TO-3P (Pb-Free)	30 Units/Rail

1

ELECTRICAL CHARACTERISTICS ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector–Emitter Sustaining Voltage $(I_C = 100 \text{ mAdc}, I_B = 0)$	V _{CEO(sus)}	250	_	_	Vdc
Collector Cutoff Current (V _{CB} = 250 Vdc, I _E = 0)	I _{CBO}	-	_	50	μAdc
Emitter Cutoff Current (V _{EB} = 5 Vdc, I _C = 0)	I _{EBO}	_	_	5	μAdc
SECOND BREAKDOWN					
Second Breakdown Collector with Base Forward Biased (V _{CE} = 50 Vdc, t = 1 s (non-repetitive)	I _{S/b}	4	_	-	Adc
ON CHARACTERISTICS					
DC Current Gain	h _{FE}	75 75 75 60 45	- - - -	150 150 150 - -	-
Collector–Emitter Saturation Voltage (I _C = 8 Adc, I _B = 0.8 Adc)	V _{CE(sat)}	-	0.4	0.6	Vdc
Base-Emitter On Voltage (I _C = 8 Adc, V _{CE} = 5 Vdc)	V _{BE(on)}	_	_	1.5	Vdc
DYNAMIC CHARACTERISTICS	·				
Current-Gain - Bandwidth Product ($I_C = 1 \text{ Adc}, V_{CE} = 5 \text{ Vdc}, f_{test} = 1 \text{ MHz}$)	f _T	_	30	-	MHz
Output Capacitance ($V_{CB} = 10 \text{ Vdc}, I_E = 0, f_{test} = 1 \text{ MHz}$)	C _{ob}	_	_	600	pF

TYPICAL CHARACTERISTICS

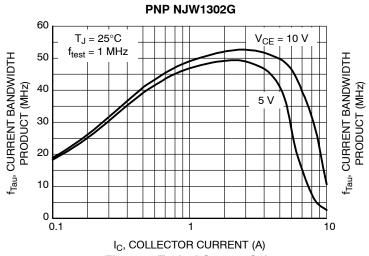


Figure 1. Typical Current Gain **Bandwidth Product**

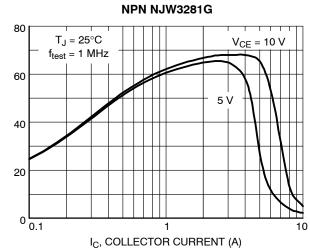


Figure 2. Typical Current Gain **Bandwidth Product**

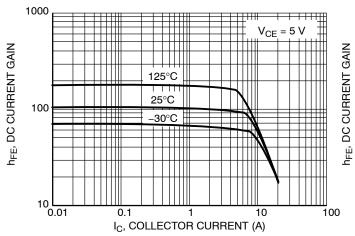


Figure 3. DC Current Gain

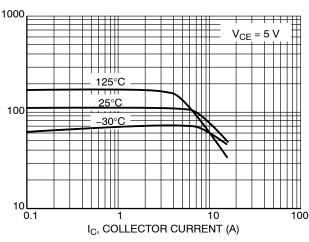


Figure 4. DC Current Gain

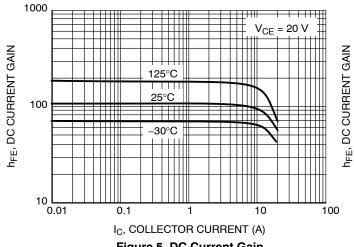


Figure 5. DC Current Gain

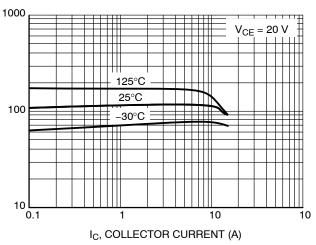


Figure 6. DC Current Gain

TYPICAL CHARACTERISTICS

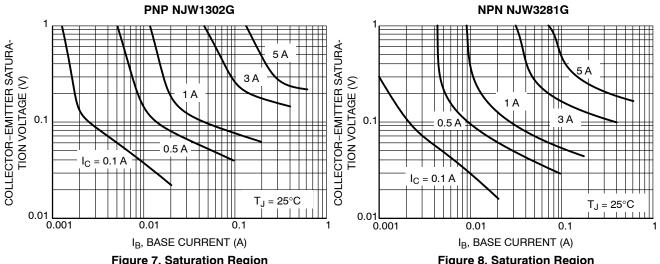


Figure 7. Saturation Region

Figure 8. Saturation Region

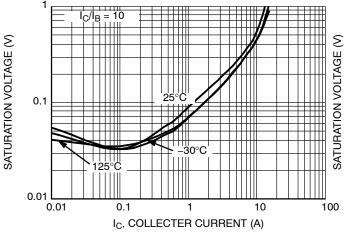


Figure 9. V_{CE(sat)}, Collector–Emitter Saturation Voltage

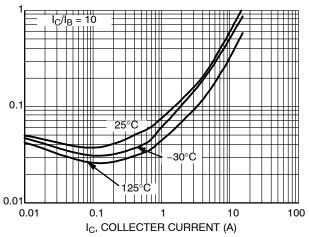


Figure 10. V_{CE(sat)}, Collector-Emitter Saturation Voltage

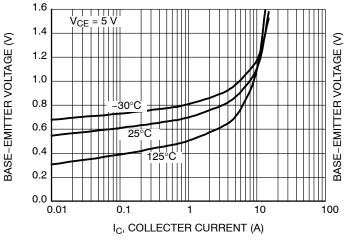


Figure 11. V_{BE(on)}, Base-Emitter Voltage

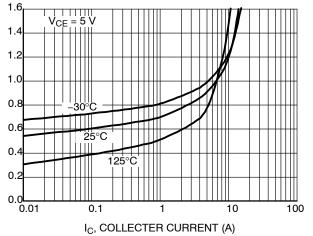
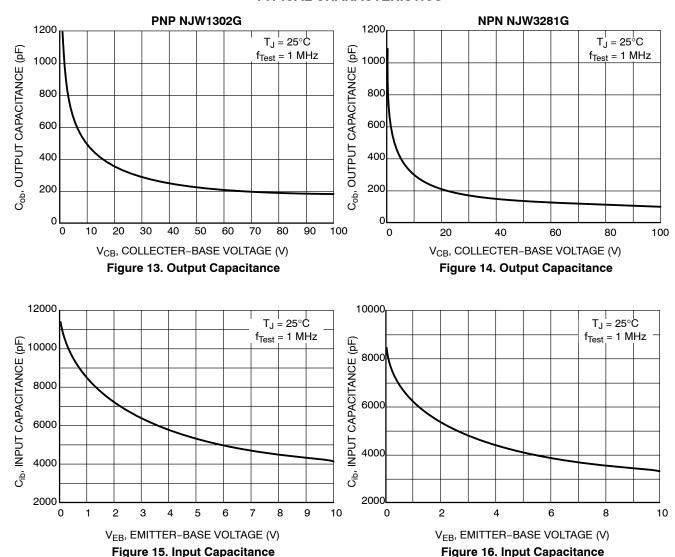


Figure 12. V_{BE(on)}, Base-Emitter Voltage

TYPICAL CHARACTERISTICS



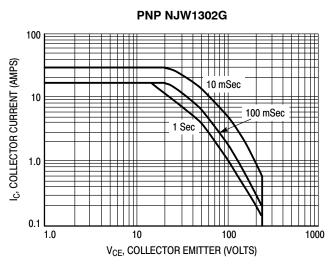


Figure 17. Active Region Safe Operating Area

There are two limitations on the power handling ability of a transistor; average junction temperature and secondary breakdown. Safe operating area curves indicate I_C – V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate.

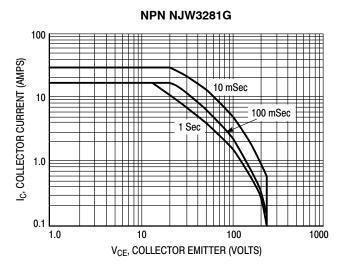


Figure 18. Active Region Safe Operating Area

The data of Figures 17 and 18 is based on $T_{J(pk)} = 150^{\circ} C$; T_C is variable depending on conditions. At high case temperatures, thermal limitations will reduce the power than can be handled to values less than the limitations imposed by second breakdown.



STYLE 1:

PIN 1. BASE

2. COLLECTOR

EMITTER

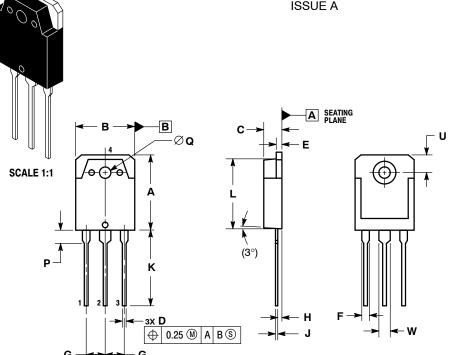
COLLECTOR

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS



DATE 30 OCT 2007



STYLE 3:

PIN 1. GATE

2. DRAIN

SOURCE

DRAIN

STYLE 2:

PIN 1.

2.

ANODE CATHODE

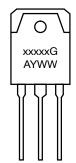
CATHODE

ANODE

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS
 DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30mm FROM THE TERMINAL TIP.
- DIMENSION A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MILLIMETERS			
DIM	MIN	NOM	MAX	
Α	19.70	19.90	20.10	
В	15.40	15.60	15.80	
С	4.60	4.80	5.00	
D	0.80	1.00	1.20	
E	1.45	1.50	1.65	
F	1.80	2.00	2.20	
G		5.45 BSC		
Н	1.20	1.40	1.60	
J	0.55	0.60	0.75	
K	19.80	20.00	20.20	
L	18.50	18.70	18.90	
P	3.30	3.50	3.70	
Q	3.10	3.20	3.50	
U	5.00 REF			
W	2.80	3.00	3.20	

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Pb-Free Package G = Assembly Location Α Υ = Year

WW = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

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