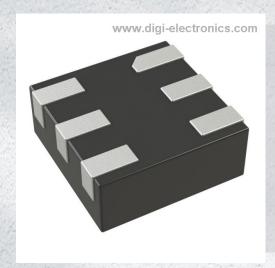


NLU1GU04CMUTCG Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number NLU1GU04CMUTCG-DG

Manufacturer onsemi

Manufacturer Product Number NLU1GU04CMUTCG

Description IC INVERTER 1CH 1-INP 6UDFN

Detailed Description Inverter IC 1 Channel 6-UDFN (1x1)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
NLU1GU04CMUTCG	onsemi
Series:	Product Status:
MiniGate™	Obsolete
Logic Type:	Number of Circuits:
Inverter	1
Number of Inputs:	Features:
1	
Voltage - Supply:	Current - Quiescent (Max):
1.65V ~ 5.5V	-1 μΑ
Current - Output High, Low:	Input Logic Level - Low:
8mA, 8mA	
Input Logic Level - High:	Max Propagation Delay @ V, Max CL:
	7ns @ 5V, 50pF
Operating Temperature:	Mounting Type:
-55°C ~ 125°C	Surface Mount
Supplier Device Package:	Package / Case:
6-UDFN (1x1)	6-UFDFN
Base Product Number:	
NLU1GU04	

Environmental & Export classification

8542.39.0001

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

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Single Unbuffered Inverter

The NLU1GU04 MiniGate $^{\text{m}}$ is an advanced high-speed CMOS unbuffered inverter in ultra-small footprint.

This device is well suited for use in oscillator, pulse-shaping and high input impedance amplifier applications. For digital applications, the NLU1GU04 is recommended.

The NLU1GU04 input and output structures provide protection when voltages up to 7.0 V are applied, regardless of the supply voltage.

Features

- High Speed: $t_{PD} = 2.5 \text{ ns (Typ)} @ V_{CC} = 5.0 \text{ V}$
- Low Power Dissipation: $I_{CC} = 1 \mu A$ (Max) at $T_A = 25^{\circ}C$
- Power Down Protection Provided on inputs
- Balanced Propagation Delays
- Overvoltage Tolerant (OVT) Input and Output Pins
- Ultra-Small Packages
- These are Pb-Free Devices

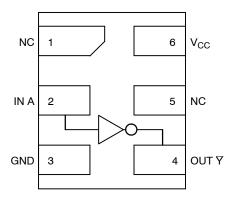


Figure 1. Pinout (Top View)

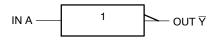


Figure 2. Logic Symbol

PIN ASSIGNMENT

1	NC
2	IN A
3	GND
4	OUT ₹
5	NC
6	V _{CC}

FUNCTION TABLE				
Α	Y			
L	Н			
Н	L			



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MARKING DIAGRAMS



UDFN6 1.2 x 1.0 CASE 517AA





UDFN6 1.0 x 1.0 CASE 517BX





UDFN6 1.45 x 1.0 CASE 517AQ



K = Device MarkingM = Date Code

ORDERING INFORMATION

See detailed ordering and shipping information on page 4 of this data sheet.

MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V _{CC}	DC Supply Voltage	−0.5 to +7.0	V	
V _{IN}	DC Input Voltage	-0.5 to +7.0	V	
V _{OUT}	DC Output Voltage		-0.5 to +7.0	V
I _{IK}	DC Input Diode Current	V _{IN} < GND	-20	mA
I _{OK}	DC Output Diode Current V	±20	mA	
Io	DC Output Source/Sink Current	±12.5	mA	
I _{CC}	DC Supply Current Per Supply Pin	±25	mA	
I _{GND}	DC Ground Current per Ground Pin	±25	mA	
T _{STG}	Storage Temperature Range	-65 to +150	°C	
TL	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C	
TJ	Junction Temperature Under Bias		150	°C
MSL	Moisture Sensitivity		Level 1	
F _R	Flammability Rating Oxygen Inde	ex: 28 to 34	UL 94 V-0 @ 0.125 in	
V _{ESD}	ESD Withstand Voltage Human Body Mod Machine Mod Charged Device Mod	del (Note 3)	> 2000 > 200 N/A	V
I _{LATCHUP}	Latchup Performance Above V_{CC} and Below GND at 125 $^{\circ}{\rm C}$	(Note 5)	±500	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow.

- 2. Tested to EIA / JESD22-A114-A.
- 3. Tested to EIA / JESD22-A115-A.
- 4. Tested to JESD22-C101-A.
- 5. Tested to EIA / JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V _{CC}	Positive DC Supply Voltage	1.65	5.5	٧
V _{IN}	Digital Input Voltage	0	5.5	٧
V _{OUT}	Output Voltage	0	5.5	٧
T _A	Operating Free-Air Temperature	-55	+125	°C
Δt/ΔV	Input Transition Rise or Fall Rate $ V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V} $ $ V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V} $	0 0	100 20	ns/V

DC ELECTRICAL CHARACTERISTICS

				T,	T _A = 25 °C		T _A = 25 °C		-85°C	T _A = -55°C to +125°C		
Symbol	Parameter	Conditions	V _{CC} (V)	Min	Тур	Max	Min	Max	Min	Max	Unit	
V _{IH}	Low-Level Input Voltage		1.65 2.3 to 5.5	0.85 x V _{CC} 0.80 x V _{CC}			0.85 x V _{CC} 0.80 x V _{CC}				V	
V _{IL}	Low-Level Input Voltage		1.65 2.3 to 5.5			0.15 x V _{CC} 0.20 x V _{CC}		0.15 x V _{CC} 0.20 x V _{CC}		0.15 x V _{CC} 0.20 x V _{CC}	V	
V _{OH}	High-Level Output Voltage	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OH} = -50 \mu A$	2.0 3.0 4.5	1.9 2.9 4.4	2.0 3.0 4.5		1.9 2.9 4.4		1.9 2.9 4.4		V	
		$V_{IN} = V_{IH}$ or V_{IL} $I_{OH} = -4$ mA $I_{OH} = -8$ mA	3.0 4.5	2.58 3.94			2.48 3.80		2.34 3.66			
V _{OL}	Low-Level Output Voltage	$V_{IN} = V_{IH} \text{ or } V_{IL}$ $I_{OL} = 50 \mu A$	2.0 3.0 4.5		0 0 0	0.1 0.1 0.1		0.1 0.1 0.1		0.1 0.1 0.1	٧	
		$V_{IN} = V_{IH}$ or V_{IL} $I_{OL} = 4$ mA $I_{OL} = 8$ mA	3.0 4.5			0.36 0.36		0.44 0.44		0.52 0.52		
I _{IN}	Input Leakage Current	$0 \le V_{IN} \le 5.5 V$	0 to 5.5			±0.1		±1.0		±1.0	μΑ	
I _{CC}	Quiescent Supply Current	V _{IN} = 5.5 V or GND	5.5			1.0		20		40	μΑ	

AC ELECTRICAL CHARACTERISTICS (Input $t_r = t_f = 3.0 \text{ ns}$)

		V _{CC} Test		Т	_A = 25 °(С	T _A = -	+85°C	T _A = - to +1	-55°C 25°C		
Symbol	Parameter	(V)			Тур	Max	Min	Max	Min	Max	Unit	
t _{PLH} ,	Propagation Delay, Input A to	3.0 to	C _L = 15 pF		3.5	8.9		10.5		12	ns	
t _{PHL}	Output ₹	Output Y 3.6	3.6	C _L = 50 pF		4.8	11.4		13		15.5	
		4.5 to	C _L = 15 pF		2.5	5.5		6.5		8.0		
		5.5	C _L = 50 pF		3.8	7.0		8.0		9.5		
C _{IN}	Input Capacitance				4	10		10		10	pF	
C _{PD}	Power Dissipation Capacitance (Note 6)	5.0			22						pF	

^{6.} C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the dynamic operating current consumption without load. Average operating current can be obtained by the equation I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no–load dynamic power consumption: P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

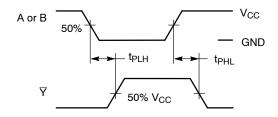
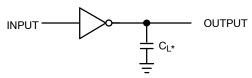


Figure 3. Switching Waveforms



*Includes all probe and jig capacitance.

A 1-MHz square input wave is recommended for propagation delay tests.

Figure 4. Test Circuit

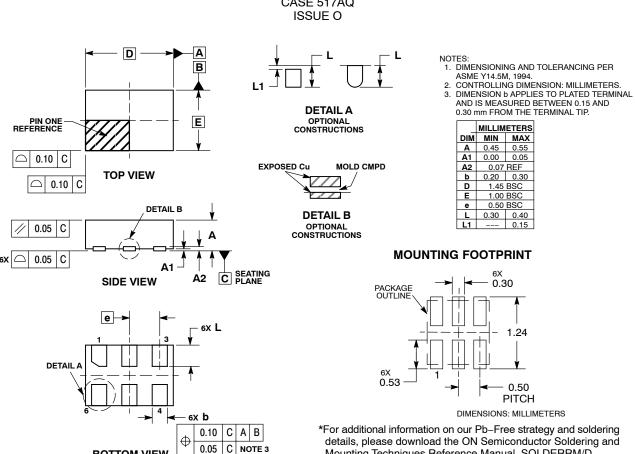
ORDERING INFORMATION

Device	Package	Shipping [†]
NLU1GU04MUTCG	UDFN6, 1.2 x 1.0, 0.4P (Pb-Free)	3000 / Tape & Reel
NLU1GU04AMUTCG	UDFN6, 1.45 x 1.0, 0.5P (Pb-Free)	3000 / Tape & Reel
NLU1GU04CMUTCG	UDFN6, 1.0 x 1.0, 0.35P (Pb-Free)	3000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

PACKAGE DIMENSIONS

UDFN6 1.45x1.0, 0.5P CASE 517AQ

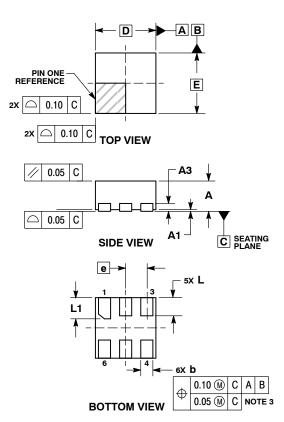


Mounting Techniques Reference Manual, SOLDERRM/D.

BOTTOM VIEW

PACKAGE DIMENSIONS

UDFN6 1.0x1.0, 0.35P CASE 517BX ISSUE O

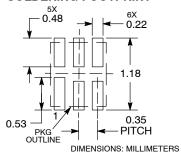


NOTES:

- DIMENSIONING AND TOLERANCING PER
- 1. DIMENSIONING ARIO TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.20 MM FROM TERMINAL TIP.
 4. PACKAGE DIMENSIONS EXCLUSIVE OF DIMENSIONS EXCLUSIVE OF DIMENSIONS EXCLUSIVE OF DIMENSIONS TOLERANCING TO THE PROPERTY OF THE PROPERT
- BURRS AND MOLD FLASH.

	MILLIMETERS					
DIM	MIN	MAX				
Α	0.45	0.55				
A1	0.00	0.05				
А3	0.13	REF				
b	0.12	0.22				
D	1.00 BSC					
E	1.00 BSC					
е	0.35 BSC					
L	0.25	0.35				
11	0.30	0.40				

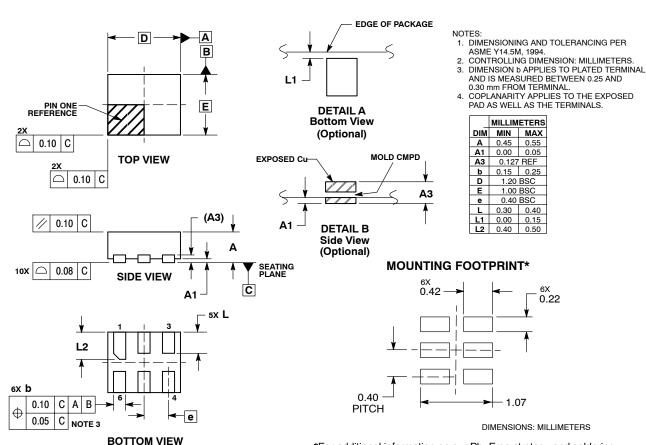
RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

PACKAGE DIMENSIONS

UDFN6, 1.2x1.0, 0.4P CASE 517AA ISSUE D



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