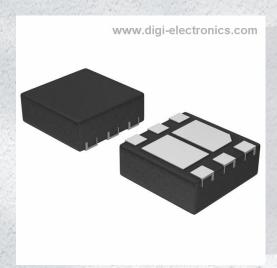


NSV20200DMTWTBG Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number NSV20200DMTWTBG-DG

Manufacturer onsemi

Manufacturer Product Number NSV20200DMTWTBG

Description TRANS NPN 20V 6WDFN

Detailed Description Bipolar (BJT) Transistor Array 2 PNP (Dual) 155MHz

Surface Mount 6-WDFN (2x2)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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8541.29.0075

Purchase and inquiry

| Manufacturer Product Number: | Manufacturer: |
|-----------------------------------|--|
| NSV20200DMTWTBG | onsemi |
| Series: | Product Status: |
| | Active |
| Transistor Type: | Vce Saturation (Max) @ lb, lc: |
| 2 PNP (Dual) | 390mV @ 200mA, 2A |
| Current - Collector Cutoff (Max): | DC Current Gain (hFE) (Min) @ Ic, Vce: |
| 100nA (ICBO) | 250 @ 100mA, 2V |
| Frequency - Transition: | Operating Temperature: |
| 155MHz | -55°C ~ 150°C (TJ) |
| Mounting Type: | Package / Case: |
| Surface Mount | 6-WDFN Exposed Pad |
| Supplier Device Package: | Base Product Number: |
| 6-WDFN (2x2) | NSV20200 |

Environmental & Export classification

| RoHS Status: | Moisture Sensitivity Level (MSL): |
|------------------|-----------------------------------|
| ROHS3 Compliant | 1 (Unlimited) |
| REACH Status: | ECCN: |
| REACH Unaffected | EAR99 |
| HTSUS: | |



Low V_{CE(sat)} PNP Transistors 20 V, 2 A

NSS20200DMT

onsemi's e^2 PowerEdge family of low $V_{CE(sat)}$ transistors are miniature surface mount devices featuring ultra low saturation voltage $(V_{CE(sat)})$ and high current gain capability. These are designed for use in low voltage, high speed switching applications where affordable efficient energy control is important.

Typical applications are DC-DC converters and LED lightning, power management...etc. In the automotive industry they can be used in air bag deployment and in the instrument cluster. The high current gain allows e²PowerEdge devices to be driven directly from PMU's control outputs, and the Linear Gain (Beta) makes them ideal components in analog amplifiers.

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- NSV20200DMTWTBG Wettable Flanks Device
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

MAXIMUM RATINGS (T_A = 25°C)

| Rating | Symbol | Max | Unit |
|--------------------------------|-----------------|-----|------|
| Collector-Emitter Voltage | V_{CEO} | 20 | Vdc |
| Collector-Base Voltage | V_{CBO} | 20 | Vdc |
| Emitter-Base Voltage | V_{EBO} | 7 | Vdc |
| Collector Current – Continuous | Ic | 2 | Α |
| Collector Current - Peak | I _{CM} | 3 | Α |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|--|-----------------------------------|----------------|------|
| Thermal Resistance Junction-to-Ambient (Notes 1 and 2) | $R_{\theta JA}$ | 60 | °C/W |
| Total Power Dissipation per Package @ T _A = 25°C (Note 2) | P _D | 2.10 | W |
| Thermal Resistance Junction-to-Ambient (Note 3) | $R_{\theta JA}$ | 79 | °C/W |
| Power Dissipation per Transistor @ T _A = 25°C (Note 3) | P _D | 1.59 | W |
| Junction and Storage Temperature Range | T _J , T _{stg} | -55 to +150 | °C |

- 1. Per JESD51-7 with 100 \mbox{mm}^2 pad area and 2 oz. Cu (Dual Operation).
- 2. P_D per Transistor when both are turned on is one half of Total P_D or 1.13 Watts.
- 3. Per JESD51–7 with 100 mm² pad area and 2 oz. Cu (Single–Operation).

20 Volt, 2 Amp PNP Low $V_{CE(sat)}$ Transistors

WDFN6 CASE 506AN



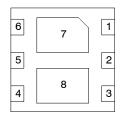
MARKING

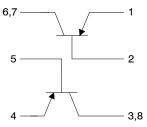
AT = Specific Device Code

M = Date CodePb-Free Package

(Note: Microdot may be in either location)

PIN CONNECTIONS





ORDERING INFORMATION

| Device | Package | Shipping [†] |
|-----------------|--------------------|-----------------------|
| NSS20200DMTTBG | WDFN6 (Pb-Free) | 3000 / Tape & Reel |
| NSV20200DMTWTBG | WDFN6 (Pb-Free) | 3000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

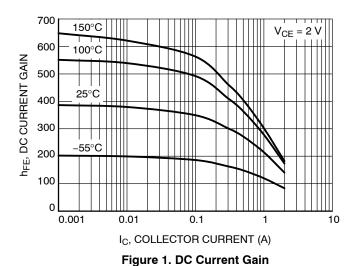
Table 1. ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)

| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|----------------------|--------------------------|-----|----------------------------------|----------|
| OFF CHARACTERISTICS | • | | • | -1 | · · |
| Collector–Emitter Breakdown Voltage (I _C = -10 mA, I _B = 0) | V _{(BR)CEO} | -20 | | | V |
| Collector-Base Breakdown Voltage (Ic = -0.1 mA, I _E = 0) | V _{(BR)CBO} | -20 | | | V |
| Emitter-Base Breakdown Voltage (I _E = -0.1 mA, I _C = 0) | V _{(BR)EBO} | -7 | | | V |
| Collector Cutoff Current (V _{CB} = -60 V, I _E = 0) | I _{CBO} | | | -100 | nA |
| Emitter Cutoff Current (V _{BE} = -5.0 V) | I _{EBO} | | | -100 | nA |
| ON CHARACTERISTICS | • | | • | -1 | <u> </u> |
| DC Current Gain (Note 4) $ (I_C = -100 \text{ mA}, V_{CE} = -2.0 \text{ V}) $ $ (I_C = -500 \text{ mA}, V_{CE} = -2.0 \text{ V}) $ $ (I_C = -1 \text{ A}, V_{CE} = -2.0 \text{ V}) $ $ (I_C = -2 \text{ A}, V_{CE} = -2.0 \text{ V}) $ | h _{FE} | 250 210 160 100 | | | |
| Collector–Emitter Saturation Voltage (Note 4) $ (I_C = -500 \text{ mA}, I_B = -50 \text{ mA}) $ $ (I_C = -700 \text{ mA}, I_B = -7 \text{ mA}) $ $ (I_C = -1 \text{ A}, I_B = -50 \text{ mA}) $ $ (I_C = -2 \text{ A}, I_B = -200 \text{ mA}) $ | V _{CE(sat)} | | | -0.11 -0.20 -0.22 -0.39 | V |
| Base – Emitter Saturation Voltage (Note 4) $ (I_C = -500 \text{ mA}, I_B = -50 \text{ mA}) $ $ (I_C = -1 \text{ A}, I_B = -50 \text{ mA}) $ $ (I_C = -1 \text{ A}, I_B = -100 \text{ mA}) $ | V _{BE(sat)} | | | -1.0 -1.1 -1.2 | V |
| Base-Emitter Turn-on Voltage (Note 4) (I _C = 500 mA, I _B = 50 mA) | V _{BE(on)} | | | -0.9 | V |
| DYNAMIC CHARACTERISTICS | | | | 1 | 1 |
| Output Capacitance (V _{CB} = 10 V, f = 1.0 MHz) | C _{obo} | | 18 | | pF |
| Cutoff Frequency ($I_C = 50 \text{ mA}$, $V_{CE} = 2.0 \text{ V}$, $f = 100 \text{ MHz}$) | f _T | | 155 | | MHz |
| SWITCHING TIMES | | | | | |
| Delay Time ($V_{CC} = -10 \text{ V}$, $I_{C} = -0.5 \text{ A}$, $I_{B1} = -25 \text{ mA}$, $I_{B2} = 25 \text{ mA}$) | t _d | | 15 | | ns |
| Rise Time ($V_{CC} = -10 \text{ V}$, $I_{C} = -0.5 \text{ A}$, $I_{B1} = -25 \text{ mA}$, $I_{B2} = 25 \text{ mA}$) | t _r | | 13 | | ns |
| Storage Time ($V_{CC} = -10 \text{ V}, I_C = -0.5 \text{ A}, I_{B1} = -25 \text{ mA}, I_{B2} = 25 \text{ mA}$) | t _s | | 360 | | ns |
| Fall Time ($V_{CC} = -10 \text{ V}, I_{C} = -0.5 \text{ A}, I_{B1} = -25 \text{ mA}, I_{B2} = 25 \text{ mA}$) | t _f | | 22 | | ns |

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

4. Pulse Condition: Pulse Width = 300 μsec, Duty Cycle ≤ 2%

TYPICAL CHARACTERISTICS



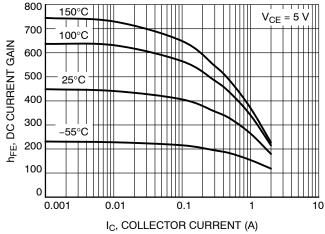


Figure 2. DC Current Gain

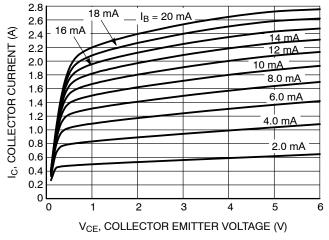


Figure 3. Collector Current as a Function of Collector Emitter Voltage

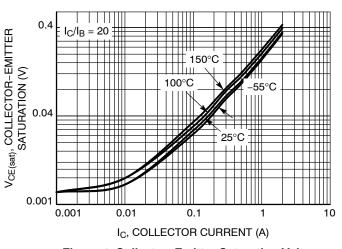


Figure 4. Collector-Emitter Saturation Voltage

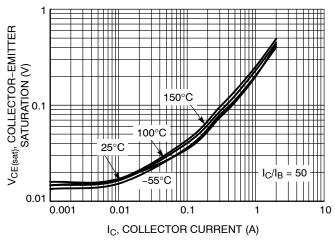


Figure 5. Collector-Emitter Saturation Voltage

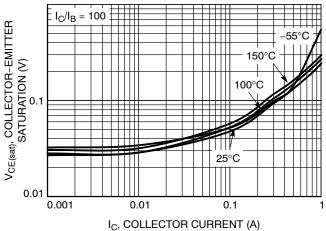
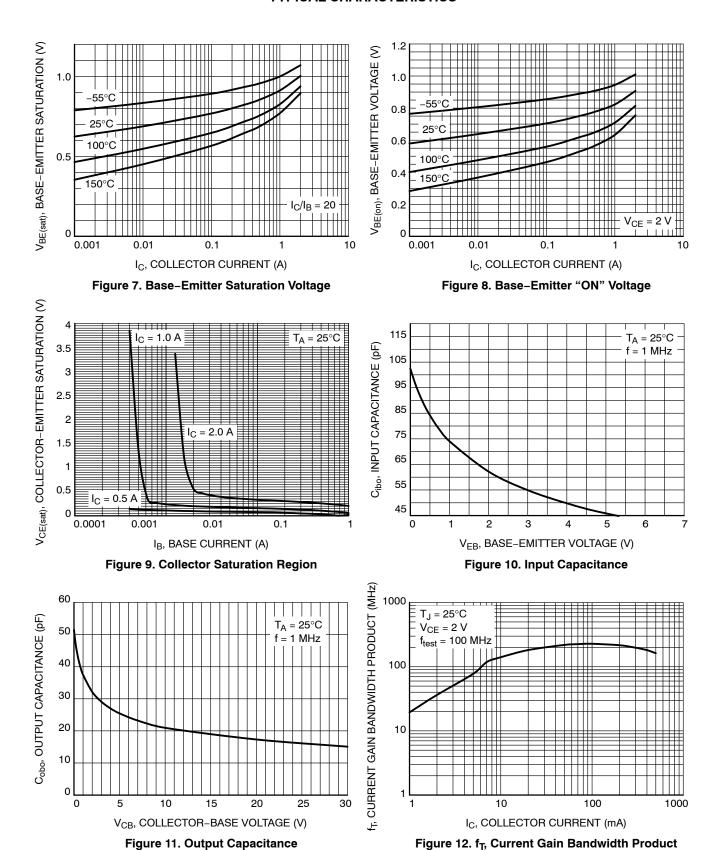


Figure 6. Collector-Emitter Saturation Voltage

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

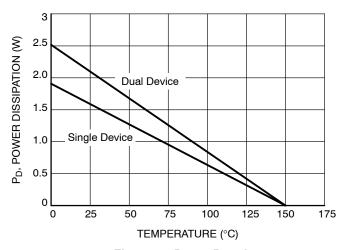


Figure 13. Power Derating

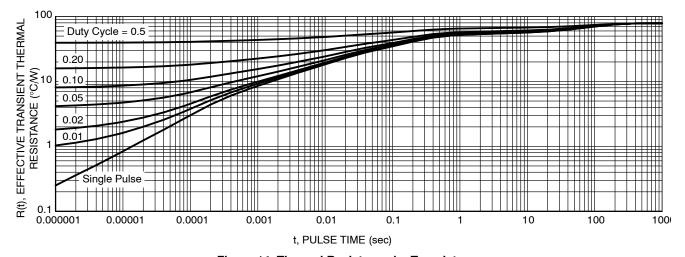


Figure 14. Thermal Resistance by Transistor

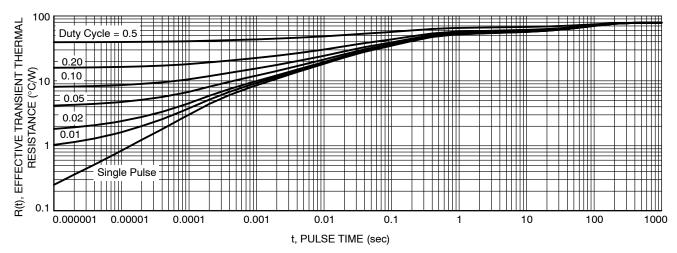


Figure 15. Thermal Resistance for Both Transistors



MECHANICAL CASE OUTLINE

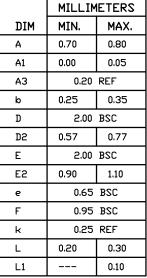
PACKAGE DIMENSIONS

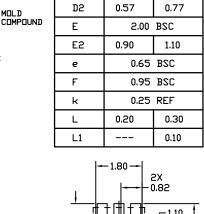
WDFN6 2x2, 0.65P CASE 506AN ISSUE H

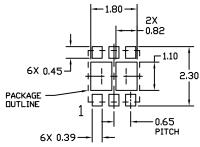
DATE 25 JAN 2022

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER. ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSION 6 APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30 MM FROM THE TERMINAL TIP.
- 4. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.



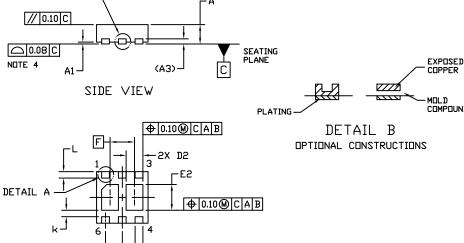




RECOMMENDED MOUNTING FOOTPRINT SOLDERMASK DEFINED

| PIN ONE | A E | 3 | |
|-----------|-----|--------|-----------------------|
| REFERENCE | E | - - | TT-L |
| TOP VIEW | | | AIL A INSTRUCTIONS |

DETAIL B



0.10 C A B

0.05 C

GENERIC MARKING DIAGRAM*

BOTTOM VIEW



XX = Specific Device Code = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "■", may or may not be present. Some products may not follow the Generic Marking.

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