

NTR1P02T1G Datasheet

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DiGi Electronics Part Number NTR1P02T1G-DG

Manufacturer onsemi

Manufacturer Product Number NTR1P02T1G

Description MOSFET P-CH 20V 1A SOT23-3

Detailed Description P-Channel 20 V 1A (Ta) 400mW (Ta) Surface Mount

SOT-23-3 (TO-236)



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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
NTR1P02T1G	onsemi
Series:	Product Status:
	Active
FET Type:	Technology:
P-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
20 V	1A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
4.5V, 10V	180mOhm @ 1.5A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
2.3V @ 250µA	2.5 nC @ 5 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	165 pF @ 5 V
FET Feature:	Power Dissipation (Max):
	400mW (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
SOT-23-3 (TO-236)	TO-236-3, SC-59, SOT-23-3
Base Product Number:	
NTD1D02	

Environmental & Export classification

8541.21.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

MOSFET – Power, P-Channel, SOT-23 -20 V, -1 A

Features

 Ultra Low On–Resistance Provides Higher Efficiency and Extends Battery Life

 $R_{DS(on)} = 0.180 \ \Omega, \ V_{GS} = -10 \ V$ $R_{DS(on)} = 0.280 \ \Omega, \ V_{GS} = -4.5 \ V$

- Power Management in Portable and Battery-Powered Products
- Miniature SOT-23 Surface Mount Package Saves Board Space
- Mounting Information for SOT-23 Package Provided
- NVR Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

- DC-DC Converters
- Computers
- Printers
- PCMCIA Cards
- Cellular and Cordless Telephones

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	-20	V
Gate-to-Source Voltage - Continuous	V _{GS}	±20	V
Drain Current - Continuous @ T_A = 25°C - Pulsed Drain Current ($t_p \le 1 \mu s$)	I _D I _{DM}	-1.0 -2.67	Α
Total Power Dissipation @ T _A = 25°C	P _D	400	mW
Operating and Storage Temperature Range	T _J , T _{stg}	– 55 to 150	°C
Thermal Resistance; Junction-to-Ambient	$R_{\theta JA}$	300	°C/W
Maximum Lead Temperature for Soldering Purposes, (1/8" from case for 10 s)	TL	260	°C

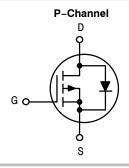
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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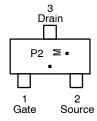
V _{(BR)DSS}	R _{DS(on)} TYP	I _D MAX	
-20 V	148 mΩ @ –10 V	–1.0 A	



MARKING DIAGRAM/ PIN ASSIGNMENT



SOT-23 CASE 318 STYLE 21



P2 = Specific Device Code

M = Date Code■ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NTR1P02T1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
NTR1P02T3G	SOT-23 (Pb-Free)	10000 / Tape & Reel
NVR1P02T1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

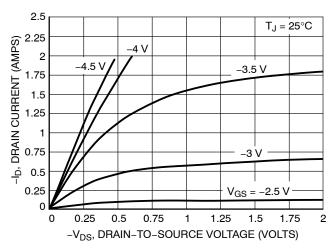
ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	<u> </u>	1			
Drain-to-Source Breakdown Voltage $(V_{GS}=0\ V,\ I_D=-10\ \mu A)$ (Positive Temperature Coefficient)	V _(BR) DSS	-20	32		V mV/°C
Zero Gate Voltage Drain Current $ (V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 25^{\circ}\text{C}) $ $ (V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}, T_J = 150^{\circ}\text{C}) $	I _{DSS}			-1.0 -10	μΑ
Gate-Body Leakage Current ($V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$)	I _{GSS}			±100	nA
ON CHARACTERISTICS (Note 1)					
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = -250 \mu A)$ (Negative Temperature Coefficient)	V _{GS(th)}	-1.1	-1.9 -4.0	-2.3	V mV/°C
Static Drain-to-Source On-State Resistance $(V_{GS} = -10 \text{ V}, I_D = -1.5 \text{ A})$ $(V_{GS} = -4.5 \text{ V}, I_D = -0.75 \text{ A})$	R _{DS(on)}		0.148 0.235	0.180 0.280	Ω
DYNAMIC CHARACTERISTICS					
Input Capacitance $(V_{DS} = -5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C _{iss}		165		pF
Output Capacitance $(V_{DS} = -5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C _{oss}		110		
Reverse Transfer Capacitance $(V_{DS} = -5 \text{ V}, V_{GS} = 0 \text{ V}, f = 1.0 \text{ MHz})$	C _{rss}		35		
SWITCHING CHARACTERISTICS (Note 2)					
Turn–On Delay Time (V_{DD} = -15 V, I_{D} = -1 A, V_{GS} = -5 V, R_{G} = 2.5 Ω)	t _{d(on)}		7.0		ns
Rise Time $ (V_{DD} = -15 \text{ V}, \text{ I}_D = -1 \text{ A}, \text{ V}_{GS} = -5 \text{ V}, \text{ R}_G = 2.5 \Omega) $	t _r		9.0		
Turn–Off Delay Time (V _{DD} = -15 V, I _D = -1 A, V _{GS} = -5 V, R _G = 2.5 Ω)	t _{d(off)}		9.0		
Fall Time $(V_{DD} = -15 \text{ V}, \text{ I}_D = -1 \text{ A}, \text{ V}_{GS} = -5 \text{ V}, \text{ R}_G = 2.5 \Omega)$	t _f		3.0		
Total Gate Charge $(V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -0.8 \text{ A})$	Q _{tot}		2.5		nC
Gate–Source Charge $(V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -0.8 \text{ A})$	Q_{gs}		0.75		
Gate-Drain Charge $(V_{DS} = -15 \text{ V}, V_{GS} = -5 \text{ V}, I_D = -0.8 \text{ A})$	Q_{gd}		1.0		
BODY-DRAIN DIODE RATINGS (Note 1)	•	•	•		
Diode Forward On–Voltage (Note 2) ($I_S = -0.6 \text{ A}, V_{GS} = 0 \text{ V}$) ($I_S = -0.6 \text{ A}, V_{GS} = 0 \text{ V}, T_J = 150^{\circ}\text{C}$)	V _{SD}		-0.8 -0.6	-1.0	V
Reverse Recovery Time	t _{rr}		13.5		ns
$(I_S = -1 \text{ A, } dI_S/dt = 100 \text{ A/}\mu\text{s, } V_{GS} = 0 \text{ V})$	ta		10.5		1
	t _b		3.0		1
Reverse Recovery Stored Charge (I _S = -1 A, dI _S /dt = 100 A/μs, V _{GS} = 0 V)	Q _{RR}		0.008		μC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%.

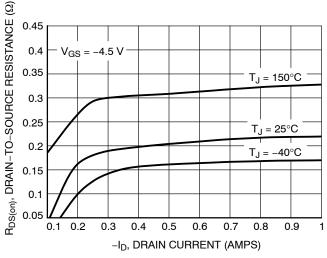
2. Switching characteristics are independent of operating junction temperature.



 $V_{DS} \ge -10 \text{ V}$ $T_J = 25^{\circ}C$ -ID, DRAIN CURRENT (AMPS) 1.75 1.5 1.25 1 0.75 T_J = 125°C $T_J = -40^{\circ}C$ 0.5 0.25 0 1.5 2 2.5 -V_{GS}, GATE-TO-SOURCE VOLTAGE (VOLTS)

Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



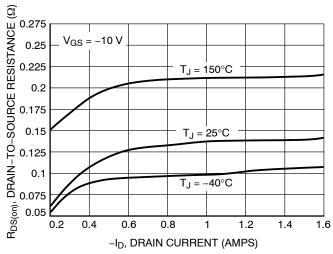
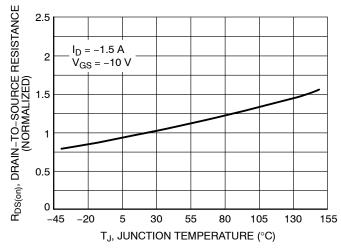


Figure 3. On–Resistance versus Drain Current and Temperature

Figure 4. On-Resistance versus Drain Current and Temperature



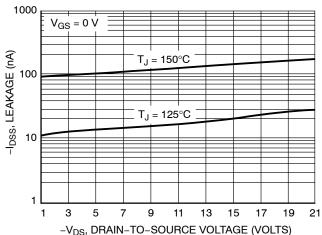


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current versus Voltage

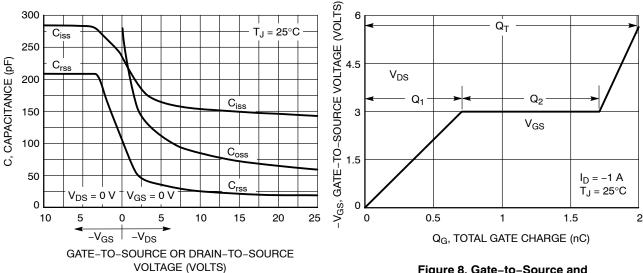


Figure 7. Capacitance Variation

Figure 8. Gate-to-Source and Drain-to-Source Voltage versus Total Charge

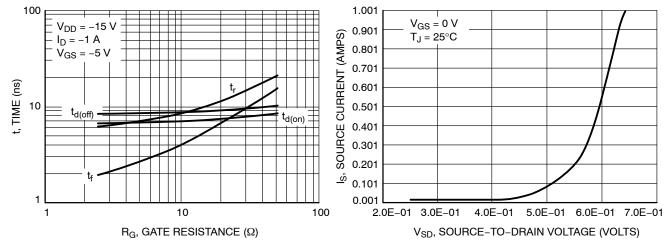


Figure 9. Resistive Switching Time Variation versus Gate Resistance

Figure 10. Diode Forward Voltage versus Current



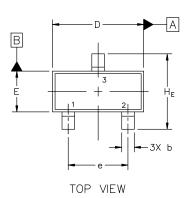
MECHANICAL CASE OUTLINE

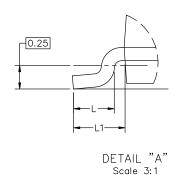
PACKAGE DIMENSIONS

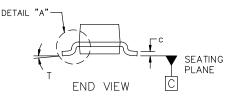


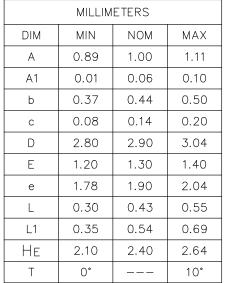
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DATE 14 AUG 2024









NOTES:

- DIMENSIONING AND TOLERANCING 1.
- PER ASME Y14.5M, 2018. CONTROLLING DIMENSIONS: MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE
- BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

GENERIC MARKING DIAGRAM*

SIDE VIEW

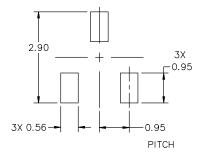


XXX = Specific Device Code

= Date Code

= Pb-Free Package

^{*}This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.



RECOMMENDED MOUNTING FOOTPRINT

* For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

STYLES ON PAGE 2

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STYLE 1 THRU 5: CANCELLED	STYLE 6: PIN 1. BASE 2. EMITTER 3. COLLECTOR	STYLE 7: STYLE 8: PIN 1. EMITTER PIN 1. ANOD 2. BASE 2. NO CC 3. COLLECTOR 3. CATHO	ONNECTION	
STYLE 9: PIN 1. ANODE 2. ANODE 3. CATHODE	STYLE 10: PIN 1. DRAIN 2. SOURCE 3. GATE	STYLE 11: STYLE 12: PIN 1. ANODE PIN 1. CATHO 2. CATHODE 2. CATHO 3. CATHODE-ANODE 3. ANODO	ODE 2. DRAIN 2. GATE	
STYLE 15: PIN 1. GATE 2. CATHODE 3. ANODE	STYLE 16: PIN 1. ANODE 2. CATHODE 3. CATHODE	STYLE 17: STYLE 18: PIN 1. NO CONNECTION PIN 1. NO CO 2. ANODE 2. CATHO 3. CATHODE 3. ANODO	ODE 2. ANODE 2. ANODE	
STYLE 21: PIN 1. GATE 2. SOURCE 3. DRAIN	STYLE 22: PIN 1. RETURN 2. OUTPUT 3. INPUT	STYLE 23: STYLE 24: PIN 1. ANODE PIN 1. GATE 2. ANODE 2. DRAIN 3. CATHODE 3. SOURCE		CTION
STYLE 27: PIN 1. CATHODE 2. CATHODE 3. CATHODE	STYLE 28: PIN 1. ANODE 2. ANODE 3. ANODE			

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