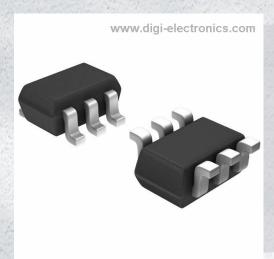


# **NVJS3151PT1G Datasheet**



https://www.DiGi-Electronics.com

DiGi Electronics Part Number NVJS3151PT1G-DG

Manufacturer onsemi

Manufacturer Product Number NVJS3151PT1G

Description MOSFET P-CH 12V 2.7A SC88

Detailed Description P-Channel 12 V 2.7A (Ta) 625mW (Ta) Surface Mou

nt SC-88/SC70-6/SOT-363



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RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
NVJS3151PT1G	onsemi
Series:	Product Status:
	Obsolete
FET Type:	Technology:
P-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
12 V	2.7A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
1.8V, 4.5V	60mOhm @ 3.3A, 4.5V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
1.2V @ 100µA	8.6 nC @ 4.5 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±12V	850 pF @ 12 V
FET Feature:	Power Dissipation (Max):
	625mW (Ta)
Operating Temperature:	Grade:
-55°C ~ 150°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Supplier Device Package:	Package / Case:
SC-88/SC70-6/SOT-363	6-TSSOP, SC-88, SOT-363
Base Product Number:	
NIVIS31	

### **Environmental & Export classification**

8541.21.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

# **MOSFET** – Power, Single, P-Channel, Trench, ESD Protected, SC-88

12 V, 3.3 A

#### **Features**

- Leading Trench Technology for Low R<sub>DS(ON)</sub> Extending Battery Life
- SC-88 Small Outline (2x2 mm, SC70-6 Equivalent)
- Gate Diodes for ESD Protection
- NV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

#### **Applications**

- High Side Load Switch
- Cell Phones, Computing, Digital Cameras, MP3s and PDAs

#### MAXIMUM RATINGS (T<sub>J</sub> = 25°C unless otherwise stated)

Param	Symbol	Value	Units			
Drain-to-Source Voltage			$V_{DSS}$	-12	V	
Gate-to-Source Voltage			V <sub>GS</sub>	±12	V	
Continuous Drain	Steady	T <sub>A</sub> = 25 °C	I <sub>D</sub>	-2.7	Α	
Current (Note 1)	State	T <sub>A</sub> = 85 °C		-2.0		
	t ≤ 5 s	T <sub>A</sub> = 25 °C		-3.3		
Power Dissipation Steady (Note 1) State		T <sub>A</sub> = 25 °C	P <sub>D</sub>	0.625	W	
Pulsed Drain Current	I <sub>DM</sub>	-8.0	Α			
Operating Junction and S	T <sub>J</sub> , T <sub>STG</sub>	–55 to 150	°C			
Source Current (Body Diode)			IS	-0.8	Α	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			$T_L$	260	°C	

#### THERMAL RESISTANCE RATINGS (Note 1)

Parameter	Symbol	Max	Units
Junction-to-Ambient - Steady State	$R_{\theta JA}$	200	°C/W
Junction-to-Ambient - t ≤ 5 s	$R_{\theta JA}$	141	
Junction-to-Lead - Steady State	$R_{\theta JL}$	102	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

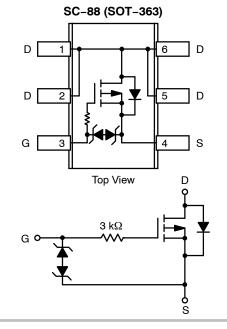
1. Surface mounted on FR4 board using 1 in sq pad size (Cu area = 1.127 in sq [1 oz] including traces).



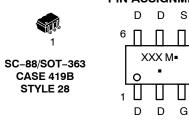
#### ON Semiconductor®

#### www.onsemi.com

V <sub>(BR)DSS</sub>	R <sub>DS(on)</sub> Typ	I <sub>D</sub> Max
	45 m $\Omega$ @ –4.5 V	
-12 V	67 mΩ @ –2.5 V	-3.3 A
	133 mΩ @ –1.8 V	



# MARKING DIAGRAM & PIN ASSIGNMENT



XXX = Device Code

M = Date Code

Pb-Free Package

(Note: Microdot may be in either location)

#### **ORDERING INFORMATION**

See detailed ordering and shipping information in the package dimensions section on page 4 of this data sheet.

#### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub>=25°C unless otherwise stated)

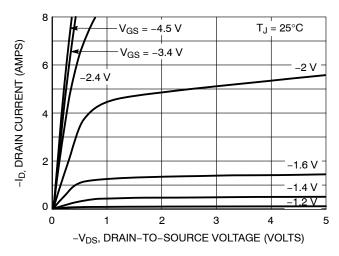
Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				-		-
Drain-to-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		-12			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V <sub>(BR)DSS</sub> /T <sub>J</sub>				10		mV/°C
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>GS</sub> = -9.6 V,	T <sub>J</sub> = 25°C			-1.0	μΑ
		$V_{GS} = -9.6 \text{ V},$ $V_{DS} = 0 \text{ V}$	T <sub>J</sub> = 125°C		-2.5		
Gate-to-Source Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>G</sub>	<sub>S</sub> = ±4.5 V			±1.5	μΑ
		$V_{DS} = 0 \text{ V, } V_{G}$	<sub>iS</sub> = ±12 V			±10	mA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	$V_{GS} = V_{DS}, I_{D}$	= 100 μΑ	-0.40		-1.2	V
Negative Threshold Temperature Coefficient	V <sub>GS(TH)</sub> /T <sub>J</sub>				3.4		mV/°C
Drain-to-Source On Resistance	R <sub>DS(on)</sub>	$V_{GS} = -4.5 \text{ V},$	<sub>D</sub> = -3.3 A		45	60	mΩ
	$V_{GS} = -2.5 \text{ V}, I_D = -2.9 \text{ A}$			67	90	7	
		$V_{GS} = -1.8 \text{ V}, I_D = -1.0 \text{ A}$			133	160	
Forward Transconductance	9FS	$V_{GS} = -10 \text{ V}, I_D = -3.3 \text{ A}$			15		S
CHARGES AND CAPACITANCES							
Input Capacitance	C <sub>ISS</sub>	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = -12 \text{ V}$			850		pF
Output Capacitance	C <sub>OSS</sub>				170		
Reverse Transfer Capacitance	C <sub>RSS</sub>	- 03			110		
Total Gate Charge	Q <sub>G(TOT)</sub>				8.6		nC
Gate-to-Source Charge	$Q_{GS}$	$V_{GS} = -4.5 \text{ V, V}$ $I_{D} = -3.5 \text{ V}$	<sub>OS</sub> = -5.0 V, 3 A		1.3		
Gate-to-Drain Charge	$Q_{GD}$	D			2.2		
Gate Resistance	$R_{G}$				3000		Ω
SWITCHING CHARACTERISTICS (No	te 3)						
Turn-On Delay Time	t <sub>d(ON)</sub>	$V_{GS}$ = -4.5 V, $V_{DD}$ = -6.0 V, $I_{D}$ = -1.0 A, $R_{G}$ = 6.0 $\Omega$			0.86		μs
Rise Time	t <sub>r</sub>				1.5		
Turn-Off Delay Time	t <sub>d(OFF)</sub>				3.5		
Fall Time	t <sub>f</sub>				3.9		
DRAIN-SOURCE DIODE CHARACTE	RISTICS (Note :	2)			•		
Forward Diode Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0 V,	$T_J = 25^{\circ}C$		-0.85	-1.2	V
		$I_S = -3.3 \text{ A}$ $T_J = 125^{\circ}\text{C}$			-0.7		7

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

2. Pulse Test: pulse width ≤ 300μs, duty cycle ≤ 2%.

<sup>3.</sup> Switching characteristics are independent of operating junction temperatures.

#### TYPICAL PERFORMANCE CURVES ( $T_J = 25^{\circ}C$ unless otherwise noted)



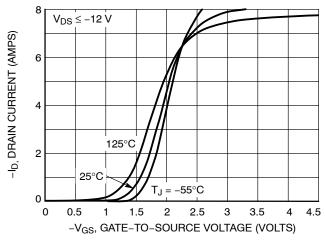
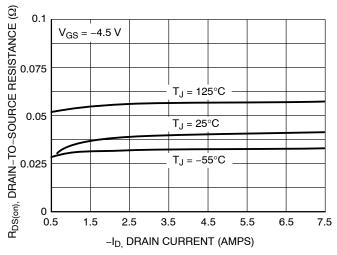


Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



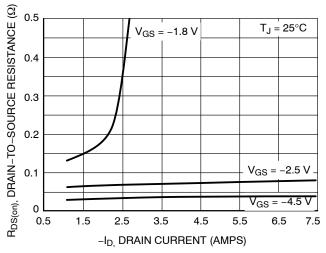


Figure 3. On–Resistance vs. Drain Current and Temperature

Figure 4. On-Resistance vs. Drain Current and Gate Voltage

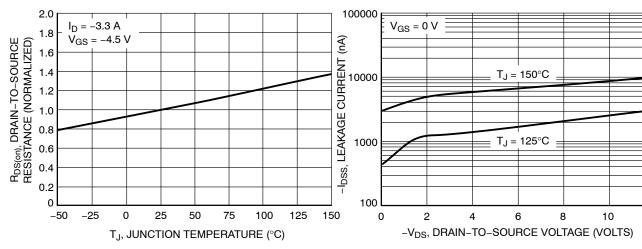
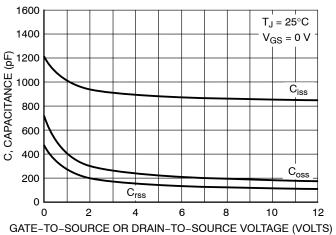


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current vs. Voltage

12

#### TYPICAL PERFORMANCE CURVES (T<sub>J</sub> = 25°C unless otherwise noted)



-V<sub>GS,</sub> GATE-TO-SOURCE VOLTAGE (VOLTS) 4.5 QT 3.5 3 2.5 2 Q2 1.5 1  $I_D = -3.3 A$ 0.5 T<sub>J</sub> = 25°C 0 4 6 8 10 0 Q<sub>a</sub>, TOTAL GATE CHARGE (nC)

Figure 7. Capacitance Variation

Figure 8. Gate-to-Source Voltage vs. Total Gate Charge

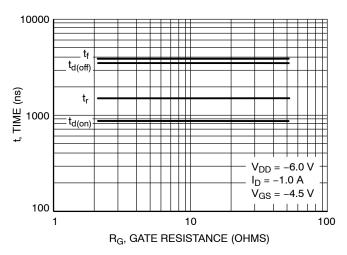


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

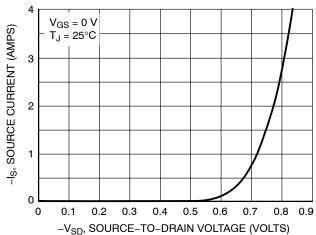


Figure 10. Diode Forward Voltage vs. Current

#### **ORDERING INFORMATION**

Device	Marking	Package	Shipping <sup>†</sup>	
NTJS3151PT1G	TJ			
NTJS3151PT2G	TJ	SC-88 (Pb-Free)	3000 / Tape & Reel	
NVJS3151PT1G*	VTJ	, ,		

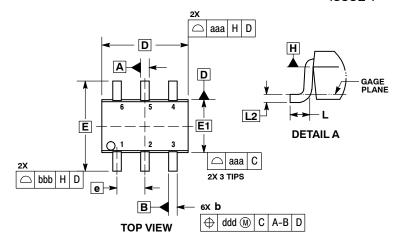
<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

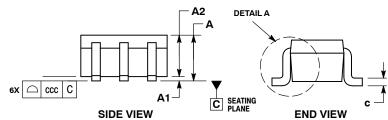
<sup>\*</sup>NV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable.

#### PACKAGE DIMENSIONS

#### SC-88/SC70-6/SOT-363 CASE 419B-02

**ISSUE Y** 





- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
  DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRU-SIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
- DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF
- THE PLASTIC BODY AND DATUM H.

  DATUMS A AND B ARE DETERMINED AT DATUM H.

  DIMENSIONS b AND CAPPLY TO THE FLAT SECTION OF THE

  LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
- DIMENSION 5 DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION 6 AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

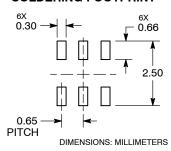
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α			1.10			0.043
A1	0.00		0.10	0.000		0.004
A2	0.70	0.90	1.00	0.027	0.035	0.039
b	0.15	0.20	0.25	0.006	0.008	0.010
С	0.08	0.15	0.22	0.003	0.006	0.009
D	1.80	2.00	2.20	0.070	0.078	0.086
E	2.00	2.10	2.20	0.078	0.082	0.086
E1	1.15	1.25	1.35	0.045	0.049	0.053
е	0.65 BSC			0.026 BSC		
L	0.26	0.36	0.46	0.010	0.014	0.018
L2	0.15 BSC			0.006 BSC		
aaa	0.15				0.006	
bbb	0.30			0.012		
ccc	0.10			0.004		
ddd	0.10				0.004	

STYLE 28: PIN 1. DRAIN

2. DRAIN 3. GATE

4. SOURCE 5. DRAIN 6. DRAIN

#### **RECOMMENDED SOLDERING FOOTPRINT\***



Mounting Techniques Reference Manual, SOLDERRM/D.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and

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