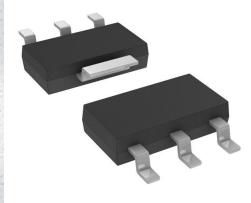


SBF720T1G Datasheet

www.digi-electronics.com



DiGi Electronics Part Number	SBF720T1G-DG
Manufacturer	onsemi
Manufacturer Product Number	SBF720T1G
Description	TRANS NPN 300V 0.1A SOT223
Detailed Description	Bipolar (BJT) Transistor NPN 300 V 100 mA 60MHz 1 .5 W Surface Mount SOT-223 (TO-261)

https://www.DiGi-Electronics.com



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
SBF720T1G	onsemi
Series:	Product Status:
	Active
Transistor Type:	Current - Collector (Ic) (Max):
NPN	100 mA
Voltage - Collector Emitter Breakdown (Max):	Vce Saturation (Max) @ lb, lc:
300 V	600mV @ 5mA, 30mA
Current - Collector Cutoff (Max):	DC Current Gain (hFE) (Min) @ lc, Vce:
10nA (ICBO)	50 @ 25mA, 20V
Power - Max:	Frequency - Transition:
1.5 W	60MHz
Operating Temperature:	Mounting Type:
-65°C ~ 150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
TO-261-4, TO-261AA	SOT-223 (TO-261)
Base Product Number:	
SBF720	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0075	

DNSemi

NPN Silicon Transistor

BF720T1G, SBF720T1G, **BF720T3G**

Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CEO}	300	Vdc
Collector - Base Voltage	V _{CBO}	300	Vdc
Collector - Emitter Voltage	V _{CER}	300	Vdc
Emitter – Base Voltage	V _{EBO}	5.0	Vdc
Collector Current	Ι _C	100	mAdc
Total Power Dissipation up to $T_A = 25^{\circ}C$	PD	1.5	W
Storage Temperature Range	T _{stg}	-65 to +150	°C
Junction Temperature	TJ	150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient (Note 2)	Bein	83.3	°C/W
Junction-to-Ambient (Note 2)	$R_{\theta JA}$	83.3	

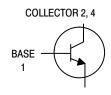
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Device mounted on a glass epoxy printed circuit board 1.575 in. x 1.575 in. x 0.059 in.; mounting pad for the collector lead min. 0.93 in².

NPN SILICON TRANSISTOR SURFACE MOUNT

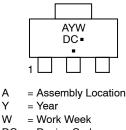


SOT-223 (TO-261) **CASE 318E** STYLE 1



EMITTER 3

MARKING DIAGRAM





= Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
BF720T1G	SOT–223 (Pb–Free)	1,000 / Tape & Reel
SBF720T1G	SOT–223 (Pb–Free)	1,000 / Tape & Reel

DISCONTINUED (Note 1)

Y

BF720T3G SOT-223 4,0 (Pb-Free) 4,0

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

1. DISCONTINUED: This device is not recommended for new design. Please contact your onsemi representative for information. The most current information on this device may be available on <u>www.onsemi.com</u>.

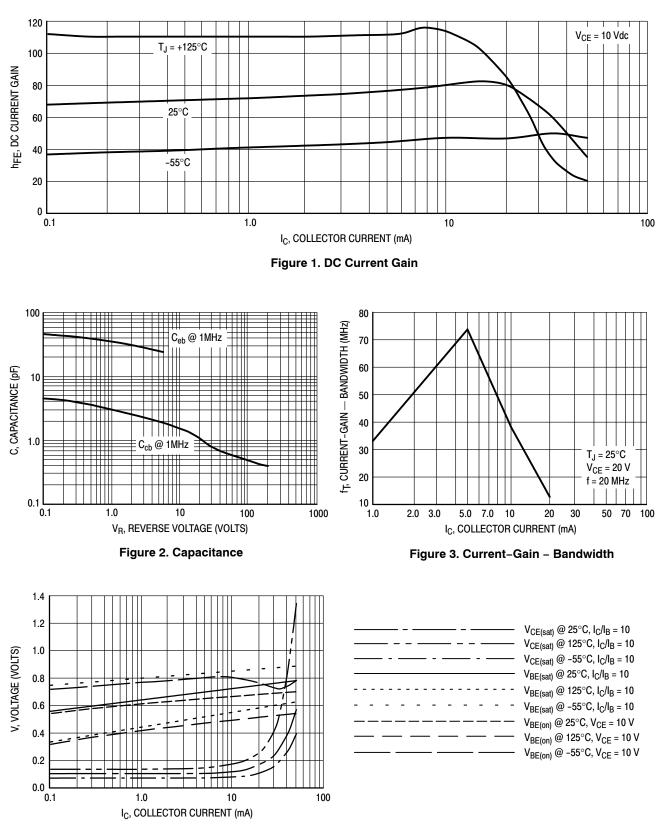
*For additional information on our Pb-Free strategy and soldering details, please download the onsemi Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

BF720T1G, SBF720T1G, BF720T3G

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristics	Symbol	Min	Мах	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage $(I_{C} = 1.0 \text{ mAdc}, I_{B} = 0)$	V _{(BR)CEO}	300	-	Vdc
Collector-Base Breakdown Voltage $(I_C = 100 \ \mu Adc, I_E = 0)$	V _(BR) CBO	300	-	Vdc
Collector-Emitter Breakdown Voltage ($I_C = 100 \ \mu Adc, R_{BE} = 2.7 \ k\Omega$)	V _{(BR)CER}	300	-	Vdc
Emitter-Base Breakdown Voltage ($I_E = 10 \ \mu Adc, I_C = 0$)	V _{(BR)EBO}	5.0	-	Vdc
Collector-Base Cutoff Current $(V_{CB} = 200 \text{ Vdc}, I_E = 0)$	I _{CBO}	_	10	nAdc
$ Collector-Emitter Cutoff Current \\ (V_{CE} = 250 \text{ Vdc}, \text{ R}_{BE} = 2.7 \text{ k}\Omega) \\ (V_{CE} = 200 \text{ Vdc}, \text{ R}_{BE} = 2.7 \text{ k}\Omega, \text{ T}_{J} = 150^{\circ}\text{C}) $	I _{CER}	- -	50 10	nAdc μAdc
ON CHARACTERISTICS				
DC Current Gain (I _C = 25 mAdc, V _{CE} = 20 Vdc)	h _{FE}	50	-	-
Collector-Emitter Saturation Voltage $(I_C = 30 \text{ mAdc}, I_B = 5.0 \text{ mAdc})$	V _{CE(sat)}	-	0.6	Vdc
DYNAMIC CHARACTERISTICS				
Current–Gain – Bandwidth Product ($I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}, f = 35 \text{ MHz}$)	f _T	60	_	MHz
Feedback Capacitance $(V_{CE} = 30 \text{ Vdc}, I_C = 0, f = 1.0 \text{ MHz})$	C _{re}	-	1.6	pF

BF720T1G, SBF720T1G, BF720T3G





nsemi

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS



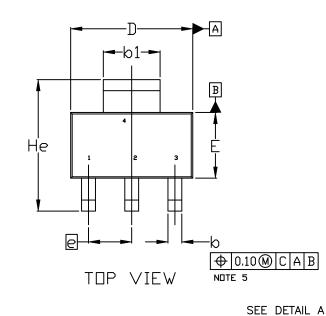
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SOT-223 (TO-261) CASE 318E-04 ISSUE R

DATE 02 OCT 2018



SIDE VIEW

DETAIL A

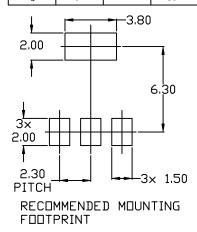
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NDTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSIONS D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.200MM PER SIDE.
- 4. DATUMS A AND B ARE DETERMINED AT DATUM H.
- 5. ALLS DEFINED AS THE VERTICAL DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT OF THE PACKAGE BODY.
- 6. POSITIONAL TOLERANCE APPLIES TO DIMENSIONS 6 AND 61.

	MILLIMETERS			
DIM	MIN.	NDM.	MAX.	
A	1.50	1.63	1.75	
A1	0.02	0.06	0.10	
b	0.60	0.75	0.89	
b1	2.90	3.06	3.20	
с	0.24	0.29	0.35	
D	6.30	6.50	6.70	
E	3.30	3.50	3.70	
e	2.30 BSC			
L	0.20			
L1	1.50	1.75	2.00	
He	6.70	7.00	7.30	
θ	0*		10*	



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FRONT VIEW

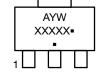
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SOT-223 (TO-261) CASE 318E-04 ISSUE R

DATE 02 OCT 2018

STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR	STYLE 2: PIN 1. ANODE 2. CATHODE 3. NC 4. CATHODE	STYLE 3: PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN	STYLE 4: Pin 1. Source 2. Drain 3. Gate 4. Drain	STYLE 5: PIN 1. DRAIN 2. GATE 3. SOURCE 4. GATE
Style 6: Pin 1. Return 2. Input 3. Output 4. Input	STYLE 7: PIN 1. ANODE 1 2. CATHODE 3. ANODE 2 4. CATHODE	STYLE 8: CANCELLED	Style 9: Pin 1. Input 2. Ground 3. Logic 4. Ground	STYLE 10: PIN 1. CATHODE 2. ANODE 3. GATE 4. ANODE
STYLE 11: PIN 1. MT 1 2. MT 2 3. GATE 4. MT 2	STYLE 12: PIN 1. INPUT 2. OUTPUT 3. NC 4. OUTPUT	STYLE 13: PIN 1. GATE 2. COLLECTOR 3. EMITTER 4. COLLECTOR		

GENERIC MARKING DIAGRAM*



- A = Assembly Location
- Y = Year
- W = Work Week
- XXXXX = Specific Device Code
- = Pb-Free Package
- (Note: Microdot may be in either location) *This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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