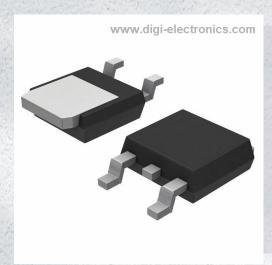


STD5407NT4G Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number STD5407NT4G-DG

Manufacturer onsemi

Manufacturer Product Number STD5407NT4G

Description MOSFET N-CH 40V 7.6A/38A DPAK

Detailed Description N-Channel 40 V 7.6A (Ta), 38A (Tc) 2.9W (Ta), 75W

(Tc) Surface Mount DPAK



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer:
onsemi
Product Status:
Obsolete
Technology:
MOSFET (Metal Oxide)
Current - Continuous Drain (Id) @ 25°C:
7.6A (Ta), 38A (Tc)
Rds On (Max) @ ld, Vgs:
26m0hm @ 20A, 10V
Gate Charge (Qg) (Max) @ Vgs:
20 nC @ 10 V
Input Capacitance (Ciss) (Max) @ Vds:
1000 pF @ 32 V
Power Dissipation (Max):
2.9W (Ta), 75W (Tc)
Mounting Type:
Surface Mount
Package / Case:
TO-252-3, DPAK (2 Leads + Tab), SC-63

Environmental & Export classification

Moisture Sensitivity Level (MSL):	REACH Status:
1 (Unlimited)	REACH Unaffected
ECCN:	HTSUS:
EAR99	8541.29.0095

Power MOSFET

40 V, 38 A, Single N-Channel, DPAK

Features

- Low R_{DS(on)}
- High Current Capability
- Low Gate Charge
- STD Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable*
- These Devices are Pb-Free and are RoHS Compliant

Applications

- Electronic Brake Systems
- Electronic Power Steering
- Bridge Circuits

MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise stated)

Parameter			Symbol	Value	Unit
Drain-to-Source Voltage			V_{DSS}	40	V
Gate-to-Source Voltage	е		V_{GS}	±20	V
Continuous Drain	Steady	T _C = 25°C	I _D	38	Α
Current – R _{θJC}	State	State $T_C = 100^{\circ}C$		27	
Power Dissipation – R _{θJC}	Steady State	T _C = 25°C	P _D	75	W
Continuous Drain	Steady	T _A = 25°C	I _D	7.6	Α
Current R _{θJA} (Note 1)	State	T _A = 100°C		5.3	
Power Dissipation – R _{θJA} (Note 1)	Steady State	T _A = 25°C	P _D	2.9	W
Pulsed Drain Current	t _p =	= 10 μs	I _{DM}	75	Α
Operating Junction and Storage Temperature			T _J , T _{STG}	–55 to 175	°C
Source Current (Body Diode)			I _S	36	Α
Single Pulse Drain–to Source Avalanche Energy – (V_{DD} = 50 V, V_{GS} = 10 V, I_{PK} = 17 A, L = 1 mH, R_G = 25 Ω)			EAS	150	mJ
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS (Note 1)

Parameter	Symbol	Max	Unit
Junction-to-Case (Drain)	$R_{\theta JC}$	2.0	°C/W
Junction-to-Ambient (Note 1)	$R_{\theta JA}$	52	°C/W

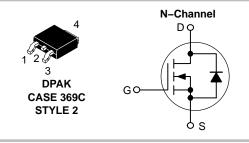
Surface mounted on FR4 board using 1 sq in pad size, (Cu Area 1.127 sq in [2 oz] including traces).



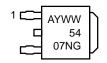
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V _{(BR)DSS}	R _{DS(ON)} TYP	I _D MAX (Note 1)
40 V	21 mΩ @ 10 V	38 A



MARKING DIAGRAM



A = Assembly Location*

Y = Year WW = Work Week

5407N = Specific Device Code

= Pb-Free Device

* The Assembly Location code (A) is front side optional. In cases where the Assembly Location is stamped in the package, the front side assembly code may be blank.

ORDERING INFORMATION

Device	Package	Shipping†
NTD5407NT4G	DPAK (Pb-Free)	2500 / Tape & Reel
STD5407NT4G*	DPAK (Pb-Free)	2500 / Tape & Reel
NVD5407NT4G*	DPAK (Pb-Free)	2500 / Tape & Reel

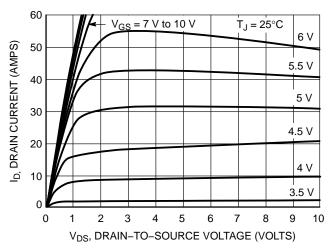
†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise stated)

Parameter	Symbol	Test Condition		Min	Тур	Max	Unit
OFF CHARACTERISTICS	-		•		-	-	•
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$		40			V
Drain-to-Source Breakdown Voltage Temperature Coefficient	V _{(BR)DSS} /T _J				39		mV/°C
Zero Gate Voltage Drain Current	I _{DSS}	$V_{GS} = 0 V$	T _J = 25°C			1.0	μΑ
		$V_{DS} = 40 \text{ V}$	T _J = 100°C			10	
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 V, V_{C}$	_{SS} = ±30 V			±100	nA
ON CHARACTERISTICS (Note 2)							
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_{E}$	ο = 250 μΑ	1.5		3.5	V
Gate Threshold Temperature Coefficient	V _{GS(TH)} /T _J				-6.0		mV/°C
Drain-to-Source On Resistance	R _{DS(on)}	V _{GS} = 10 V,	I _D = 20 A		21	26	mΩ
		$V_{GS} = 5.0 \text{ V},$	I _D = 10 A		32	40	7
Forward Transconductance	9FS	V _{GS} = 10 V,	I _D = 18 A		15		S
CHARGES AND CAPACITANCES					•	•	
Input Capacitance	C _{ISS}				615	1000	pF
Output Capacitance	C _{OSS}	$V_{GS} = 0 \text{ V, f} = V_{DS} = 3$: 1.0 MHz,		173		
Reverse Transfer Capacitance	C _{RSS}	VDS - V	, , , , , , , , , , , , , , , , , , ,		80		
Total Gate Charge	Q _{G(TOT)}				20		nC
Gate-to-Source Charge	Q _{GS}	$V_{GS} = 10 \text{ V}, \text{ V}$ $I_{D} = 38$	_{DS} = 32 V,		2.25		7
Gate-to-Drain Charge	Q_{GD}	ID = 30 A			10.5		
SWITCHING CHARACTERISTICS, Vo	_{SS} = 10 V (Note	3)	•				•
Turn-On Delay Time	t _{d(ON)}				6.8		ns
Rise Time	t _r	V _{GS} = 10 V. V	nn = 32 V.		17		
Turn-Off Delay Time	t _{d(OFF)}	$V_{GS} = 10 \text{ V, V}$ $I_D = 38 \text{ A, R}$	$_{\rm G}$ = 2.5 Ω		66		1
Fall Time	t _f		Ī		51		1
SWITCHING CHARACTERISTICS, Vo	_{SS} = 5 V (Note 3)			•	•	
Turn-On Delay Time	t _{d(ON)}				10		ns
Rise Time	t _r	$V_{GS} = 5 \text{ V}, V_{I}$	nn = 20 V,		175		1
Turn-Off Delay Time	t _{d(OFF)}	$V_{GS} = 5 \text{ V}, V_{DD} = 20 \text{ V},$ $I_{D} = 20 \text{ A}, R_{G} = 2.5 \Omega$			13		1
Fall Time	t _f				23		1
DRAIN-SOURCE DIODE CHARACTE	RISTICS (Note	2)					
Forward Diode Voltage	V_{SD}	$V_{GS} = 0 \text{ V},$ $I_{S} = 5.0 \text{ A}$ $T_{J} = 25^{\circ}\text{C}$ $T_{J} = 125^{\circ}\text{C}$			0.9	1.1	V
					0.75		
Reverse Recovery Time	t _{RR}	$V_{GS} = 0 \text{ V, } dI_{S}/dt = 100 \text{ A/}\mu\text{s,}$ $I_{S} = 15 \text{ A}$			38		ns
Charge Time	t _a				20.5		7
Discharge Time	t _b				17		1
Reverse Recovery Charge	Q_{RR}				40		nC

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.
2. Pulse Test: pulse width $\leq 300~\mu s$, duty cycle $\leq 2\%$.
3. Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES



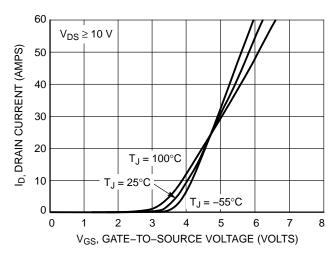
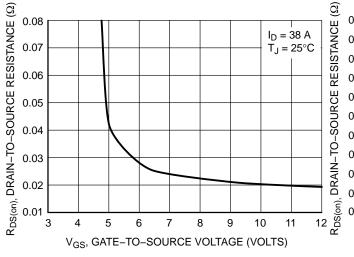


Figure 1. On-Region Characteristics

Figure 2. Transfer Characteristics



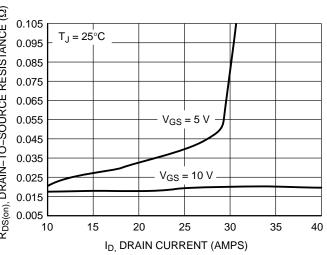
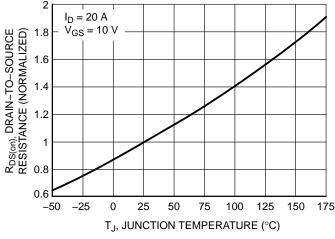


Figure 3. On-Resistance vs. Gate-to-Source Voltage

Figure 4. On-Resistance vs. Drain Current and Gate Voltage



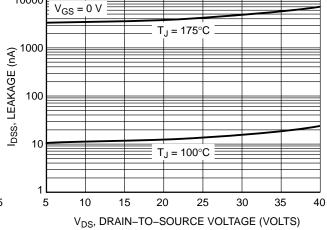
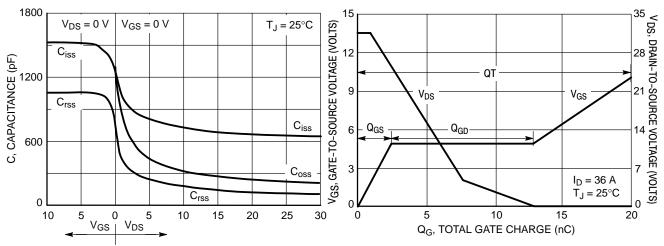


Figure 5. On–Resistance Variation with Temperature

Figure 6. Drain-to-Source Leakage Current vs. Voltage

10000

TYPICAL PERFORMANCE CURVES



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-To-Source and Drain-To-Source Voltage vs. Total Charge

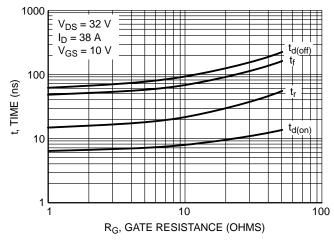


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

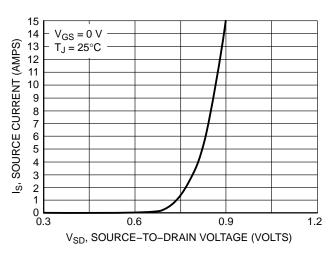


Figure 10. Diode Forward Voltage vs. Current

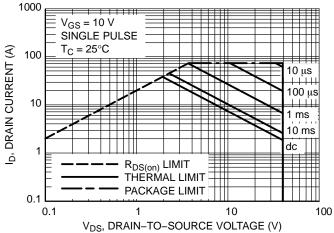
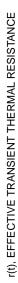


Figure 11. Maximum Rated Forward Biased Safe Operating Area

TYPICAL PERFORMANCE CURVES



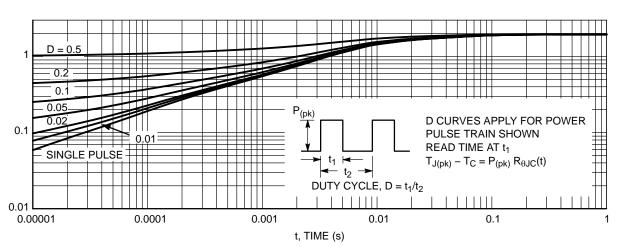
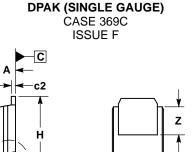


Figure 12. Thermal Response

PACKAGE DIMENSIONS



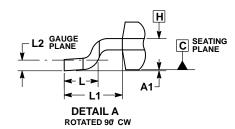
е ⊕ 0.005 (0.13) M C **TOP VIEW**

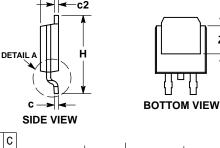
NOTE 7

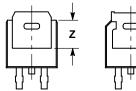
A

L3

В

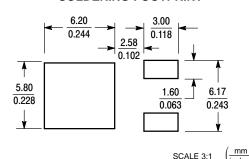






BOTTOM VIEW ALTERNATE CONSTRUCTIONS

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: INCHES.
 3. THERMAL PAD CONTOUR OPTIONAL WITHIN DI-MENSIONS b3, L3 and Z.

 4. DIMENSIONS D AND E DO NOT INCLUDE MOLD
- FLASH, PROTRUSIONS, OR BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.006 INCHES PER SIDE
- DIMENSIONS D AND E ARE DETERMINED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY.
- DATUMS A AND B ARE DETERMINED AT DATUM PLANE H.
- OPTIONAL MOLD FEATURE

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.086	0.094	2.18	2.38
A1	0.000	0.005	0.00	0.13
b	0.025	0.035	0.63	0.89
b2	0.028	0.045	0.72	1.14
b3	0.180	0.215	4.57	5.46
С	0.018	0.024	0.46	0.61
c2	0.018	0.024	0.46	0.61
D	0.235	0.245	5.97	6.22
Е	0.250	0.265	6.35	6.73
е	0.090 BSC		2.29	BSC
Н	0.370	0.410	9.40	10.41
L	0.055	0.070	1.40	1.78
L1	0.114 REF		F 2.90 REF	
L2	0.020	BSC	0.51	BSC
L3	0.035	0.050	0.89	1.27
L4	-	0.040		1.01
Z	0.155		3.93	

STYLE 2:

- PIN 1. GATE 2. DRAIN 3. SOURCE 4. DRAIN

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