

DS3695N Datasheet

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DS3695N

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DiGi Electronics Part Number	DS3695N-DG
Manufacturer	Texas Instruments
Manufacturer Product Number	DS3695N
Description	IC TRANSCEIVER HALF 1/1 8DIP
Detailed Description	1/1 Transceiver Half RS422, RS485 8-PDIP



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Purchase and inquiry

Manufacturer Product Number:

DS3695N

Series:

-

Type:

Transceiver

Number of Drivers/Receivers:

1/1

Receiver Hysteresis:

70 mV

Voltage - Supply:

4.75V ~ 5.25V

Mounting Type:

Through Hole

Supplier Device Package:

8-PDIP

Manufacturer:

Texas Instruments

Product Status:

Obsolete

Protocol:

RS422, RS485

Duplex:

Half

Data Rate:

-

Operating Temperature:

0°C ~ 70°C

Package / Case:

8-DIP (0.300", 7.62mm)

Base Product Number:

DS3695

Environmental & Export classification

RoHS Status:

RoHS non-compliant

REACH Status:

REACH Unaffected

HTSUS:

8542.39.0001

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

Multipoint RS485/RS422 Transceivers/Repeaters

Check for Samples: [DS3695](#), [DS3695T](#), [DS3696](#), [DS3697](#)

FEATURES

- Meets EIA standard RS485 for Multipoint Bus Transmission and is Compatible with RS-422
- 15 ns Driver Propagation Delays with 2 ns Skew (Typical)
- Single +5V supply
- -7V to +12V Bus Common Mode Range Permits $\pm 7V$ Ground Difference Between Devices on the Bus
- Thermal Shutdown Protection
- High Impedance to Bus with Driver in TRI-STATE or with Power Off, Over the Entire Common Mode Range Allows the Unused Devices on the Bus to be Powered Down
- Combined Impedance of a Driver Output and Receiver Input is Less than one RS485 Unit Load, Allowing up to 32 Transceivers on the Bus
- 70 mV Typical Receiver Hysteresis

DESCRIPTION

The DS3695, DS3696, and DS3697 are high speed differential TRI-STATE bus/line transceivers/repeaters designed to meet the requirements of EIA standard RS485 with extended common mode range (+12V to -7V), for multipoint data transmission.

The driver and receiver outputs feature TRI-STATE capability. The driver outputs remain in TRI-STATE over the entire common mode range of +12V to -7V. Bus faults that cause excessive power dissipation within the device trigger a thermal shutdown circuit, which forces the driver outputs into the high impedance state. The DS3696 provides an output pin TS (thermal shutdown) which reports the occurrence of the thermal shutdown of the device. This is an "open collector" pin with an internal 10 k Ω pull-up resistor. This allows the line fault outputs of several devices to be wire OR-ed.

Both AC and DC specifications are specified over the 0°C to 70°C temperature and 4.75V to 5.25V supply voltage range.

Connection and Logic Diagrams

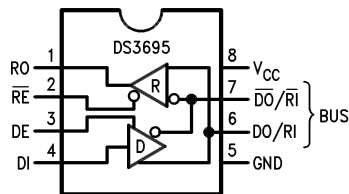


Figure 1. PDIP (Top View)
See Package Number P (R-PDIP-T8)

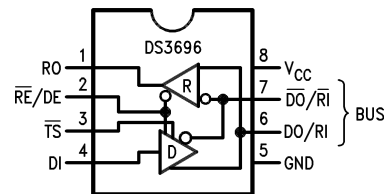


Figure 2. PDIP (Top View)
See Package Number P (R-PDIP-T8)

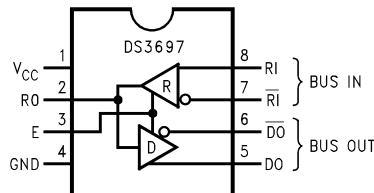


Figure 3. PDIP (Top View)
See Package Number P (R-PDIP-T8)

\overline{TS} pin was \overline{LF} (Line Fault) in previous data sheets and reports the occurrence of a thermal shutdown of the device.



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These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.

Absolute Maximum Ratings ⁽¹⁾⁽²⁾

	VALUE	UNIT
Supply Voltage, V_{CC}	7	V
Control Input Voltages	7	V
Driver Input Voltage	7	V
Driver Output Voltages	+15/-10	V
Receiver Input Voltages (DS3695, DS3696)	+15/-10	V
Receiver Common Mode Voltage (DS3697)	± 25	V
Receiver Output Voltage	5.5	V
Continuous Power Dissipation @ 25°C - N Package ⁽³⁾	1.07	W
Storage Temperature Range	-65 to +150	°C
Lead Temperature (Soldering, 4 sec.)	260	°C

- (1) "Absolute Maximum Ratings" are those beyond which the safety of the device cannot be verified. They are not meant to imply that the device should be operated at these limits. The tables of "Electrical Characteristics" provide conditions for actual device operation.
- (2) If Military/Aerospace specified devices are required, please contact the TI Sales Office/ Distributors for availability and specifications.
- (3) All typicals are given for $V_{CC} = 5V$ and $T_A = 25^\circ C$.

Recommended Operating Conditions

		Min	Max	Units
Supply Voltage, V_{CC}		4.75	5.25	V
Bus Voltage		-7	+12	V
Operating Free Air Temp. (T_A)	Commercial	0	+70	°C
	Industrial	-40	+85	°C

Electrical Characteristics ⁽¹⁾⁽²⁾

0°C $\leq T_A \leq +70^\circ C$, 4.75V $< V_{CC} < 5.25V$ unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units			
V_{OD1}	Differential Driver Output Voltage (Unloaded)	$I_O = 0$			5	V			
V_{OD2}	Differential Driver Output Voltage (with Load)	See Figure 4	R = 50Ω; (RS-422) ⁽³⁾			V			
						R = 27Ω; (RS-485)	1.5		V
ΔV_{OD}	Change in Magnitude of Driver Differential Output Voltage for Complementary Output States	See Figure 4	R = 27Ω			0.2			
V_{OC}	Driver Common Mode Output Voltage							3.0	V
$\Delta V_{OC} $	Change in Magnitude of Driver Common Mode Output Voltage for Complementary Output States							0.2	V
V_{IH}	Input High Voltage	DI, DE, \overline{RE} , E, \overline{RE}/DE				2			
V_{IL}	Input Low Voltage							0.8	V
V_{CL}	Input Clamp Voltage					$I_{IN} = -18$ mA			
I_{IL}	Input Low Current					$V_{IL} = 0.4V$			
I_{IH}	Input High Current					$V_{IH} = 2.4V$			
I_{IN}	Input Current	DO/RI, $\overline{DO}/\overline{RI}$ RI, \overline{RI}	$V_{CC} = 0V$ or 5.25V \overline{RE}/DE or $DE = 0V$	$V_{IN} = 12V$		+1.0			
						$V_{IN} = -7V$			-0.8

- (1) All currents into device pins are positive; all currents out of device pins are negative. All voltages are referenced to device ground unless otherwise specified.
- (2) All typicals are given for $V_{CC} = 5V$ and $T_A = 25^\circ C$.
- (3) All limits for which derate linearly at 11.1 mW/°C to 570 mW at 70°C is applied must be derated by 10% for DS3695T and DS3696T. Other parameters remain the same for this extended temperature range device (-40°C $\leq T_A \leq +85^\circ C$).

Electrical Characteristics ⁽¹⁾⁽²⁾ (continued)0°C ≤ T_A ≤ +70°C, 4.75V < V_{CC} < 5.25V unless otherwise specified

Symbol	Parameter	Conditions	Min	Typ	Max	Units	
I _{OZD}	TRI-STATE Current DS3697 & DS3698	DO, \overline{DO}	V _{CC} = 0V or 5.25V, E = 0V -7V < V _O < +12V		±100	μA	
V _{TH}	Differential Input Threshold Voltage for Receiver		-7V ≤ V _{CM} ≤ +12V		-0.2	+0.2	V
ΔV _{TH}	Receiver Input Hysteresis		V _{CM} = 0V			70	mV
V _{OH}	Receiver Output High Voltage		I _{OH} = -400 μA		2.4		V
V _{OL}	Output Low Voltage	RO	I _{OL} = 16 mA ⁽³⁾			0.5	V
		\overline{TS}	I _{OL} = 8 mA			0.45	V
I _{OZR}	OFF-State (High Impedance) Output Current at Receiver		V _{CC} = Max 0.4V ≤ V _O ≤ 2.4V			±20	μA
R _{IN}	Receiver Input Resistance		-7V ≤ V _{CM} ≤ +12V		12		kΩ
I _{CC}	Supply Current	No Load ⁽³⁾	Driver Outputs Enabled		42	60	mA
			Driver Outputs Disabled		27	40	mA
		Driver Short-Circuit Output Current	V _O = -7V ⁽³⁾				-250
		V _O = +12V ⁽³⁾				+250	mA
I _{OSR}	Receiver Short-Circuit Output Current	V _O = 0V		-15		-85	mA

Receiver Switching Characteristics ⁽¹⁾⁽²⁾0°C ≤ T_A ≤ +70°C, 4.75V < V_{CC} < 5.25V unless otherwise specified (Figure 5, Figure 6, Figure 7)

Symbol	Conditions	Min	Typ	Max	Units
t _{PLH}	C _L = 15 pF	15	25	37	ns
t _{PHL}	S1 and S2	15	25	37	ns
t _{PLH} - t _{PHL}	Closed	0			ns
t _{PLZ}	C _L = 15 pF, S2 Open	5	12	16	ns
t _{PHZ}	C _L = 15 pF, S1 Open	5	12	16	ns
t _{PZL}	C _L = 15 pF, S2 Open	7	15	20	ns
t _{PZH}	C _L = 15 pF, S1 Open	7	15	20	ns

(1) All typicals are given for V_{CC} = 5V and T_A = 25°C.

(2) Switching Characteristics apply for DS3695, DS3695T, DS3696, DS3697 only.

Driver Switching Characteristics0°C ≤ T_A ≤ +70°C, 4.75V < V_{CC} < 5.25V unless otherwise specified

Symbol	Conditions	Min	Typ	Max	Units
SINGLE ENDED CHARACTERISTICS (Figure 8, Figure 9, Figure 10)					
t _{PLH}	R _L DIFF = 60Ω	9	15	22	ns
t _{PHL}	C _{L1} = C _{L2} = 100 pF	9	15	22	ns
t _{SKEW} t _{PLH} - t _{PHL}			2	8	ns
t _{PLZ}	C _L = 15 pF, S2 Open	7	15	30	ns
t _{PHZ}	C _L = 15 pF, S1 Open	7	15	30	ns
t _{PZL}	C _L = 100 pF, S2 Open	30	35	50	ns
t _{PZH}	C _L = 100 pF, S1 Open	30	35	50	ns
DIFFERENTIAL CHARACTERISTICS (Figure 8 Figure 11)					
t _r , t _f	R _L DIFF = 60Ω C _{L1} = C _{L2} = 100 pF	6	10	18	ns

AC Test Circuits and Switching Waveforms

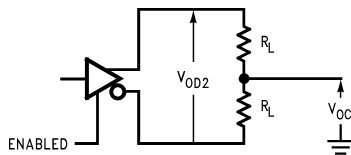


Figure 4. Driver V_{OD} and V_{OC}

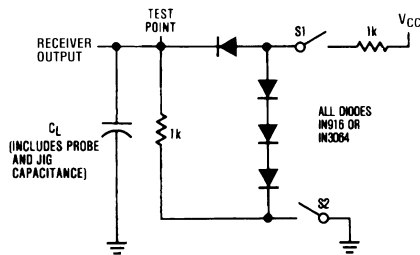
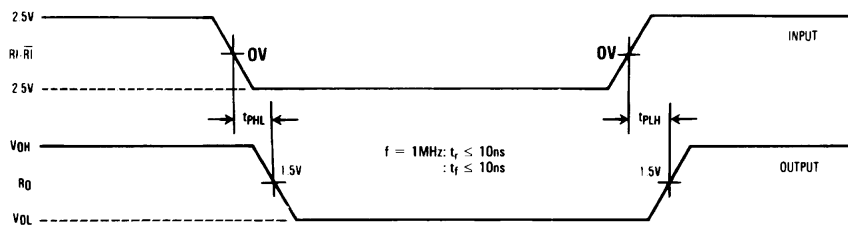


Figure 5. Receiver Propagation Delay Test Circuit



Note: Differential input voltage may be realized by grounding \overline{RI} and pulsing RI between +2.5V and -2.5V.

Figure 6. Receiver Input-to-Output Propagation Delay Timing

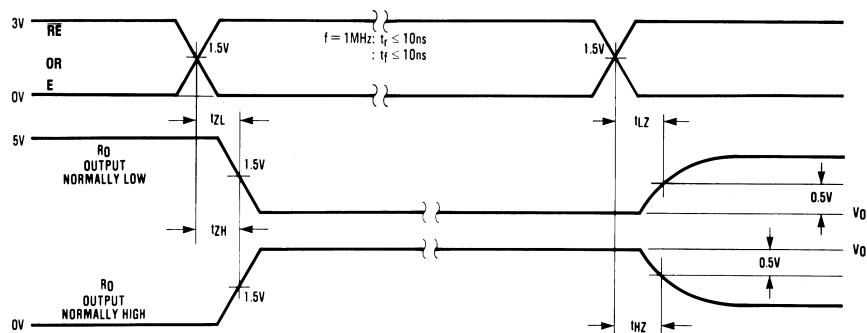
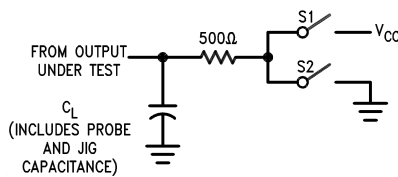


Figure 7. Receiver Enable/Disable Propagation Delay Timing



Note: Unless otherwise specified the switches are closed.

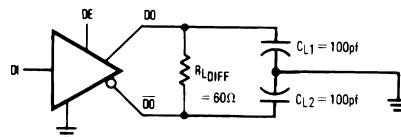
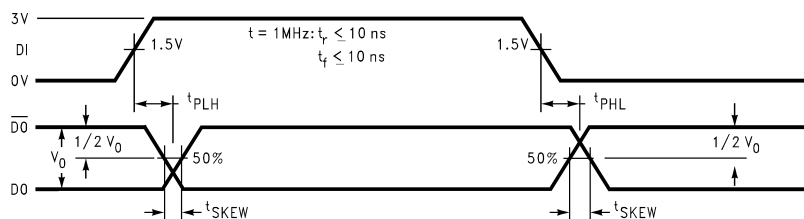


Figure 8. Driver Propagation Delay and Transition Time Test Circuits



Note: t_{PLH} and t_{PHL} are measured to the respective 50% points. t_{SKEW} is the difference between propagation delays of the complementary outputs.

Figure 9. Driver Input-to-Output Propagation Delay Timing (Single-Ended)

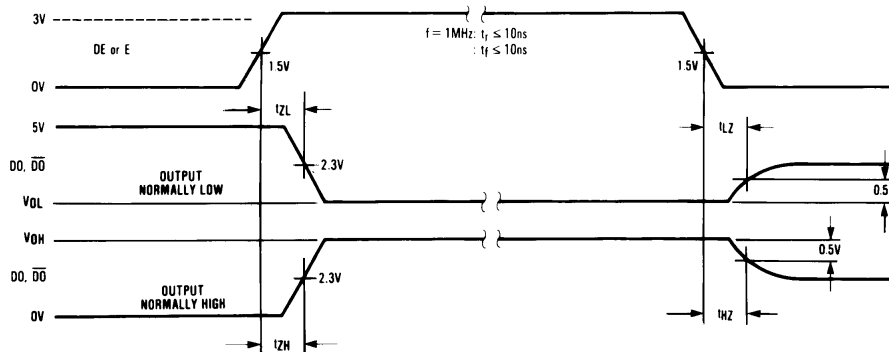


Figure 10. Driver Enable/Disable Propagation Delay Timing

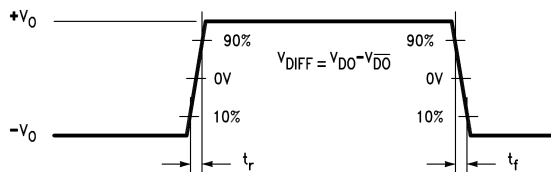


Figure 11. Driver Differential Transition Timing

Function Tables

Table 1. DS3695/DS3696 Transmitting⁽¹⁾

Inputs			Thermal Shutdown	Outputs		
\overline{RE}	DE	DI		\overline{DO}	DO	\overline{TS}^* (DS3696 Only)
X	1	1	OFF	0	1	H
X	1	0	OFF	1	0	H
X	0	X	OFF	Z	Z	H
X	1	X	ON	Z	Z	L

(1) X—Don't care condition

Z—High impedance state

* \overline{TS} is an "open collector" output with an on-chip 10 k Ω pull-up resistor that reports the occurrence of a thermal shutdown of the device.
Table 2. DS3695/DS3696 Receiving⁽¹⁾

Inputs			Outputs	
\overline{RE}	DE	RI- \overline{RI}	RO	\overline{TS}^* (DS3696 Only)
0	0	$\geq +0.2V$	1	H
0	0	$\leq -0.2V$	0	H
1	0	X	Z	H

(1) X—Don't care condition

Z—High impedance state

* \overline{TS} is an "open collector" output with an on-chip 10 k Ω pull-up resistor that reports the occurrence of a thermal shutdown of the device.
Table 3. DS3697⁽¹⁾

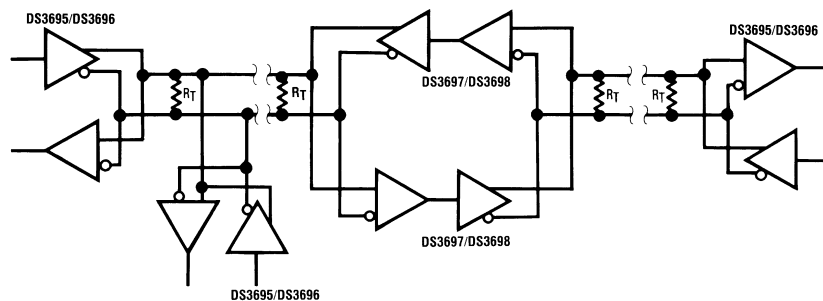
Inputs		Thermal Shutdown	Outputs		
E	RI- \overline{RI}		\overline{DO}	DO	RO (DS3697 Only)
1	$\geq +0.2V$	OFF	0	1	1
1	$\leq -0.2V$	OFF	1	0	0
0	X	OFF	Z	Z	Z
1	$\geq +0.2V$	ON	Z	Z	1
1	$\leq -0.2V$	ON	Z	Z	0

(1) X—Don't care condition

Z—High impedance state

* \overline{TS} is an "open collector" output with an on-chip 10 k Ω pull-up resistor that reports the occurrence of a thermal shutdown of the device.

Typical Application



Note: Repeater control logic not shown

Typical Performance Characteristics

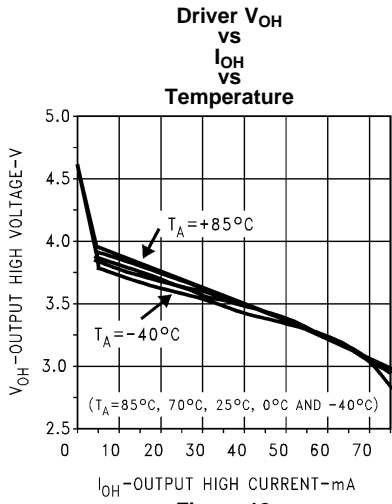


Figure 12.

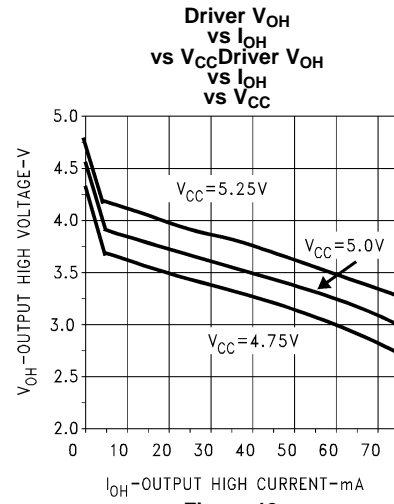


Figure 13.

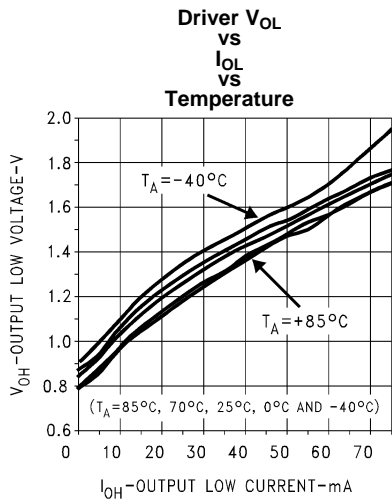


Figure 14.

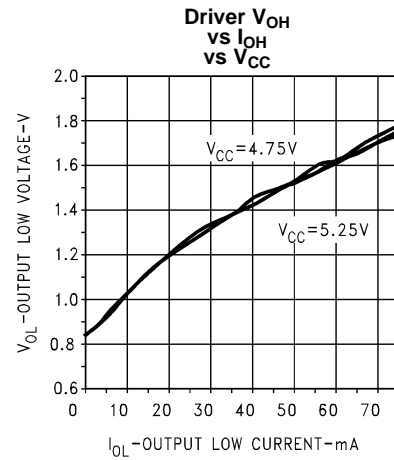


Figure 15.

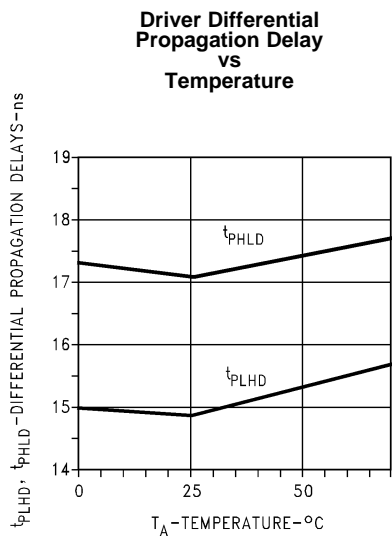


Figure 16.

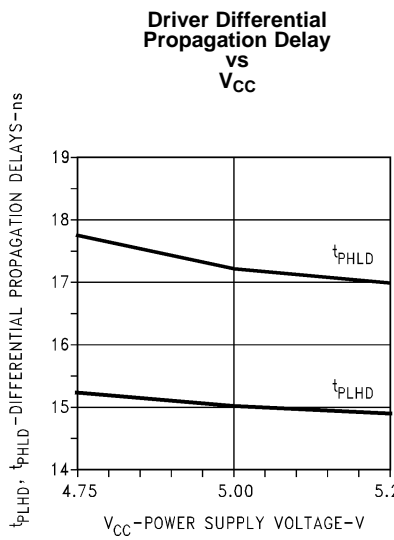


Figure 17.

Typical Performance Characteristics (continued)

Driver Single-Ended Propagation Delay vs Temperature

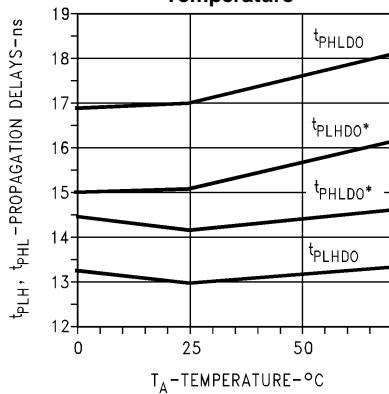


Figure 18.

Driver Single-Ended Propagation Delay vs VCC

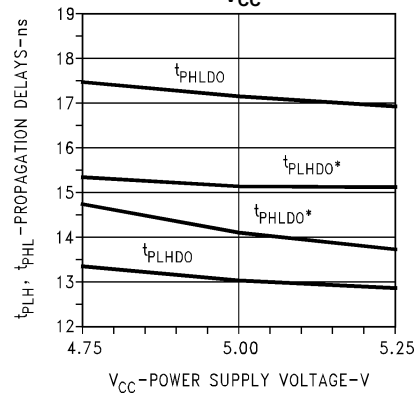


Figure 19.

Driver Transition Time vs Temperature

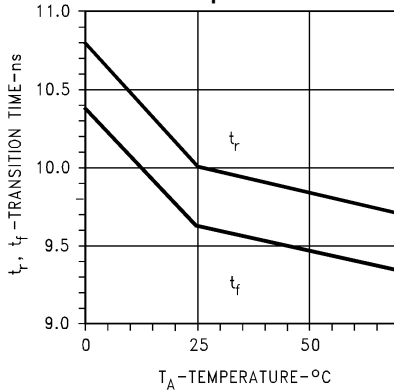


Figure 20.

Driver Transition Time vs VCC

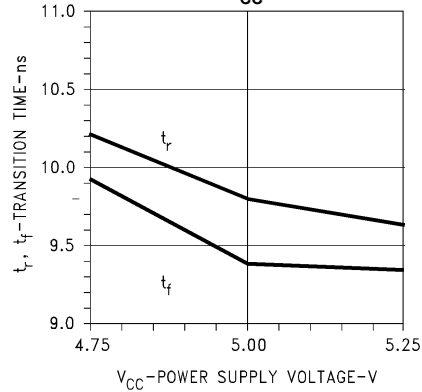


Figure 21.

Cable Length vs Data Rate

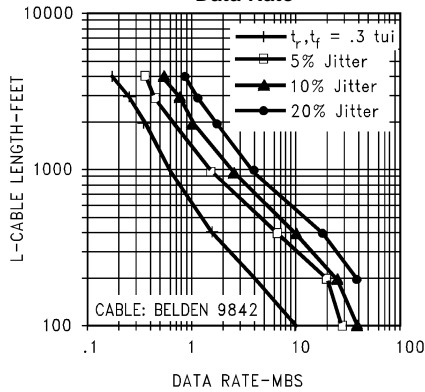


Figure 22.

Supply Current vs Temperature

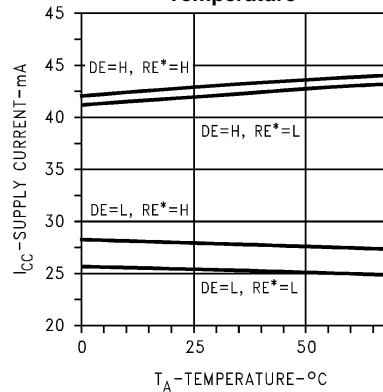
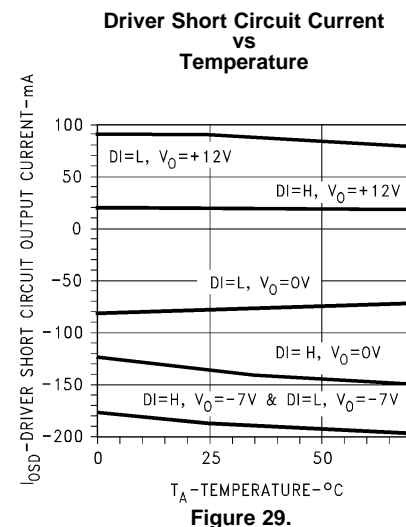
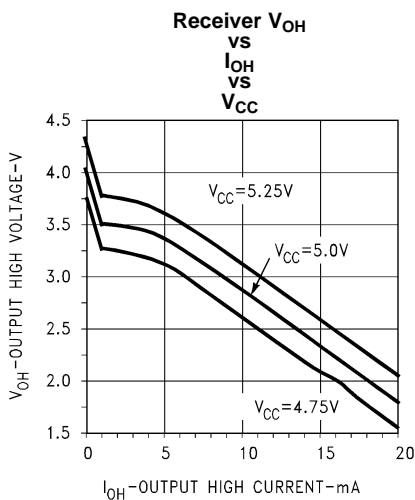
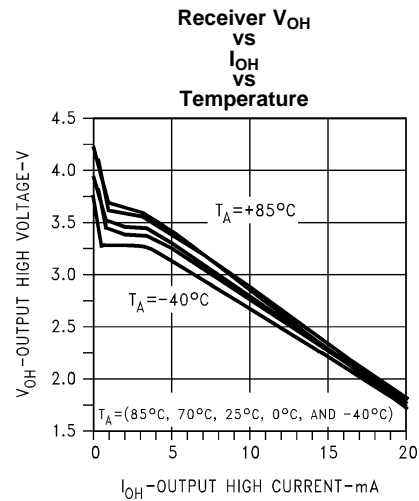
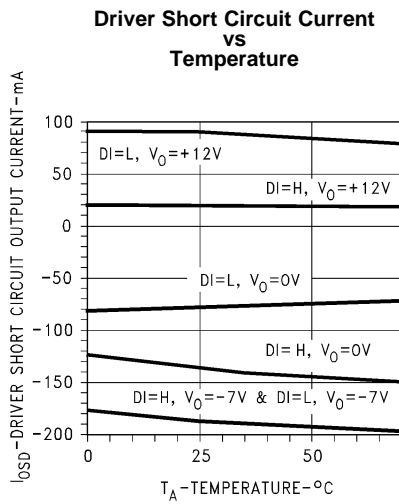
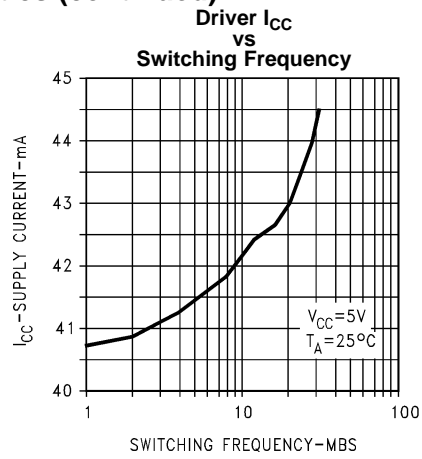
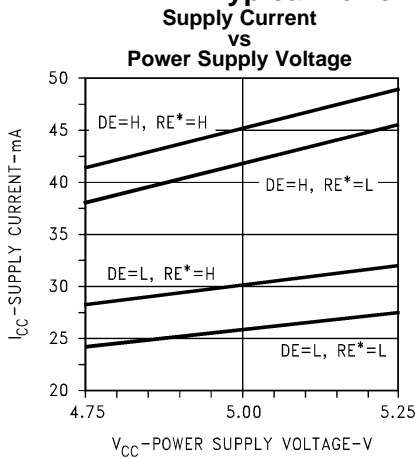
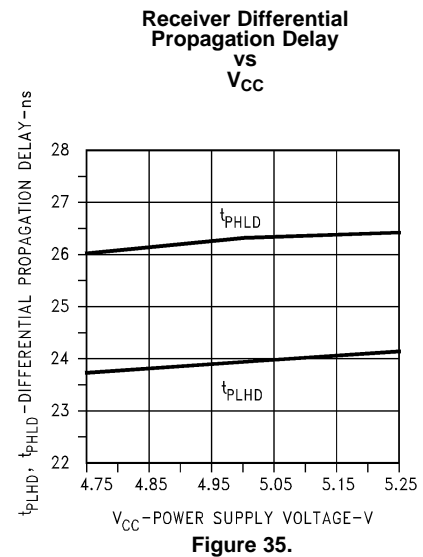
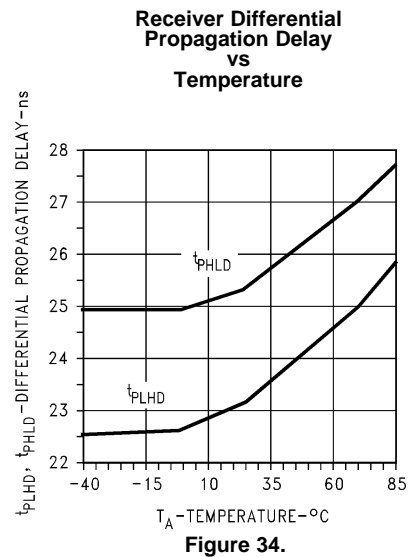
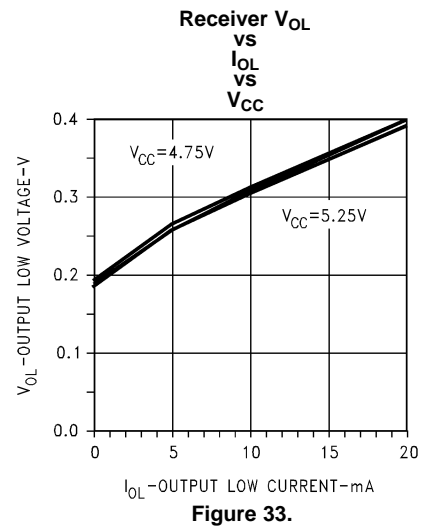
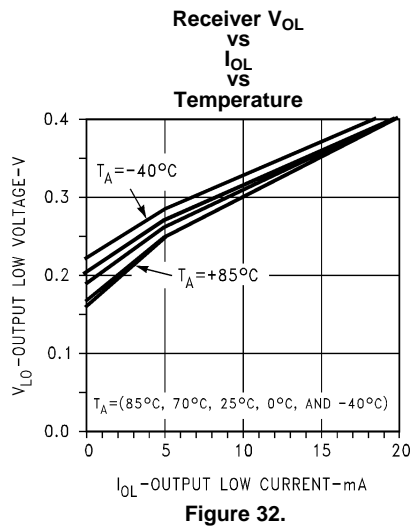
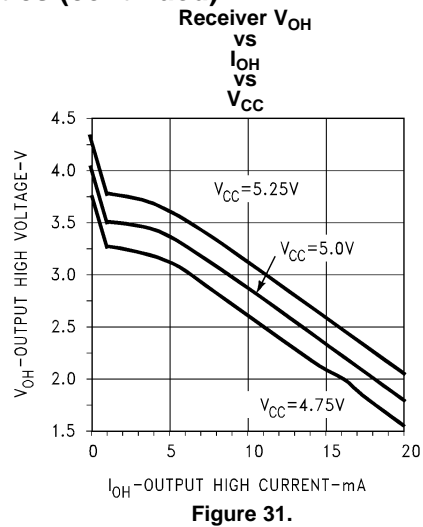
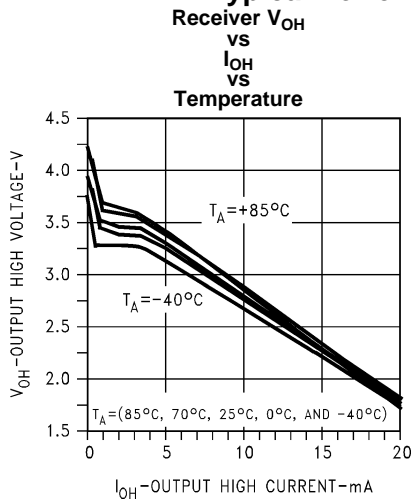


Figure 23.

Typical Performance Characteristics (continued)



Typical Performance Characteristics (continued)



Typical Performance Characteristics (continued)

Receiver Short Circuit Current vs Temperature

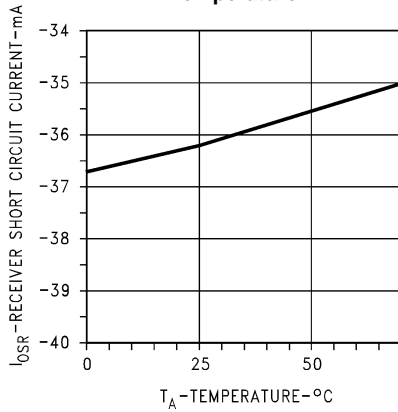


Figure 36.

Receiver Short Circuit Current vs Power Supply

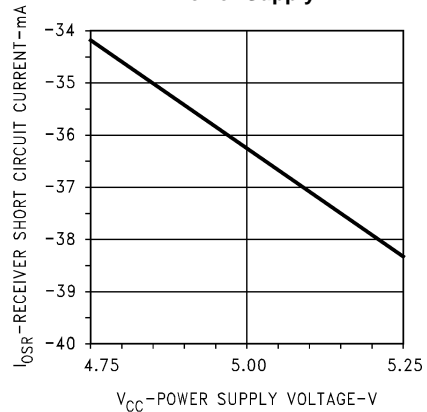


Figure 37.

Receiver Non-Inverting Input Current vs Temperature

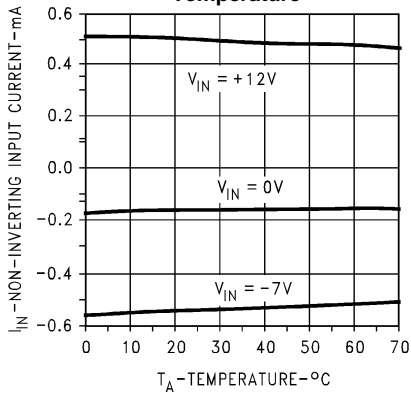


Figure 38.

Receiver Non-Inverting Input Current vs Power Supply Voltage

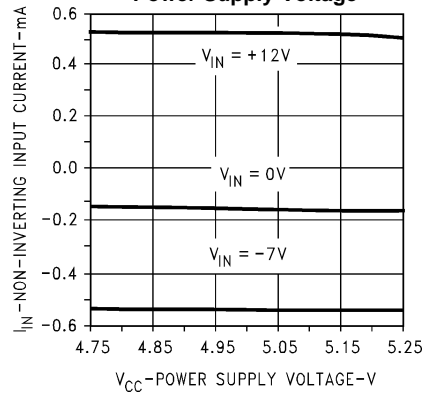


Figure 39.

Receiver Inverting Input Current vs Temperature

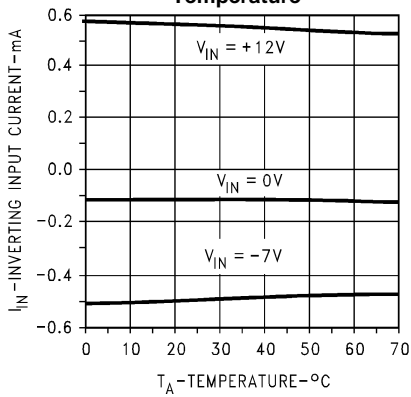


Figure 40.

Receiver Inverting Input Current vs Power Supply Voltage

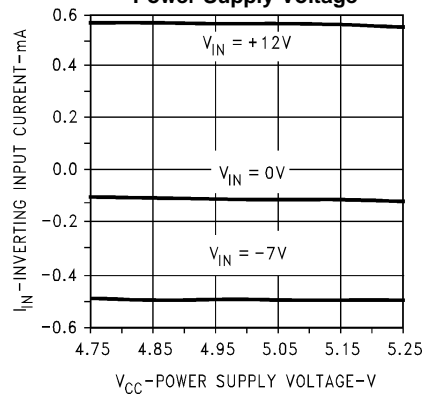
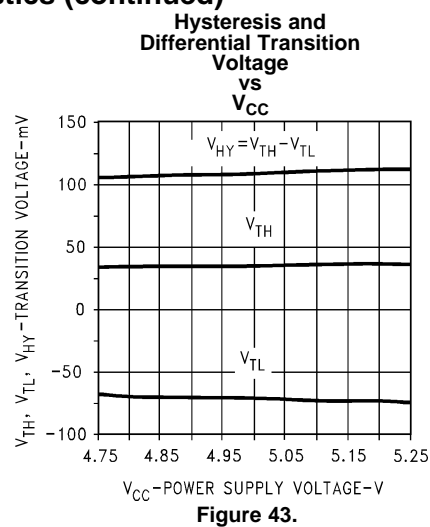
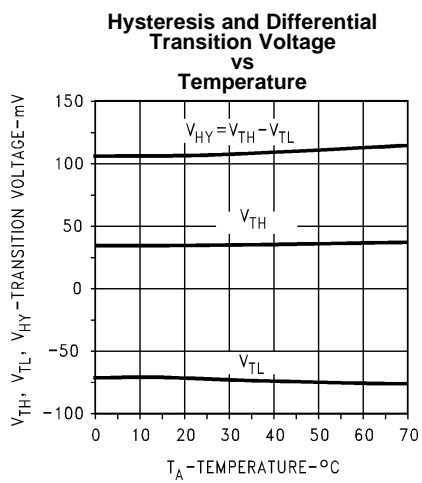


Figure 41.

Typical Performance Characteristics (continued)



REVISION HISTORY

Changes from Revision B (April 2013) to Revision C	Page
• Changed layout of National Data Sheet to TI format	11

PACKAGING INFORMATION

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
DS3695N/NOPB	LIFEBUY	PDIP	P	8	40	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	0 to 70	DS3695N	
DS3695TN/NOPB	LIFEBUY	PDIP	P	8	40	Green (RoHS & no Sb/Br)	CU SN	Level-1-NA-UNLIM	-40 to 85	DS 3695TN	

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) **RoHS:** TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

Green: TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "-" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

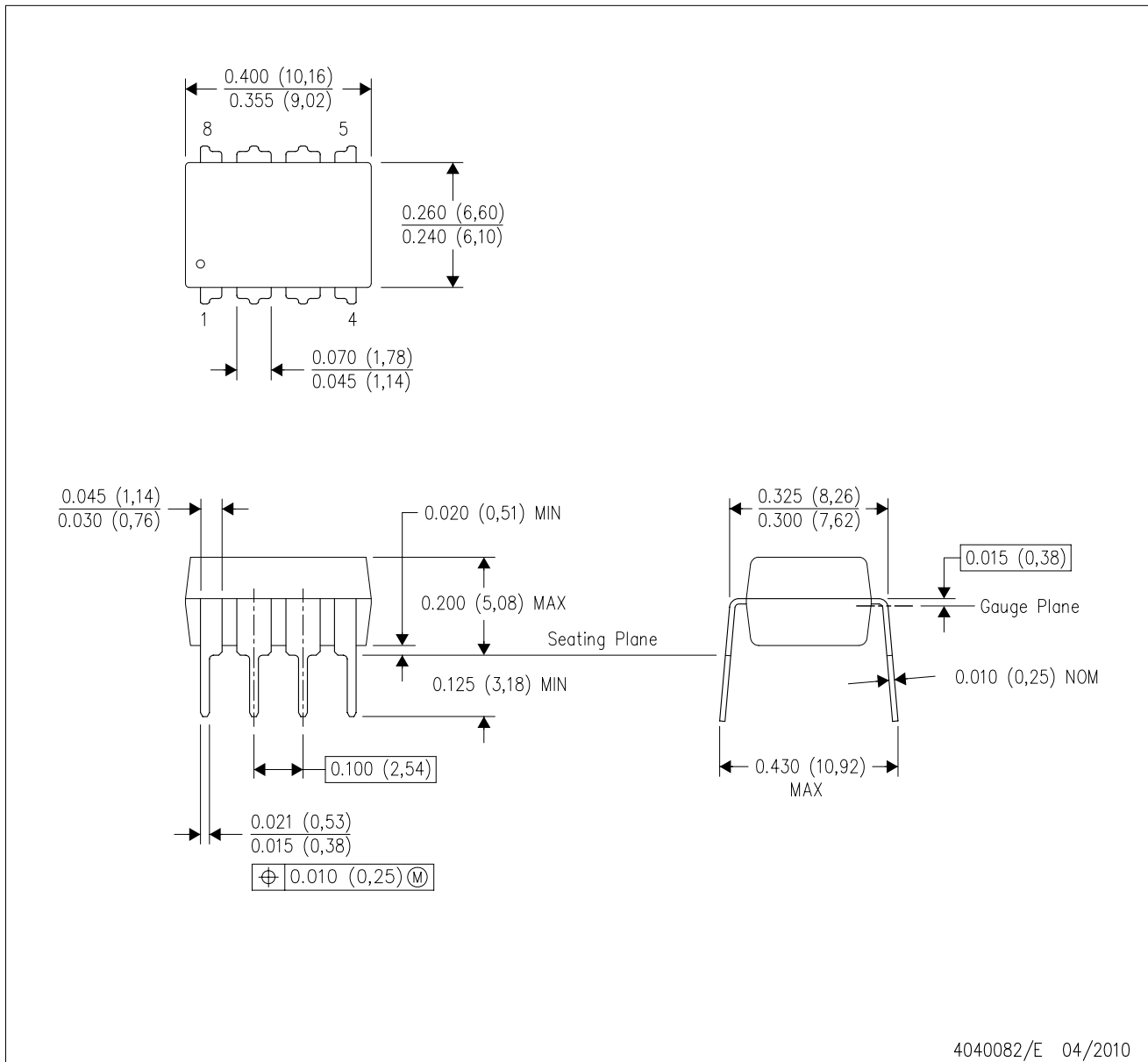
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MECHANICAL DATA

P (R-PDIP-T8)

PLASTIC DUAL-IN-LINE PACKAGE



- NOTES:
- All linear dimensions are in inches (millimeters).
 - This drawing is subject to change without notice.
 - Falls within JEDEC MS-001 variation BA.

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