

7UL1G17FU,LF Datasheet



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DiGi Electronics Part Number 7UL1G17FU,LF-DG

Manufacturer Toshiba Semiconductor and Storage

Manufacturer Product Number 7UL1G17FU,LF

Description IC BUFFER NON-INVERT 3.6V USV

Detailed Description Buffer, Non-Inverting 1 Element 1 Bit per Element 0

utput USV



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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
7UL1G17FU,LF	Toshiba Semiconductor and Storage
Series:	Product Status:
	Active
Logic Type:	Number of Elements:
Buffer, Non-Inverting	1
Number of Bits per Element:	Input Type:
1	Schmitt Trigger
Output Type:	Current - Output High, Low:
	8mA, 8mA
Voltage - Supply:	Operating Temperature:
0.9V ~ 3.6V	-40°C ~ 125°C (TA)
Mounting Type:	Package / Case:
Surface Mount	5-TSSOP, SC-70-5, SOT-353
Supplier Device Package:	Base Product Number:
USV	7UL1G17

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
ECCN:	HTSUS:
EAR99	8542.39.0001



CMOS Digital Integrated Circuits Silicon Monolithic

7UL1G17FU

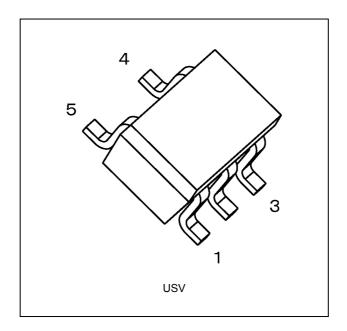
1. Functional Description

· Schmitt Buffer

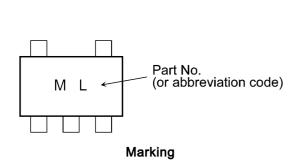
2. Features

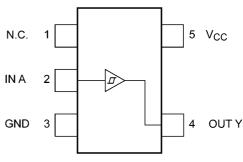
- (1) Wide operating temperature range: $T_{opr} = -40$ to 125 °C
- (2) High output current: ± 8.0 mA (min) at $V_{CC} = 3.0$ V
- (3) Super high speed operation: $t_{pd} = 3.0$ ns (typ.) at $V_{CC} = 3.3$ V, $C_L = 15$ pF
- (4) Operating voltage range: $V_{CC} = 0.9$ to 3.6 V
- (5) 3.6 V tolerant input
- (6) 3.6 V power down protection output

3. Packaging



4. Marking and Pin Assignment





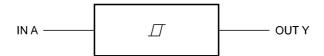
Pin Assignment (Top view)

Start of commercial production

2021-04



5. IEC Logic Symbol



6. Truth Table

Input A	Output Y
L	L
Н	Н

7. Absolute Maximum Ratings (Note) (Unless otherwise specified, T_a = 25 °C)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 4.6	V
Input voltage	V _{IN}		-0.5 to 4.6	٧
DC output voltage	V _{OUT}	(Note 1)	-0.5 to 4.6	٧
		(Note 2)	-0.5 to V _{CC} + 0.5	
Input diode current	I _{IK}		-20	mA
Output diode current	I _{OK}	(Note 3)	-20	mA
DC output current	l _{out}		±25	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	P_D		200	mW
Storage temperature	T _{stg}		-65 to 150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High (H) or Low (L) state. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND



8. Operating Ranges (Note)

Characteristics	Symbol	Note	Test Condition	Rating	Unit
Supply voltage	V _{CC}		_	0.9 to 3.6	V
Input voltage	V _{IN}		_	0 to 3.6	V
Output voltage	V _{OUT}	(Note 1)	_	0 to 3.6	V
		(Note 2)	_	0 to V _{CC}	
Output current	I _{OH} ,I _{OL}		V _{CC} = 3.0 to 3.6 V	±8.0	mA
			V _{CC} = 2.3 to 2.7 V	±4.0	
			V _{CC} = 1.65 to 1.95 V	±3.0	
			V _{CC} = 1.4 to 1.6 V	±1.7	
			V _{CC} = 1.1 to 1.3 V	±0.3	
			V _{CC} = 0.9 V	±0.02	
Operating temperature	T _{opr}		_	-40 to 125	°C

Note: The operating ranges must be maintained to ensure the normal operation of the device.

Unused inputs must be tied to either $V_{\mbox{\footnotesize CC}}$ or GND.

Note 1: $V_{CC} = 0 V$

Note 2: High (H) or Low (L) state.





9. Electrical Characteristics

9.1. DC Characteristics (Unless otherwise specified, $T_a = 25$ °C)

Characteristics	Symbol	Test Condition	1	V _{CC} (V)	Min	Тур.	Max	Unit
Positive threshold voltage	V _P	_		0.9	_	_	0.73	V
				1.1	_		0.86	
				1.4	_	_	1.07	
				1.65	_	_	1.23	
				2.3	_		1.66	
				3.0	_	_	2.14	
Negative threshold	V _N	_		0.9	0.18	_	_	V
voltage				1.1	0.26	_	_	
				1.4	0.36	_	_	
				1.65	0.45		_	
				2.3	0.69	_	_	
				3.0	0.96	_	_	
Hysteresis voltage	V _H	_		0.9	0.15	_	0.38	V
				1.1	0.18		0.41	
				1.4	0.20		0.48	
				1.65	0.22		0.60	
				2.3	0.35		0.76	
				3.0	0.45	_	0.93	
High-level output voltage	V _{OH}	$V_{IN} = V_{IH}$	I _{OH} = -0.02 mA	0.9	0.75		_	V
			I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75		_	
			I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	_	
			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0		_	
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 0.02 mA	0.9	_		0.1	V
			I _{OL} = 0.3 mA	1.1 to 1.3	_		$V_{CC} \times 0.25$	
			I _{OL} = 1.7 mA	1.4 to 1.6	_	_	$V_{CC} \times 0.25$	
			I _{OL} = 3.0 mA	1.65 to 1.95	_		0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	_		0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	_		0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V	•	0 to 3.6	_		±0.1	μА
Power-OFF leakage current	I _{OFF}	V _{IN} = 0 to 3.6 V, V _{OUT} = 0 to 3.6 V		0	_	_	1.0	μА
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		3.6	_	_	1.0	μА



9.2. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C)

Characteristics	Symbol	Test Conditi	on	V _{CC} (V)	Min	Max	Unit
Positive threshold voltage	V _P	_	0.9	_	0.73	V	
				1.1	_	0.86	
				1.4	_	1.07	
				1.65	_	1.23	
				2.3	_	1.66	
				3.0	_	2.14	
Negative threshold voltage	V _N	_		0.9	0.18	_	V
				1.1	0.26	_	
				1.4	0.36	_	
				1.65	0.45	_	
				2.3	0.69	_	
				3.0	0.96	_	
Hysteresis voltage	V _H	_		0.9	0.15	0.38	V
				1.1	0.18	0.41	
				1.4	0.20	0.48	
				1.65	0.22	0.60	
				2.3	0.35	0.76	
				3.0	0.45	0.93	
High-level output voltage	V _{OH}	V _{IN} = V _{IH}	I _{OH} = -0.02 mA	0.9	0.75	_	V
			I _{OH} = -0.3 mA	1.1 to 1.3	V _{CC} × 0.75	_	
			I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.75	_	
			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.45	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	2.0	_	
			I _{OH} = -8.0 mA	3.0 to 3.6	2.48	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 0.02 mA	0.9	_	0.1	V
			I _{OL} = 0.3 mA	1.1 to 1.3	_	V _{CC} × 0.25	
			I _{OL} = 1.7 mA	1.4 to 1.6	_	V _{CC} × 0.25	
			I _{OL} = 3.0 mA	1.65 to 1.95	_	0.45	
			I _{OL} = 4.0 mA	2.3 to 2.7	_	0.4	
			I _{OL} = 8.0 mA	3.0 to 3.6	_	0.4	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		0 to 3.6	_	±0.5	μА
Power-OFF leakage current	I _{OFF}	V _{IN} = 0 to 3.6 V, V _{OUT} = 0 to 3.6 V		0	_	10.0	μА
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		3.6		10.0	μΑ



9.3. DC Characteristics (Unless otherwise specified, T_a = -40 to 125 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
Positive threshold voltage	V _P	_	0.9	_	0.73	V	
				1.1	_	0.86	
				1.4	_	1.07	
				1.65	_	1.23	
				2.3	_	1.66	
				3.0	_	2.14	V
Negative threshold voltage	V _N	_		0.9	0.18	_	
				1.1	0.26	_	
				1.4	0.36	_	
				1.65	0.45	_	
				2.3	0.69	_	
				3.0	0.96	_	
Hysteresis voltage	V _H	_		0.9	0.15	0.38	V
				1.1	0.18	0.41	
				1.4	0.20	0.48	
				1.65	0.22	0.60	
				2.3	0.35	0.76	
				3.0	0.45	0.93	
High-level output voltage	V _{OH}	V _{IN} = V _{IH}	I _{OH} = -0.02 mA	0.9	0.75	_	V
			I _{OH} = -0.3 mA	1.1 to 1.3	$V_{CC} \times 0.73$	_	
			I _{OH} = -1.7 mA	1.4 to 1.6	V _{CC} × 0.73	_	
			I _{OH} = -3.0 mA	1.65 to 1.95	V _{CC} -0.5	_	
			I _{OH} = -4.0 mA	2.3 to 2.7	1.95	_	
			I _{OH} = -8.0 mA	3.0 to 3.6	2.4	_	
Low-level output voltage	V _{OL}	$V_{IN} = V_{IL}$	I _{OL} = 0.02 mA	0.9	_	0.1	V
			I _{OL} = 0.3 mA	1.1 to 1.3	_	V _{CC} × 0.27	
			I _{OL} = 1.7 mA	1.4 to 1.6	_	V _{CC} × 0.27	
			I _{OL} = 3.0 mA	1.65 to 1.95	_	0.5	
			I _{OL} = 4.0 mA	2.3 to 2.7	_	0.45	
			I _{OL} = 8.0 mA	3.0 to 3.6	_	0.45	
Input leakage current	I _{IN}	V _{IN} = 0 to 3.6 V		0 to 3.6	_	±2.0	μА
Power-OFF leakage current	I _{OFF}	V _{IN} = 0 to 3.6 V, V _{OUT} = 0 to 3.6 V		0	_	80.0	μА
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND		3.6		80.0	μΑ



9.4. AC Characteristics (Unless otherwise specified, $T_a = 25$ °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Unit			
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	0.9	10	_	22.0	_	ns			
			See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		_	11.1	22.6				
			Table 9.7.1	1.4 to 1.6		_	6.7	10.5				
				1.65 to 1.95		_	5.0	7.8				
				2.3 to 2.7		_	3.3	5.4				
				3.0 to 3.6		_	2.7	4.4				
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	0.9	15	_	24.2	_	ns			
			See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		_	12.1	25.1				
		Table 8	la la	labi	Table 9.7.1	Table 9.7.1	1.4 to 1.6		_	7.3	11.5	
				1.65 to 1.95		_	5.5	8.4				
				2.3 to 2.7		_	3.7	5.7				
				3.0 to 3.6		_	3.0	4.6				
Propagation delay time	t _{PLH} ,t _{PHL}		$R_L = 1 M\Omega$	0.9	30	_	31.0	_	ns			
			See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		_	15.7	35.7				
			Table 9.7.1	1.4 to 1.6		_	9.1	15.8				
				1.65 to 1.95		_	7.1	10.7				
		2.3 to 2.7		_	4.7	6.9						
				3.0 to 3.6		_	3.9	5.2				
Input capacitance	C _{IN}			3.6	_	1	3	1	pF			
Power dissipation capacitance	C _{PD}	(Note 1)	_	0.9 to 3.6	_	_	9	_	pF			

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

9.5. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 85 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	0.9	10	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	35.9	
		Table 9.7.1	1.4 to 1.6		1.0	11.3	
			1.65 to 1.95		1.0	8.2	
			2.3 to 2.7		1.0	5.8	
			3.0 to 3.6		1.0	4.6	
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	0.9	15	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	41.8	
		Table 9.7.1	1.4 to 1.6		1.0	12.6	
			1.65 to 1.95		1.0	8.7	
			2.3 to 2.7		1.0	6.1	
			3.0 to 3.6		1.0	5.0	
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	0.9	30	_	_	ns
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	58.1	
		Table 9.7.1	1.4 to 1.6		1.0	17.6	
			1.65 to 1.95		1.0	11.7	
			2.3 to 2.7		1.0	8.1	
			3.0 to 3.6		1.0	6.1	



9.6. AC Characteristics (Unless otherwise specified, $T_a = -40$ to 125 °C, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	C _L (pF)	Min	Max	Unit		
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	0.9	10	_	_	ns		
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	44.8			
			1.4 to 1.6]	1.0	11.9			
			1.65 to 1.95]	1.0	8.5			
			2.3 to 2.7		1.0	6.1			
			3.0 to 3.6]	1.0	4.8			
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	0.9	15	_	_	ns		
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3		1.0	53.0			
				14016 9.7.1	1.4 to 1.6		1.0	13.4	
			1.65 to 1.95]	1.0	8.9			
			2.3 to 2.7		1.0	6.4			
			3.0 to 3.6]	1.0	5.3			
Propagation delay time	t _{PLH} ,t _{PHL}	$R_L = 1 M\Omega$	0.9	30	_	_	ns		
		See Fig. 9.7.1, Table 9.7.1	1.1 to 1.3]	1.0	73.1			
		Table 9.7.1	1.4 to 1.6		1.0	18.8			
			1.65 to 1.95]	1.0	12.4			
			2.3 to 2.7		1.0	8.9			
			3.0 to 3.6		1.0	6.7			

9.7. AC Waveform

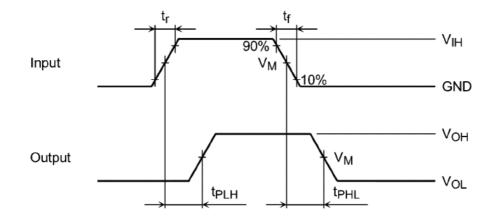


Fig. 9.7.1 t_{PLH}, t_{PHL}

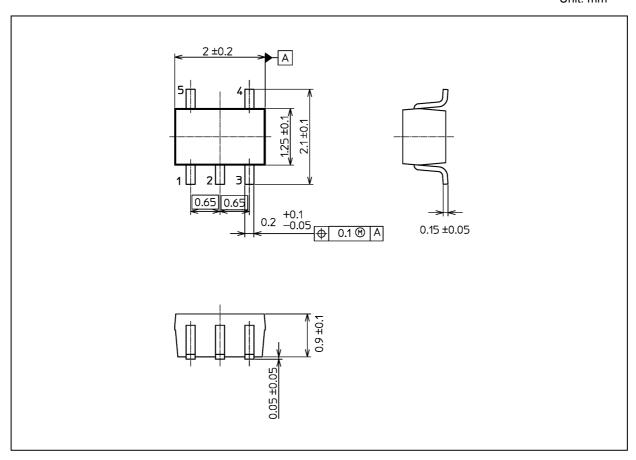
Table 9.7.1 AC Waveform Symbols

	Symbol	V _{CC} = 3.3 ± 0.3 V	V _{CC} = 2.5 ± 0.2 V	V _{CC} = 1.8 ± 0.15 V	V _{CC} = 1.5 ± 0.1 V	V _{CC} = 1.2 ± 0.1 V	V _{CC} = 0.9 V
Input	V _{IH}	V _{CC}	V _{CC}	V_{CC}	V _{CC}	V _{CC}	V _{CC}
	V _M	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2
Output	V _M	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2	V _{CC} /2



Package Dimensions

Unit: mm



Weight: 6.2 mg (typ.)

	Package Name(s)
Nickname: USV	



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