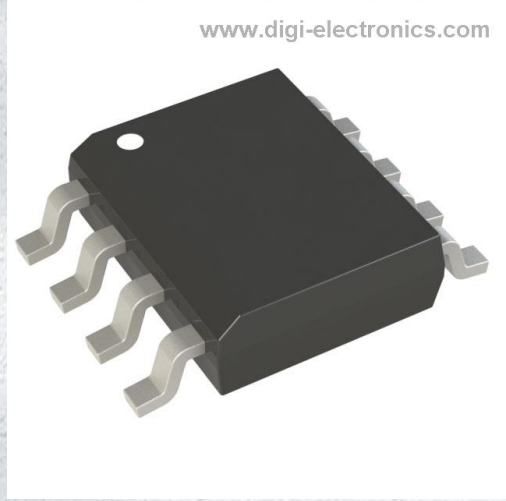
SQ4153EY-T1_BE3 Datasheet



DiGi Electronics Part Number	SQ4153EY-T1_BE3-DG
Manufacturer	Vishay Siliconix
Manufacturer Product Number	SQ4153EY-T1_BE3
Description	MOSFET P-CHANNEL 12V 25A 8SOIC
Detailed Description	P-Channel 12 V 25A (Tc) 7.1W (Tc) Surface Mount 8-SOIC

https://www.DiGi-Electronics.com

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
SQ4153EY-T1_BE3	Vishay Siliconix
Series:	Product Status:
TrenchFET®	Active
FET Type:	Technology:
P-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
12 V	25A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
1.8V, 4.5V	8.32mOhm @ 14A, 4.5V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
900mV @ 250µA	151 nC @ 4.5 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±8V	11000 pF @ 6 V
FET Feature:	Power Dissipation (Max):
-	7.1W (Tc)
Operating Temperature:	Grade:
-55°C ~ 175°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Supplier Device Package:	Package / Case:
8-SOIC	8-SOIC (0.154", 3.90mm Width)
Base Product Number:	
SQ4153	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
ECCN:	HTSUS:
EAR99	8541.29.0095

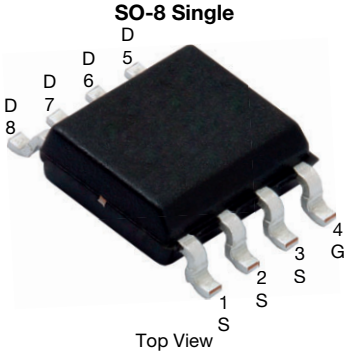
VISHAY

www.vishay.com

SQ4153EY

Vishay Siliconix

Automotive P-Channel 12 V (D-S) 175 °C MOSFET



FEATURES

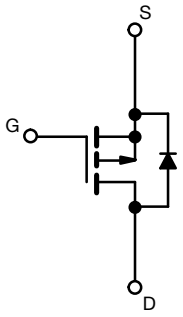
- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE

Pb-free

RoHS COMPLIANT

HALOGEN FREE



P-Channel MOSFET

PRODUCT SUMMARY	
V_{DS} (V)	-12
$R_{DS(on)}$ (Ω) at V_{GS} = -4.5 V	0.00832
$R_{DS(on)}$ (Ω) at V_{GS} = -2.5 V	0.01000
$R_{DS(on)}$ (Ω) at V_{GS} = -1.8 V	0.01430
I_D (A)	-25
Configuration	Single

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4153EY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	-12	V
Gate-source voltage		V_{GS}	± 8	
Continuous drain current [a]	T_C = 25 °C	I_D	-25	A
	T_C = 125 °C		-14	
Continuous source current (diode conduction) [a]		I_S	-6.5	
Pulsed drain current [b]		I_{DM}	-100	
Single pulse avalanche current	L = 0.1 mH	I_{AS}	-19	
Single pulse avalanche energy		E_{AS}	18	mJ
Maximum power dissipation [b]	T_C = 25 °C	P_D	7.1	W
	T_C = 125 °C		2.3	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB Mount [c]	R_{thJA}	85	°C/W
Junction-to-foot (drain)		R_{thJF}	21	

Notes

a. Package limited
b. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %
c. When mounted on 1" square PCB (FR4 material)

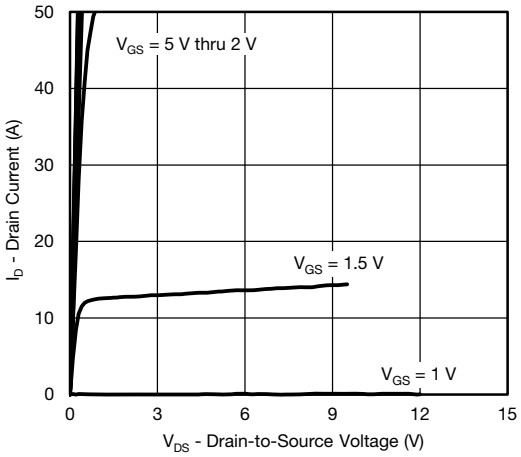
SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static							
Drain-source breakdown voltage	V_{DS}	V_{GS} = 0, I_D = -250 µA		-12	-	-	V
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250 µA		-0.4	-0.6	-0.9	
Gate-source leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 8 V		-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	V_{GS} = 0 V	V_{DS} = -12 V	-	-	-1	µA
		V_{GS} = 0 V	V_{DS} = -12 V, T_J = 125 °C	-	-	-50	
		V_{GS} = 0 V	V_{DS} = -12 V, T_J = 175 °C	-	-	-150	
On-state drain current [a]	$I_{D(on)}$	V_{GS} = -4.5 V	V_{DS} ≥ -5 V	-30	-	-	A
Drain-source on-state resistance [a]	$R_{DS(on)}$	V_{GS} = -4.5 V	I_D = -14 A	-	0.00510	0.00832	Ω
		V_{GS} = -4.5 V	I_D = -14 A, T_J = 125 °C	-	-	0.00900	
		V_{GS} = -4.5 V	I_D = -14 A, T_J = 175 °C	-	-	0.01100	
		V_{GS} = -2.5 V	I_D = -13 A	-	0.00650	0.01000	
		V_{GS} = -1.8 V	I_D = -12 A	-	0.00940	0.01430	
Forward transconductance [a]	g_{fs}	V_{DS} = -6 V, I_D = -10.5 A		-	54	-	S
Dynamic [b]							
Input capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = -6 V, f = 1 MHz	-	7500	11 000	pF
Output capacitance	C_{oss}			-	2800	4200	
Reverse transfer capacitance	C_{rss}			-	2400	3600	
Total gate charge [c]	Q_g	V_{GS} = -4.5 V	V_{DS} = -6 V, I_D = -10.5 A	-	101	151	nC
Gate-source charge [c]	Q_{gs}			-	15	-	
Gate-drain charge [c]	Q_{gd}			-	45	-	
Gate resistance	R_g	f = 1 MHz		1.1	2.2	3.2	Ω
Turn-on delay time [c]	$t_{d(on)}$	V_{DD} = -6 V, R_L = 15 Ω I_D ≅ -10.5 A, V_{GEN} = -4.5 V, R_g = 6 Ω		-	31	42	ns
Rise time [c]	t_r			-	168	224	
Turn-off delay time [c]	$t_{d(off)}$			-	310	412	
Fall time [c]	t_f			-	283	376	
Source-Drain Diode Ratings and Characteristics [b]							
Pulsed current [a]	I_{SM}			-	-	-100	A
Forward voltage	V_{SD}	I_F = -10.5 A, V_{GS} = 0		-	-0.8	-1.2	V

Notes

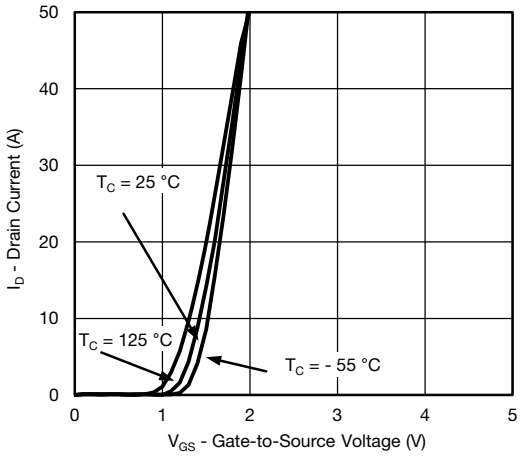
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %
b. Guaranteed by design, not subject to production testing
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

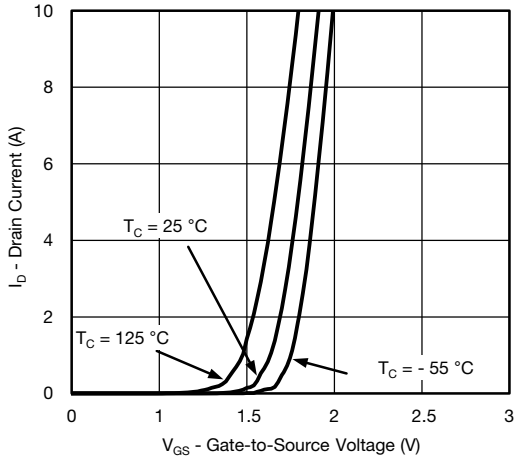
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



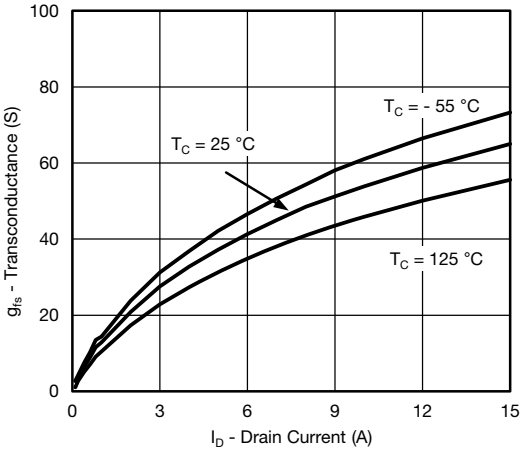
Output Characteristics



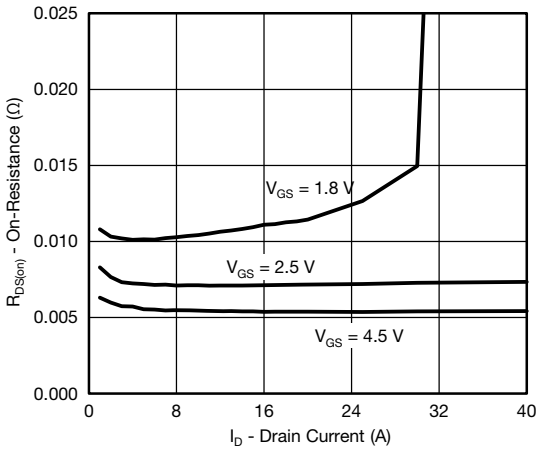
Transfer Characteristics



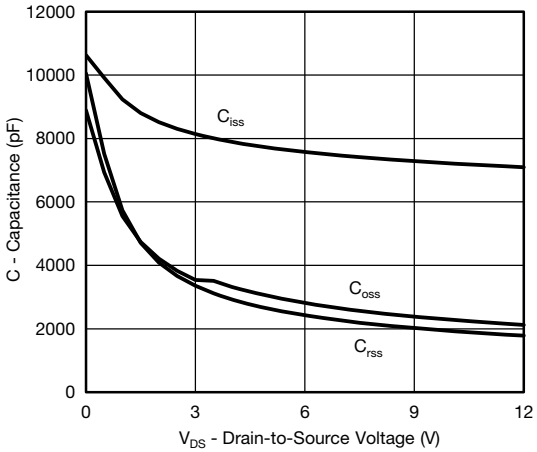
Transfer Characteristics



Transconductance

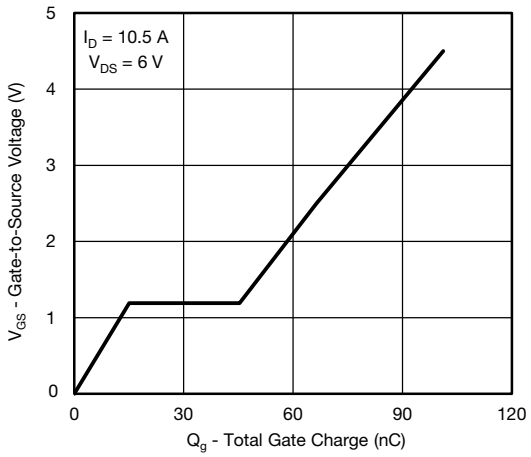


On-Resistance vs. Drain Current

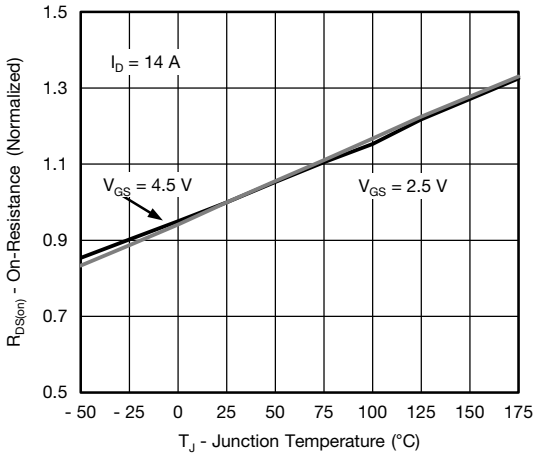


Capacitance

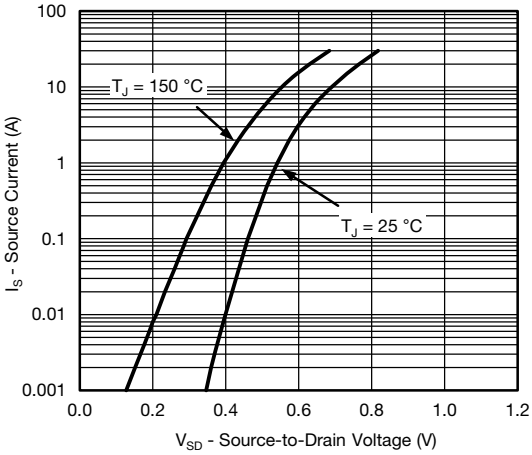
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



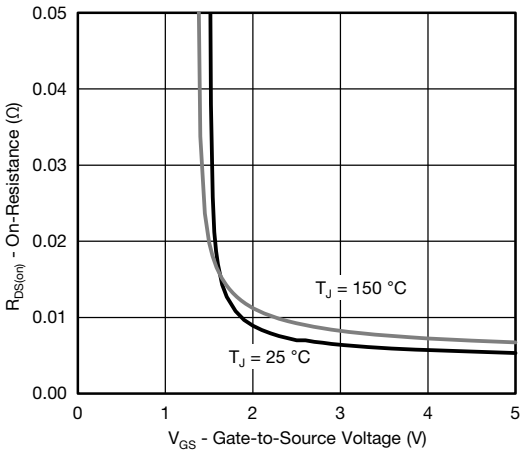
Gate Charge



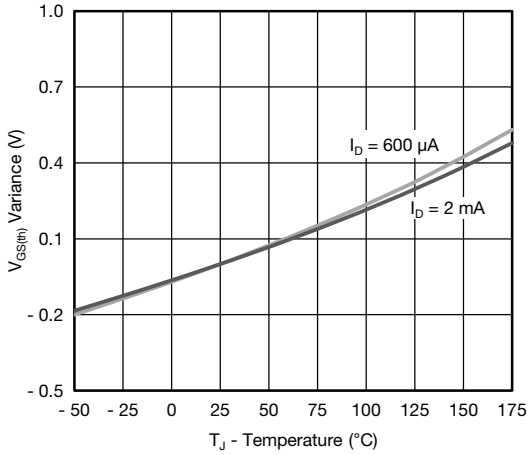
On-Resistance vs. Junction Temperature



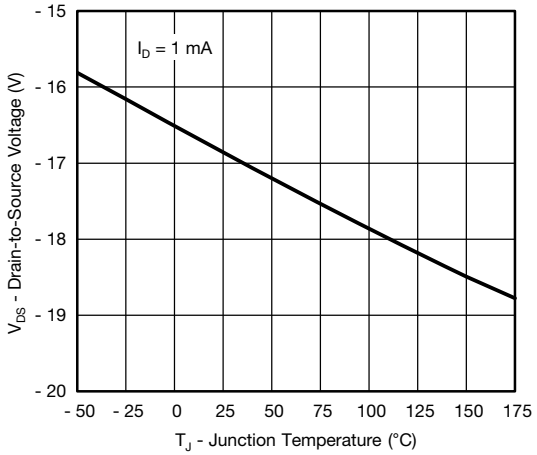
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage

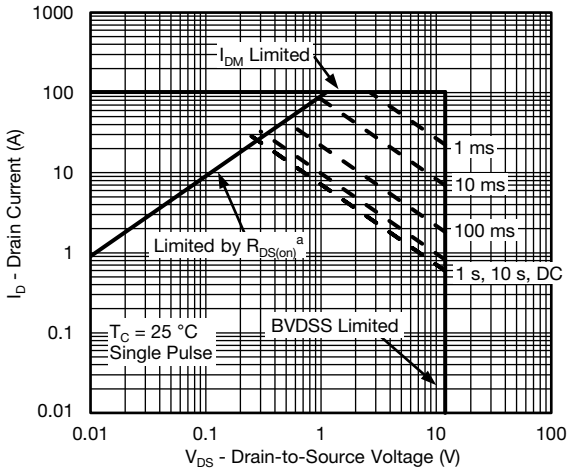


Threshold Voltage



Breakdown Voltage vs. Junction Temperature

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)

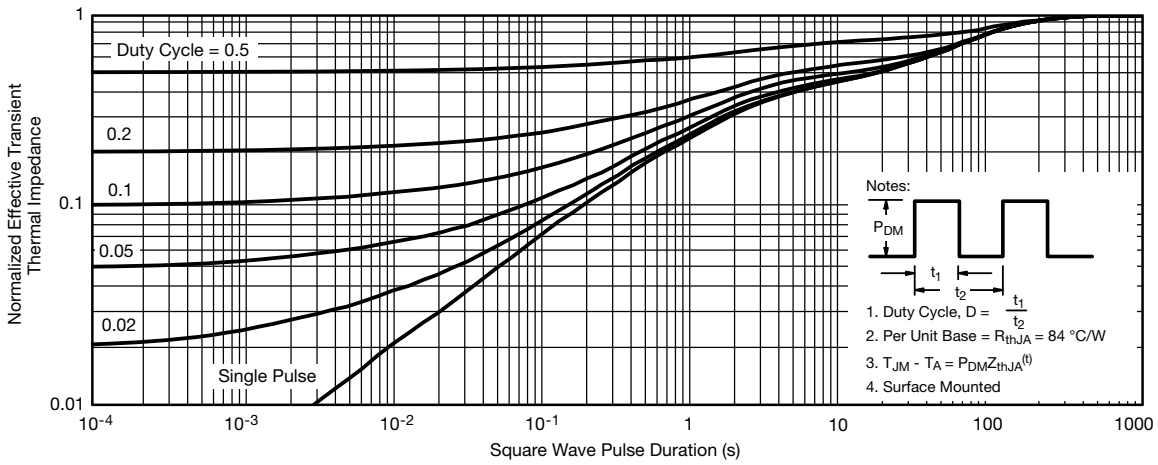


Safe Operating Area

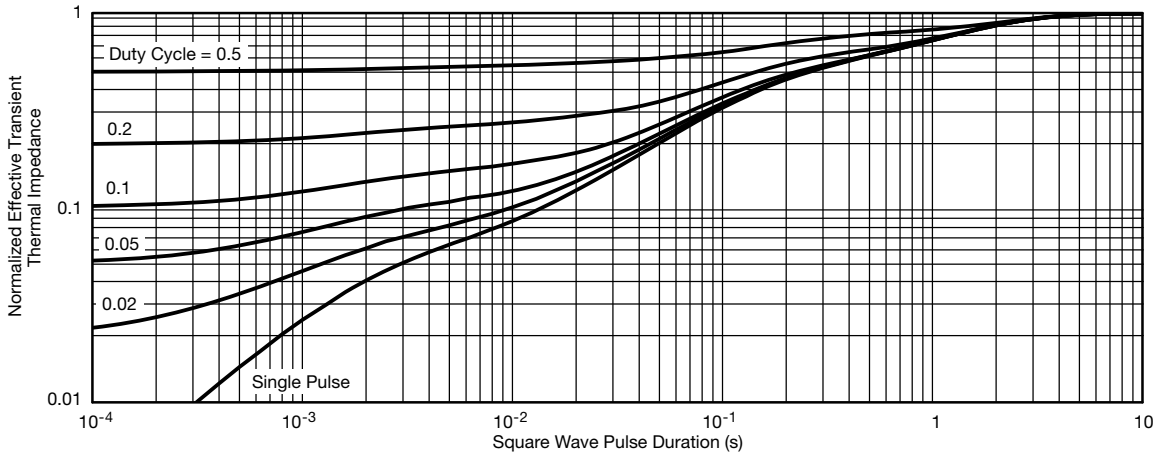
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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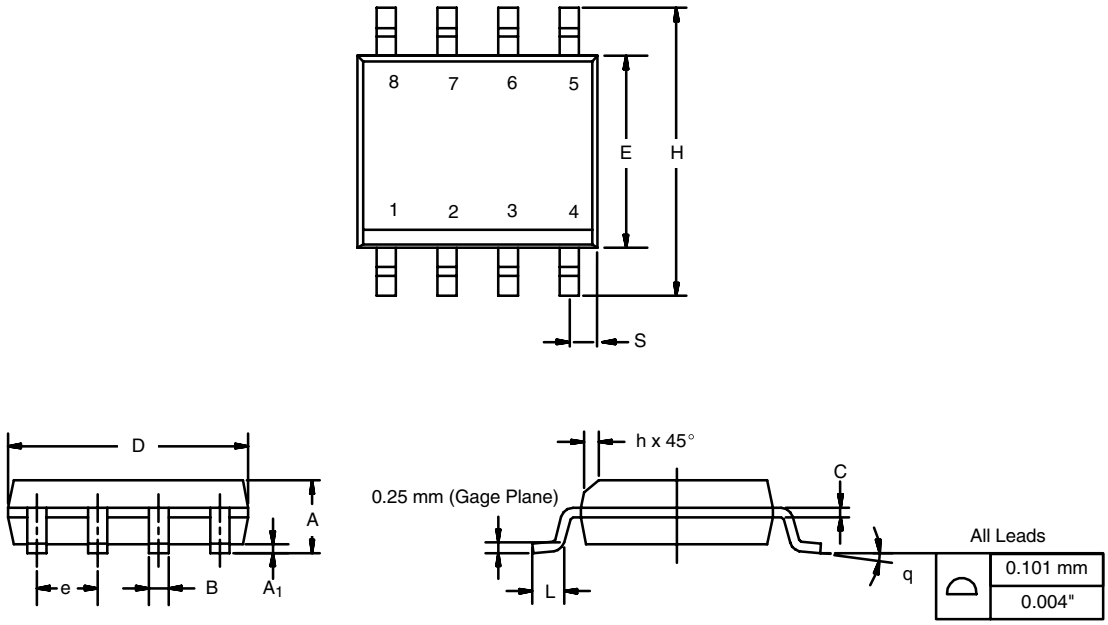
Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012

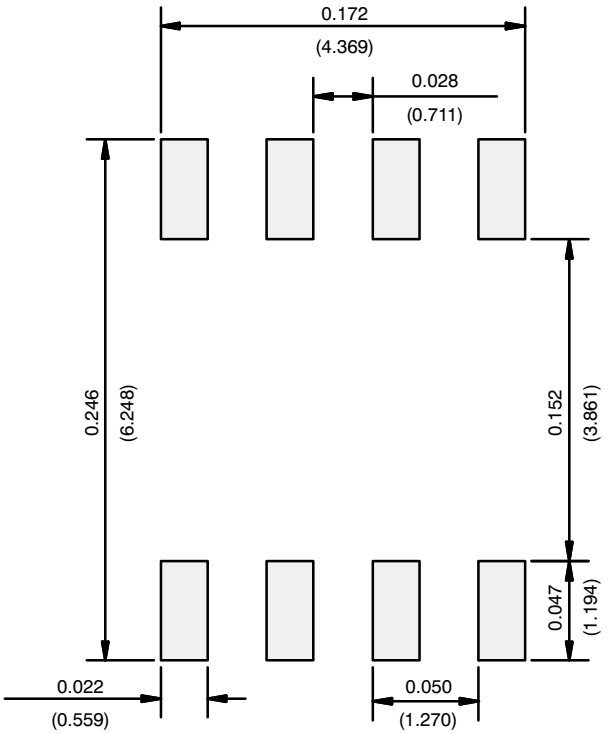


DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A_1	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498				

Application Note 826

Vishay Siliconix

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

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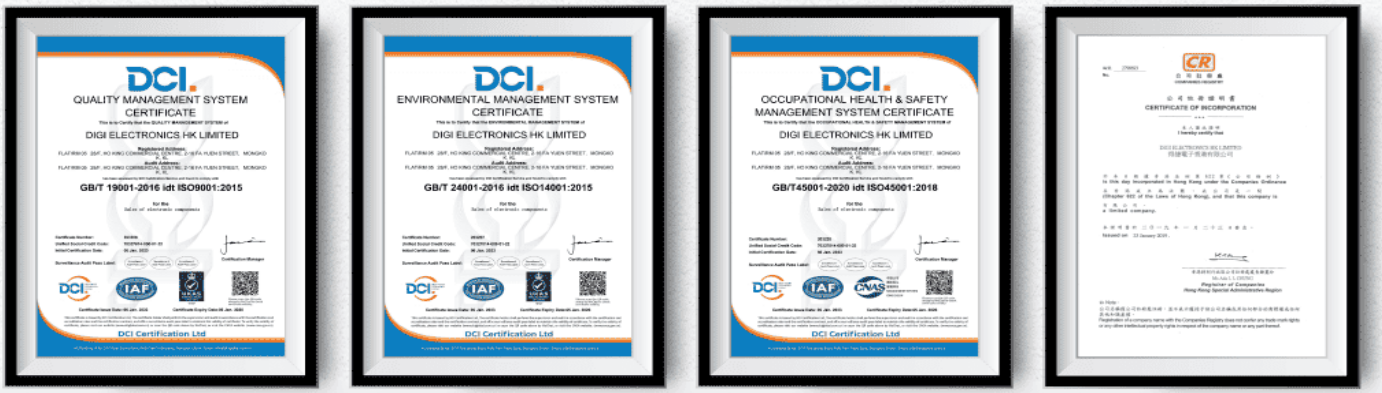

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