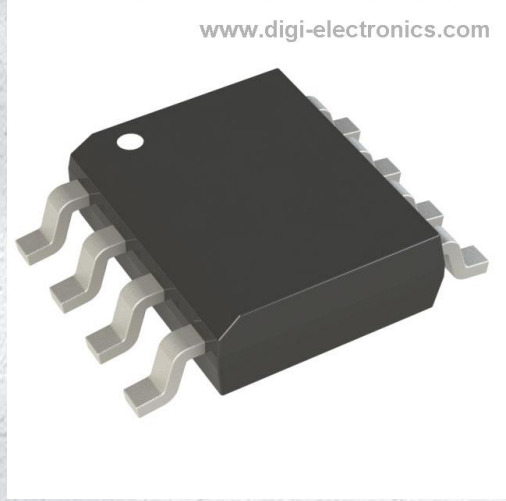
SQ4917CEY-T1_GE3 Datasheet



DiGi Electronics Part Number	SQ4917CEY-T1_GE3-DG
Manufacturer	Vishay Siliconix
Manufacturer Product Number	SQ4917CEY-T1_GE3
Description	MOSFET 2P-CH 60V 8A 8SOIC
Detailed Description	Mosfet Array 60V 8A (Tc) 5W (Tc) Surface Mount 8-SOIC

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This model SQ4917CEY-T1_GE3 is available at DiGi Electronics.

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Manufacturer Product Number:	Manufacturer:
SQ4917CEY-T1_GE3	Vishay Siliconix
Series:	Product Status:
TrenchFET®	Active
Technology:	Configuration:
MOSFET (Metal Oxide)	2 P-Channel (Dual)
FET Feature:	Drain to Source Voltage (Vdss):
-	60V
Current - Continuous Drain (Id) @ 25°C:	Rds On (Max) @ Id, Vgs:
8A (Tc)	48mOhm @ 4.3A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
2.5V @ 250µA	65nC @ 10V
Input Capacitance (Ciss) (Max) @ Vds:	Power - Max:
1910pF @ 30V	5W (Tc)
Operating Temperature:	Grade:
-55°C ~ 175°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Package / Case:	Supplier Device Package:
8-SOIC (0.154", 3.90mm Width)	8-SOIC
Base Product Number:	
SQ4917	

Environmental & Export classification

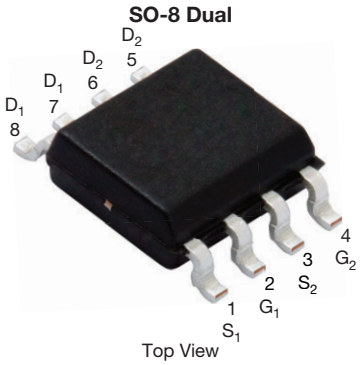
RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
ECCN:	HTSUS:
EAR99	8541.29.0095

VISHAY
www.vishay.com

SQ4917CEY

Vishay Siliconix

Automotive Dual P-Channel 60 V (D-S) 175 °C MOSFET



FEATURES

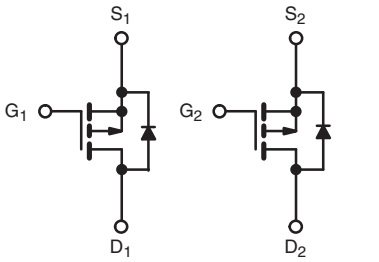
- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

AUTOMOTIVE GRADE

Pb-free

RoHS COMPLIANT

HALOGEN FREE



PRODUCT SUMMARY	
V_{DS} (V)	-60
$R_{DS(on)}$ (Ω) at V_{GS} = -10 V	0.0480
$R_{DS(on)}$ (Ω) at V_{GS} = -4.5 V	0.0612
I_D (A) per leg	-8
Configuration	Dual

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4917CEY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V_{DS}	-60	V
Gate-source voltage		V_{GS}	± 20	
Continuous drain current	T_C = 25 °C	I_D	-8	A
	T_C = 125 °C		-4.75	
Continuous source current (diode conduction)		I_S	-4.5	
Pulsed drain current [a]		I_{DM}	-32	
Single pulse avalanche current	L = 0.1 mH	I_{AS}	-22.4	
Single pulse avalanche energy		E_{AS}	25	mJ
Maximum power dissipation	T_C = 25 °C	P_D	5	W
	T_C = 125 °C		1.67	
Operating junction and storage temperature range		T_J, T_{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount [b]	R_{thJA}	110	°C/W
Junction-to-foot (drain)		R_{thJF}	30	

Notes

a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %
b. When mounted on 1" square PCB (FR-4 material)

SPECIFICATIONS (T_C = 25 °C, unless otherwise noted)							
PARAMETER	**SYMBOL**	**TEST CONDITIONS**		**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static							
Drain-source breakdown voltage	V_{DS}	V_{GS} = 0 V, I_D = -250 µA		-60	-	-	V
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = -250 µA		-1.5	-2.0	-2.5	
Gate-source leakage	I_{GSS}	V_{DS} = 0 V, V_{GS} = ± 20 V		-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	V_{GS} = 0 V	V_{DS} = -60 V	-	-	-1	µA
		V_{GS} = 0 V	V_{DS} = -60 V, T_J = 125 °C	-	-	-50	
		V_{GS} = 0 V	V_{DS} = -60 V, T_J = 175 °C	-	-	-150	
On-state drain current [a]	$I_{D(on)}$	V_{GS} = -10 V	V_{DS} ≤ -5 V	-30	-	-	A
Drain-source on-state resistance [a]	$R_{DS(on)}$	V_{GS} = -10 V	I_D = -4.3 A	-	0.0421	0.0480	Ω
		V_{GS} = -10 V	I_D = -4.3 A, T_J = 125 °C	-	-	0.0780	
		V_{GS} = -10 V	I_D = -4.3 A, T_J = 175 °C	-	-	0.0960	
		V_{GS} = -4.5 V	I_D = -3.8 A	-	0.0566	0.0612	
Forward transconductance [b]	g_{fs}	V_{DS} = -15 V, I_D = -4.3 A		-	12	-	S
Dynamic [b]							
Input capacitance	C_{iss}	V_{GS} = 0 V	V_{DS} = -30 V, f = 1 MHz	-	1575	1910	pF
Output capacitance	C_{oss}			-	175	417	
Reverse transfer capacitance	C_{rss}			-	113	142	
Total gate charge [c]	Q_g	V_{GS} = -10 V	V_{DS} = -30 V, I_D = -5 A	-	36.3	65	nC
Gate-source charge [c]	Q_{gs}			-	5.3	-	
Gate-drain charge [c]	Q_{gd}			-	8.9	-	
Gate resistance	R_g	f = 1 MHz		1.3	2.36	4	Ω
Turn-on delay time [c]	$t_{d(on)}$	V_{DD} = -30 V, R_L = 8.8 Ω I_D ≅ -5 A, V_{GEN} = -10 V, R_g = 1 Ω		-	11	17	ns
Rise time [c]	t_r			-	5	17	
Turn-off delay time [c]	$t_{d(off)}$			-	32	52	
Fall time [c]	t_f			-	5	9	
Source-Drain Diode Ratings and Characteristics [b]							
Pulsed current [a]	I_{SM}			-	-	-32	A
Forward voltage	V_{SD}	I_F = -2.8 A, V_{GS} = 0 V		-	-0.79	-1.2	V
Body diode reverse recovery time	t_{rr}	I_F = -2.5 A, di/dt = 100 A/µs		-	29	58	ns
Body diode reverse recovery charge	Q_{rr}			-	44	88	nC
Reverse recovery fall time	t_a			-	24	-	ns
Reverse recovery rise time	t_b			-	5	-	
Body diode peak reverse recovery current	$I_{RM(REC)}$			-	-3.4	-	A

Notes

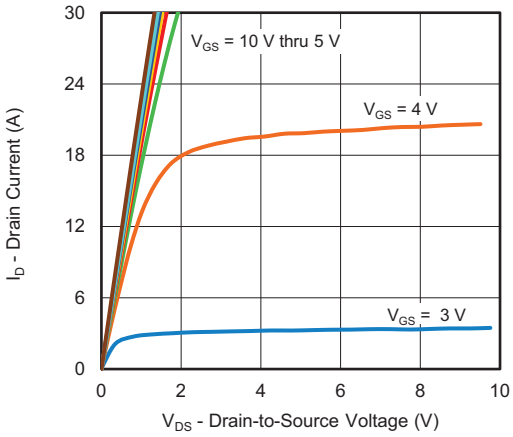
a. Pulse test; pulse width ≤ 300 µs, duty cycle ≤ 2 %

b. Guaranteed by design, not subject to production testing

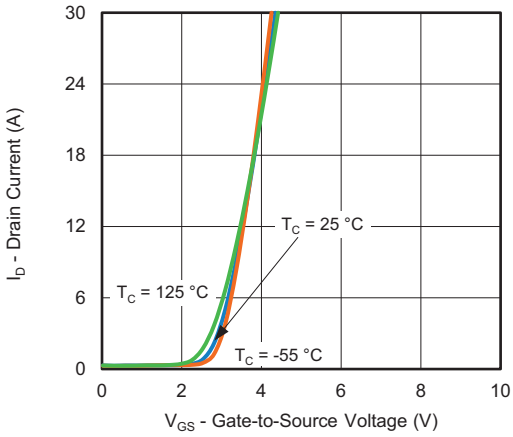
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

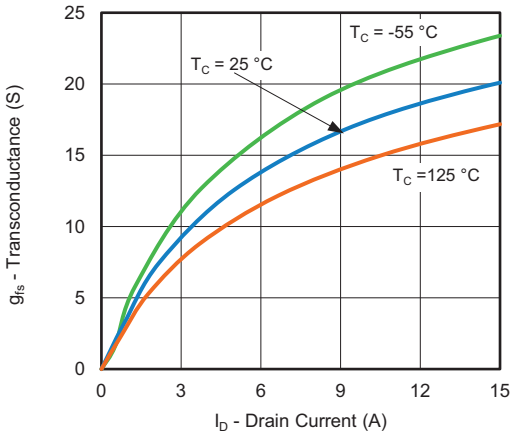
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



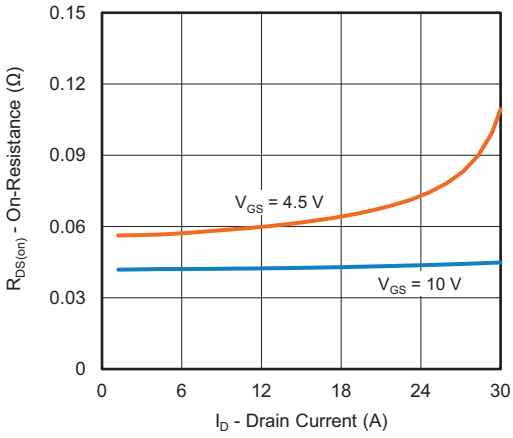
Output Characteristics



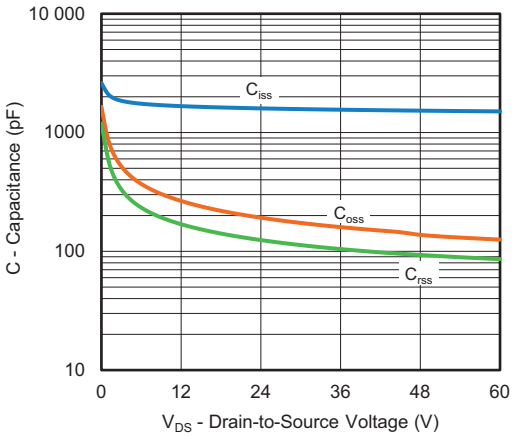
Transfer Characteristics



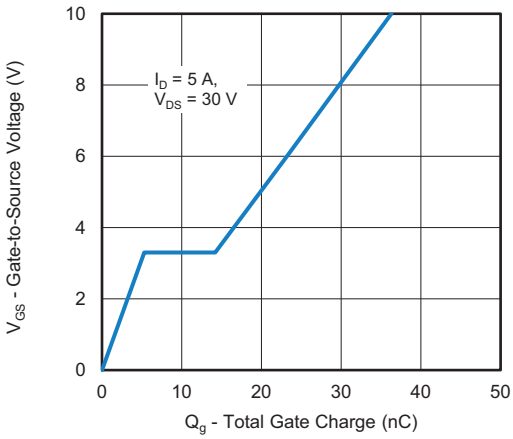
Transconductance



On-Resistance vs. Drain Current

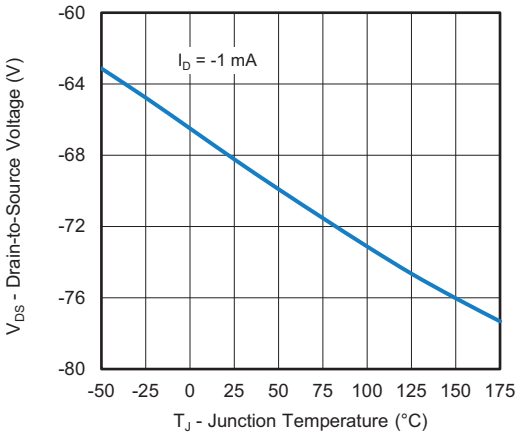


Capacitance

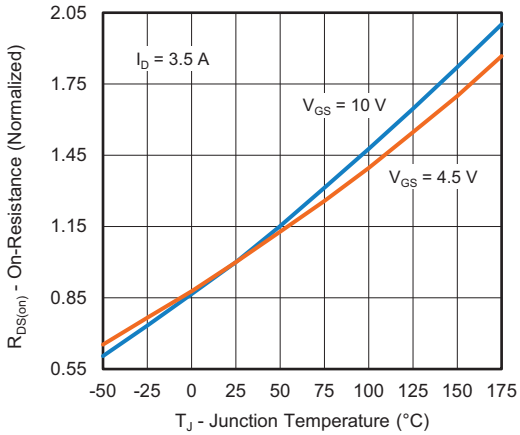


Gate Charge

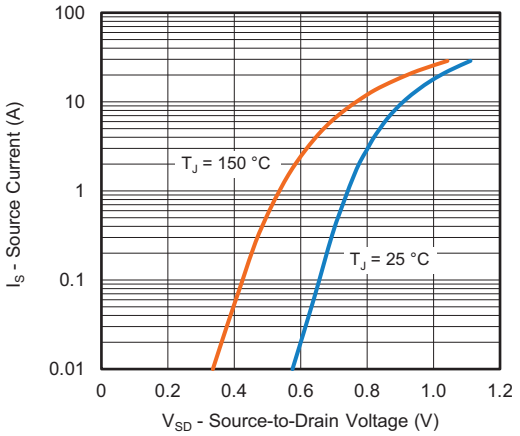
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



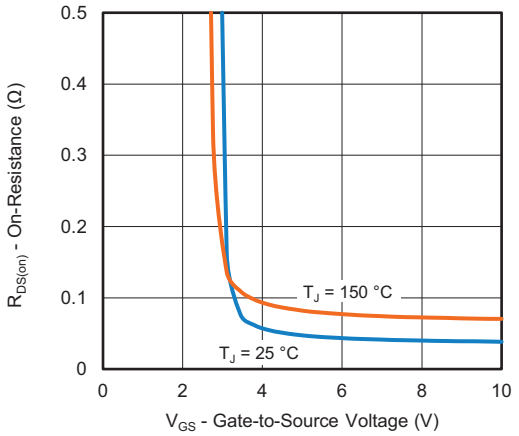
Drain Source Breakdown vs. Junction Temperature



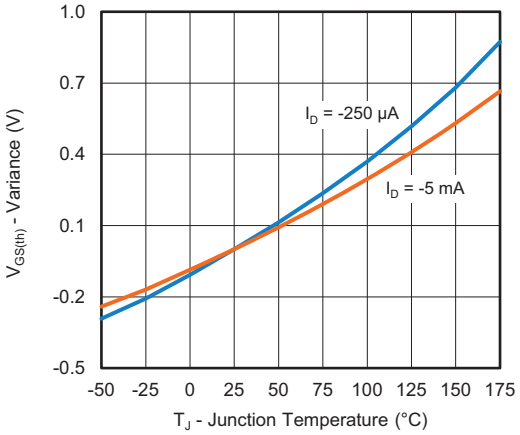
On-Resistance vs. Junction Temperature



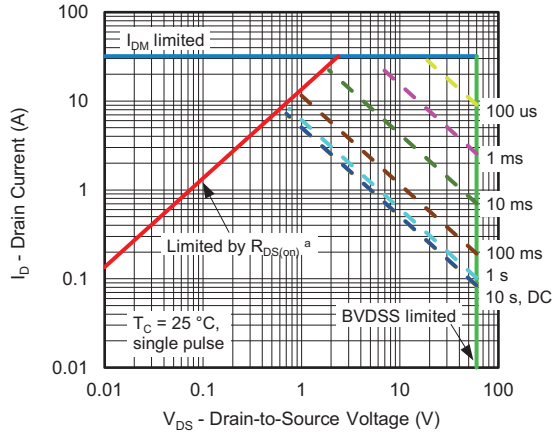
Source Drain Diode Forward Voltage



On-Resistance vs. Gate-to-Source Voltage



Threshold Voltage

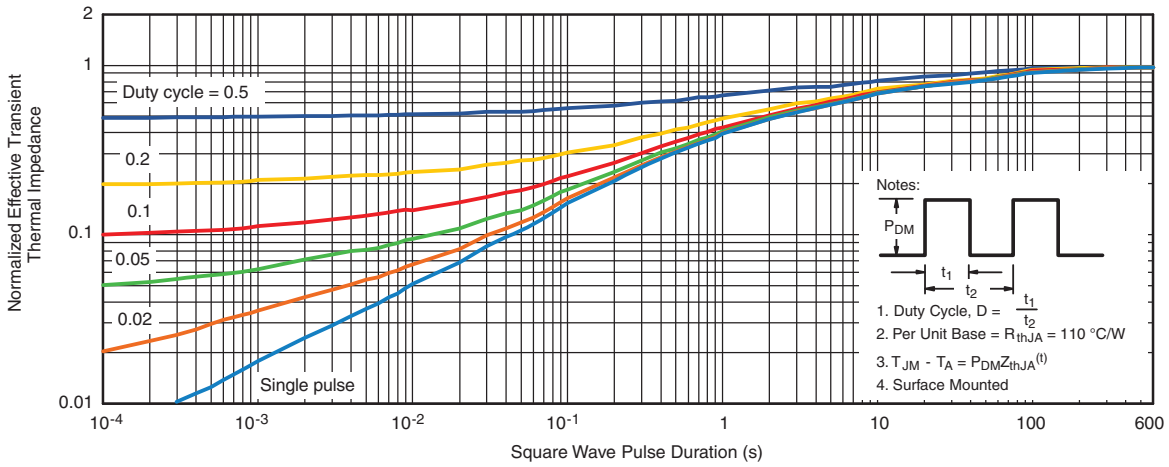


Safe Operating Area

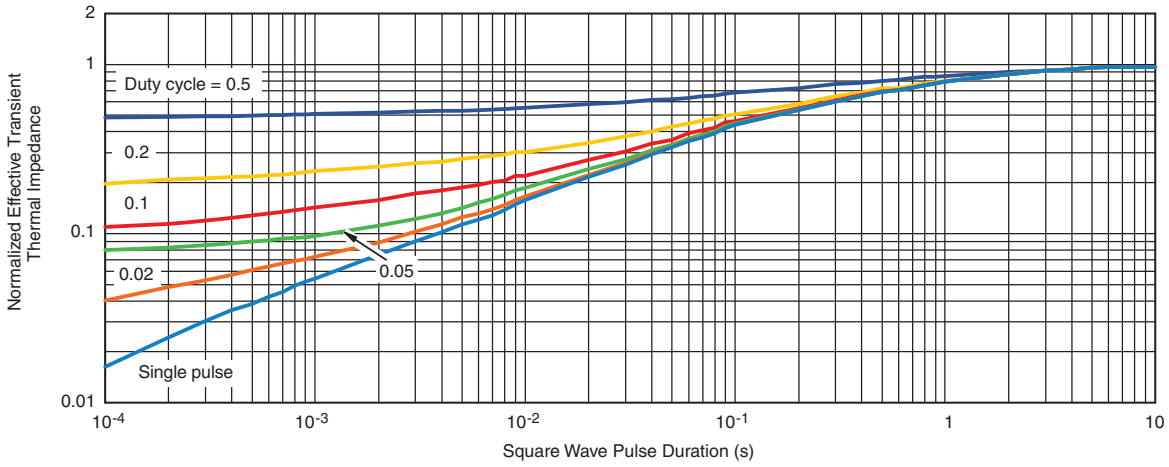
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

  are given for general guidelines only to enable the user to get a “ball park” indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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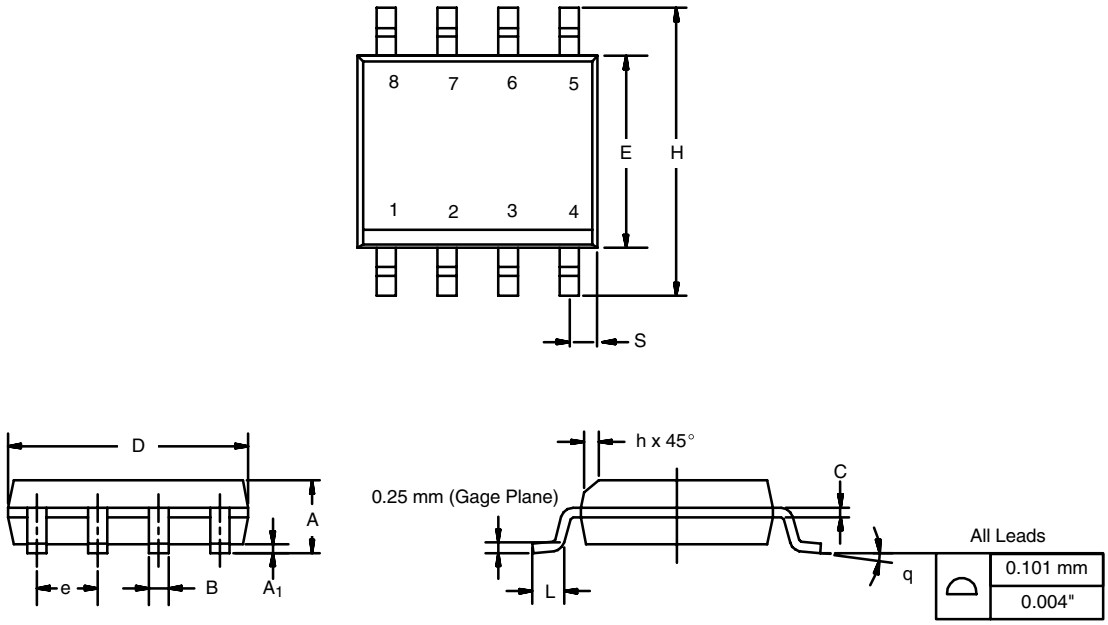
Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012

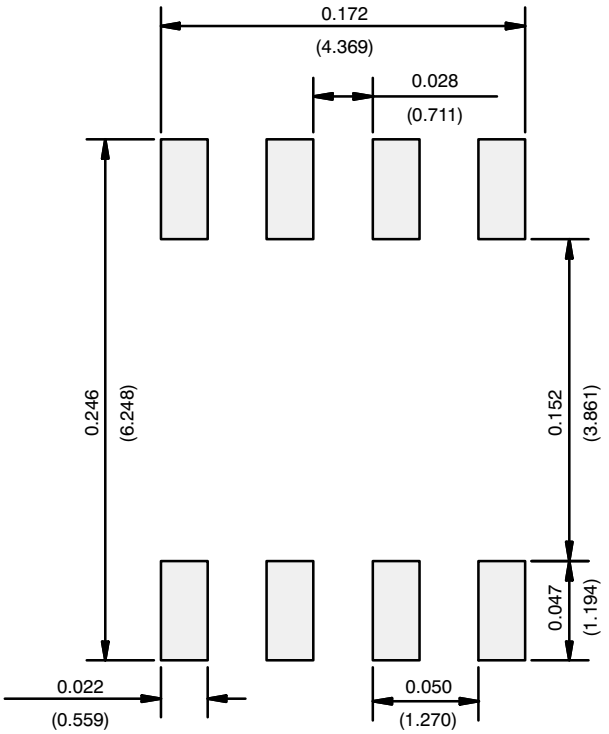


DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A_1	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498				

Application Note 826

Vishay Siliconix

RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads
Dimensions in Inches/(mm)

Return to Index

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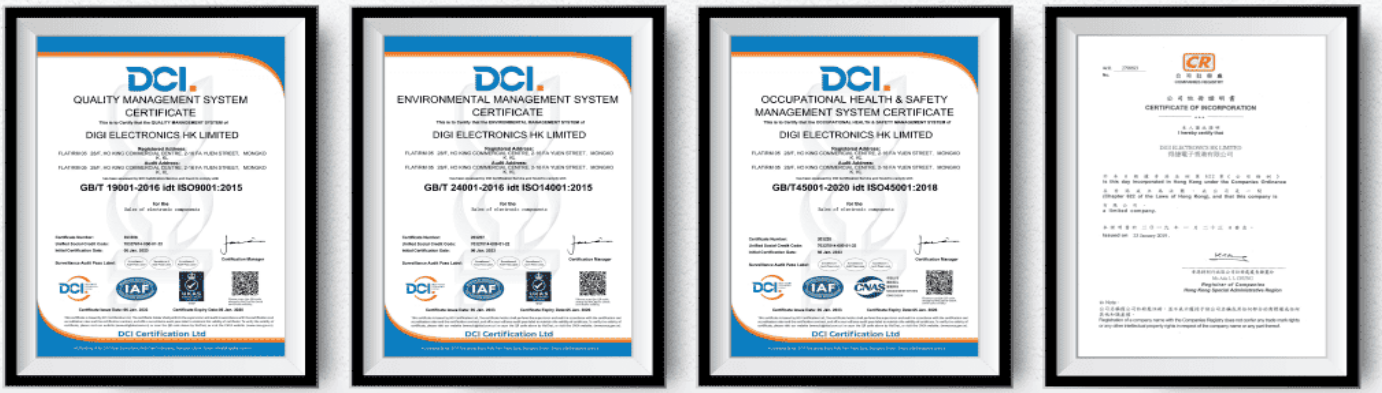

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