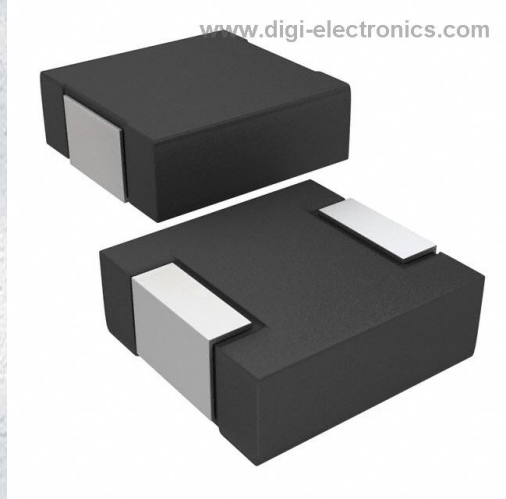
IHLP2020CZER2R2M01 Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number	IHLP2020CZER2R2M01-DG
Manufacturer	Vishay Dale
Manufacturer Product Number	IHLP2020CZER2R2M01
Description	FIXED IND 2.2UH 5.8A 29.2MOHM SM
Detailed Description	2.2 µH Shielded Molded Inductor 5.8 A 29.2mOhm Max Nonstandard

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
IHLP2020CZER2R2M01	Vishay Dale
Series:	Product Status:
IHLP-2020CZ-01	Active
Type:	Material - Core:
Molded	-
Inductance:	Tolerance:
2.2 µH	±20%
Current Rating (Amps):	Current - Saturation (Isat):
5.8 A	10A
Shielding:	DC Resistance (DCR):
Shielded	29.2mOhm Max
Q @ Freq:	Frequency - Self Resonant:
-	-
Ratings:	Operating Temperature:
-	-55°C ~ 125°C
Inductance Frequency - Test:	Mounting Type:
100 kHz	Surface Mount
Package / Case:	Supplier Device Package:
Nonstandard	-
Size / Dimension:	Height - Seated (Max):
0.216" L x 0.204" W (5.49mm x 5.18mm)	0.118" (3.00mm)

Environmental & Export classification

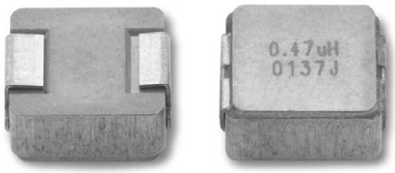
RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8504.50.4000	

VISHAY
www.vishay.com

IHLP-2020CZ-01

Vishay Dale

IHLP® Commercial Inductors, High Saturation Series



LINKS TO ADDITIONAL RESOURCES

3D Models Calculators

FEATURES

- Shielded construction
- Frequency range up to 5.0 MHz
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- Excellent temperature stability for inductance and saturation
- IHLP design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free
RoHS COMPLIANT
HALOGEN FREE GREEN (5-2008)

APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS

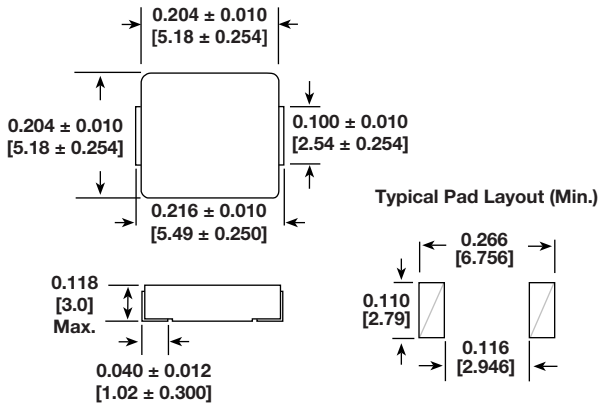
L_0 INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (µH)	DCR TYP. 25 °C (mΩ)	DCR MAX. 25 °C (mΩ)	HEAT RATING CURRENT DC TYP. (A) (1)	SATURATION CURRENT DC TYP. (A) (2)	SRF TYP. (MHz)
0.10	3.00	3.16	23.0	27.0	255
0.22	4.30	4.52	15.5	21.0	160
0.33	5.70	6.10	13.7	19.0	128
0.47	6.70	7.04	12.2	16.0	84
0.68	8.53	8.96	10.2	13.5	80
0.82	11.3	11.9	9.3	13.0	73
1.0	13.1	13.7	9.2	12.0	59
1.5	19.7	20.7	7.2	11.0	42
2.2	27.8	29.2	5.8	10.0	39
3.3	52.1	54.7	5.0	8.5	31
4.7	73.8	77.5	3.5	8.2	25
5.6	103	108	3.0	4.1	24
10.0	158	164	2.5	4.0	16
15.0	252	265	1.9	2.5	13.5

Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V

(1) DC current (A) that will cause an approximate ΔT of 40 °C
(2) DC current (A) that will cause L_0 to drop approximately 20 %

DIMENSIONS in inches [millimeters]



DESCRIPTION

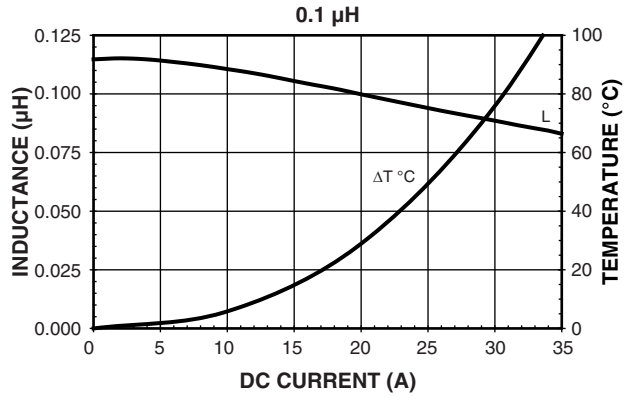
IHLP-2020CZ-01	4.7 µH	± 20 %	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

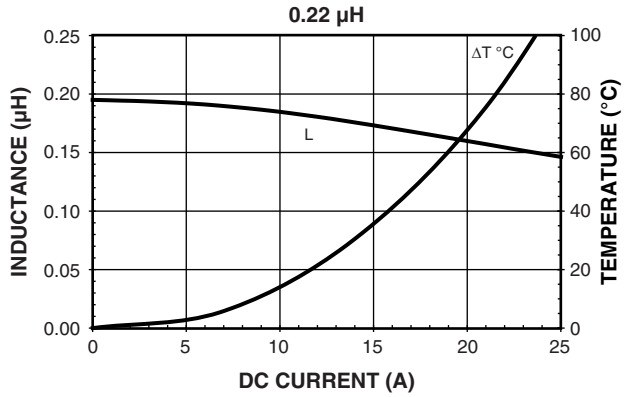
GLOBAL PART NUMBER

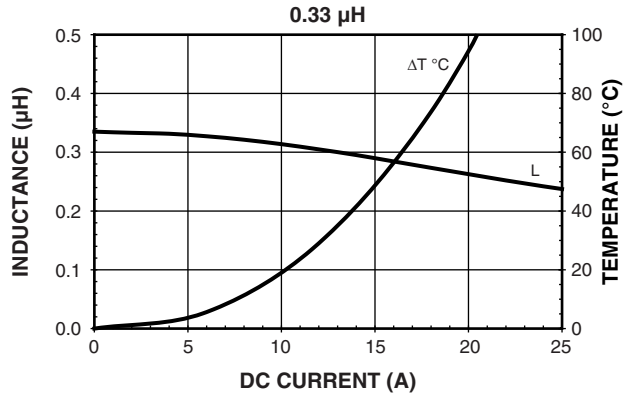
I	H	L	P	2	0	2	0	C	Z	E	R	4	R	7	M	0	1
PRODUCT FAMILY				SIZE						PACKAGE CODE		INDUCTANCE VALUE			TOL.	SERIES	

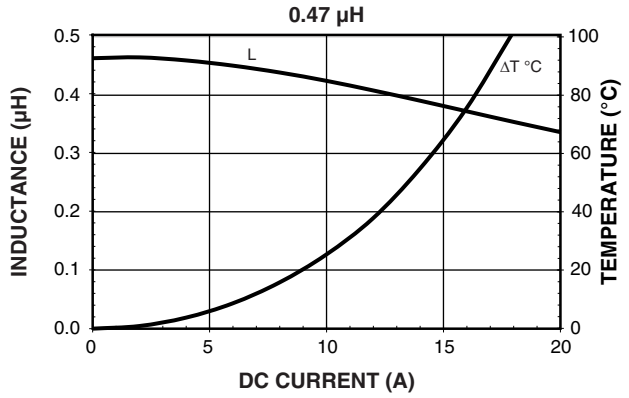
PATENT(S): www.vishay.com/patents
This Vishay product is protected by one or more United States and international patents.

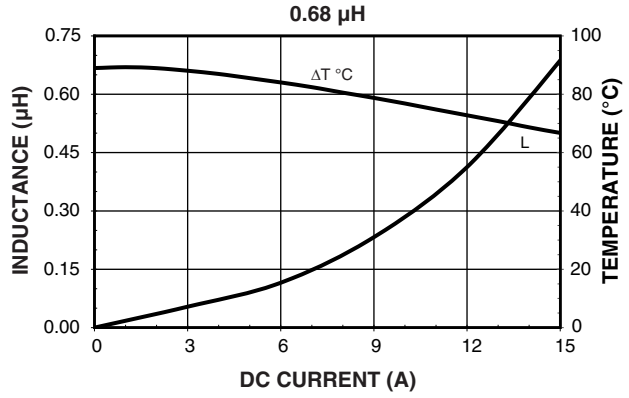
PERFORMANCE GRAPHS

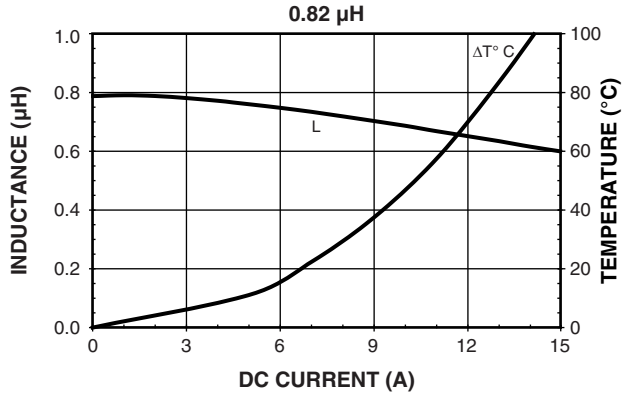


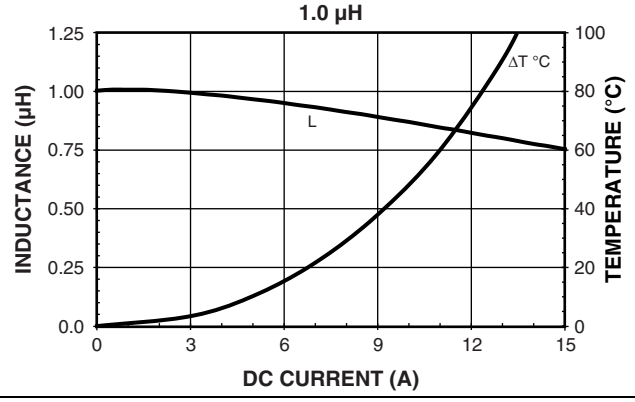


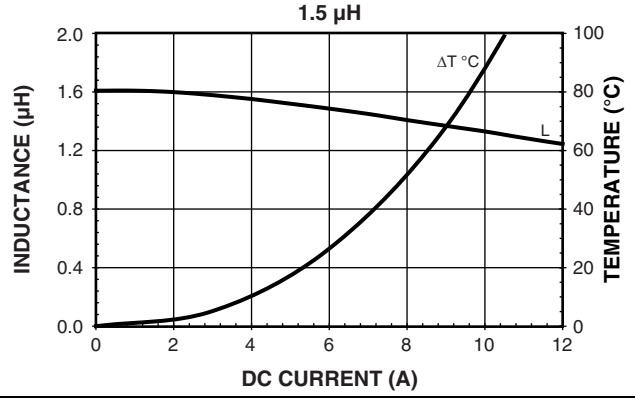












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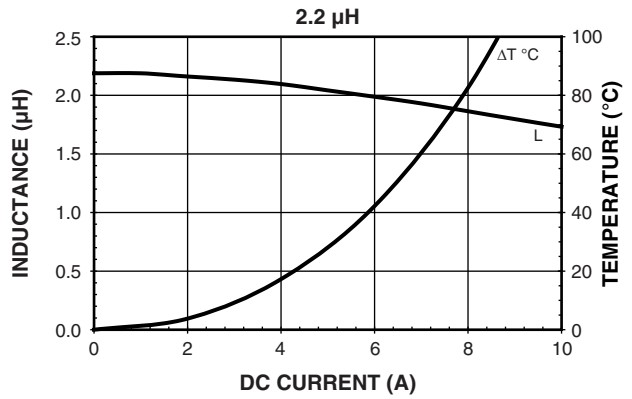
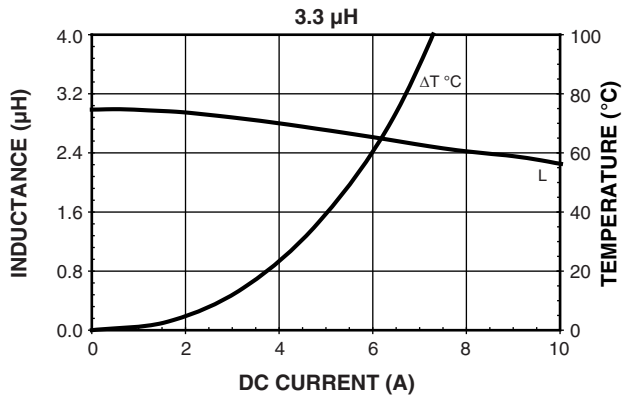
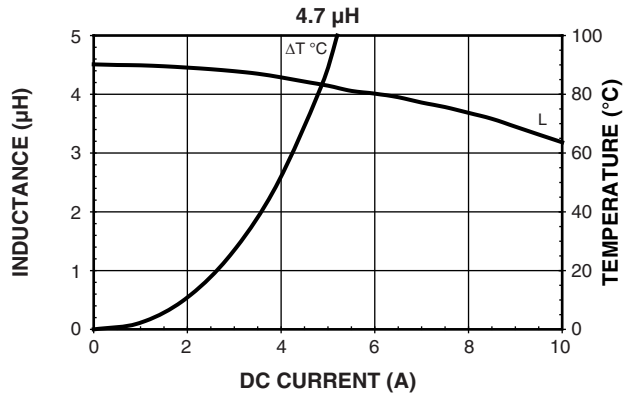
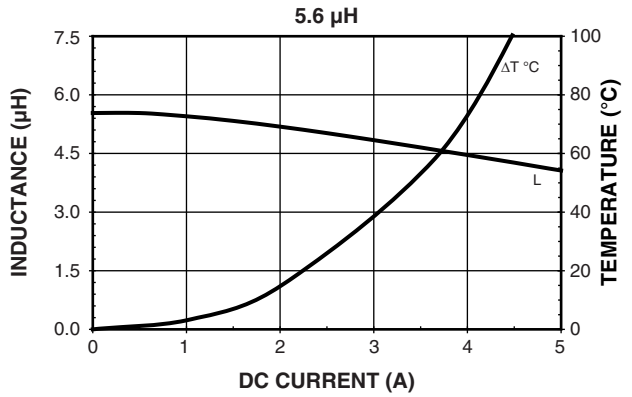
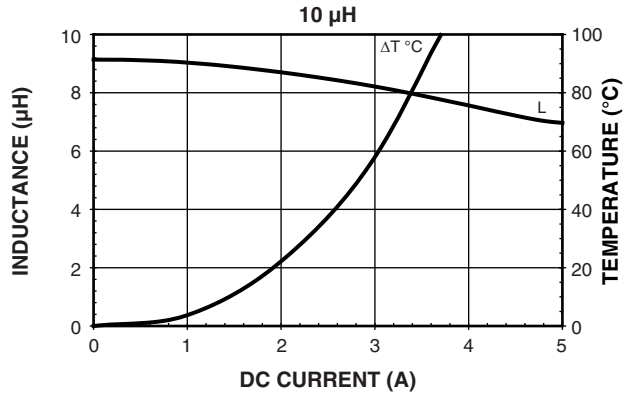
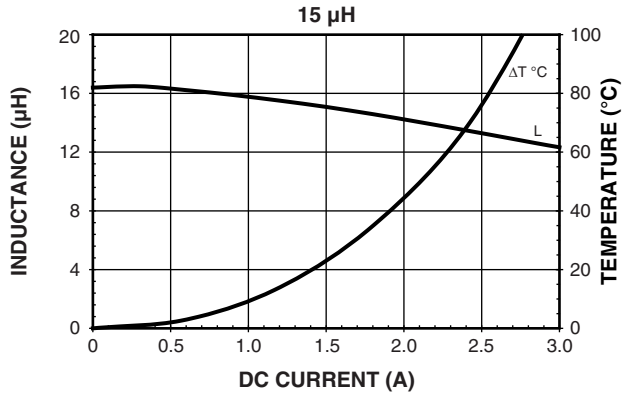




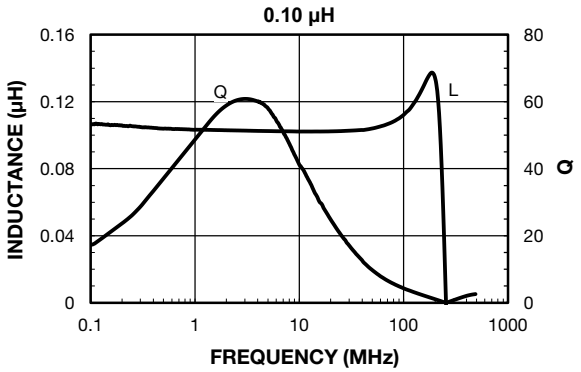


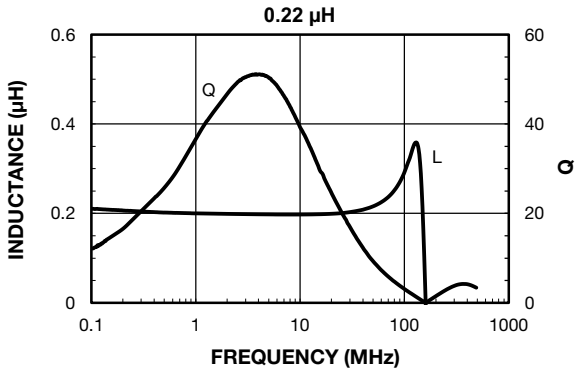


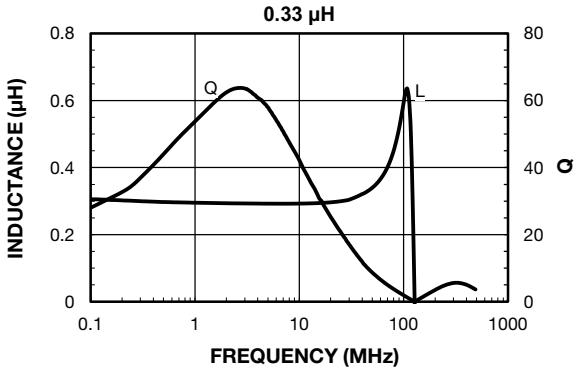


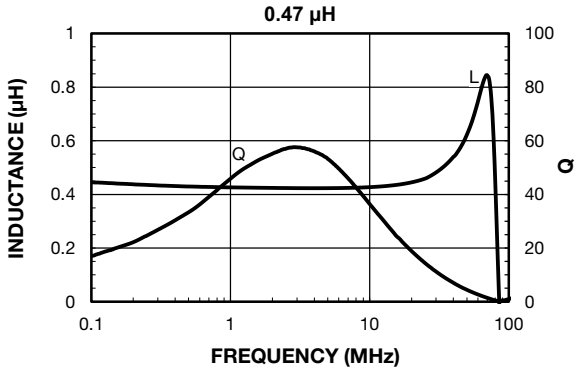


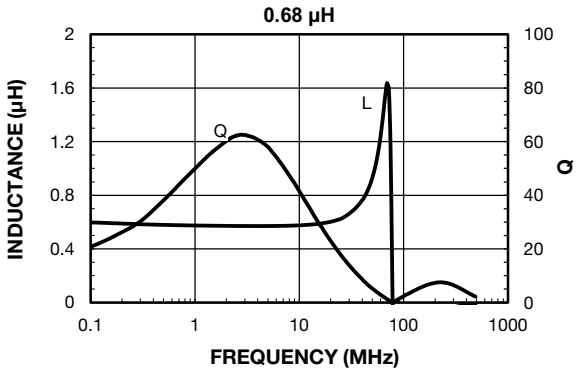
PERFORMANCE GRAPHS: INDUCTANCE AND Q VS. FREQUENCY

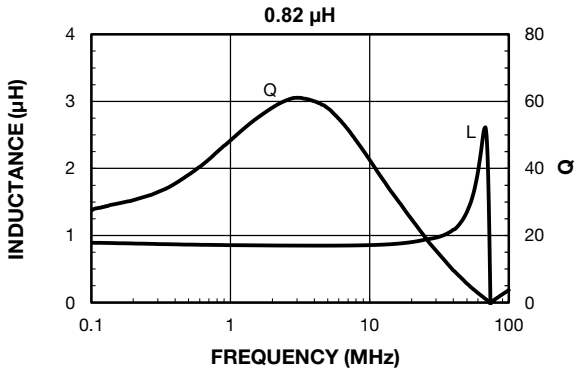


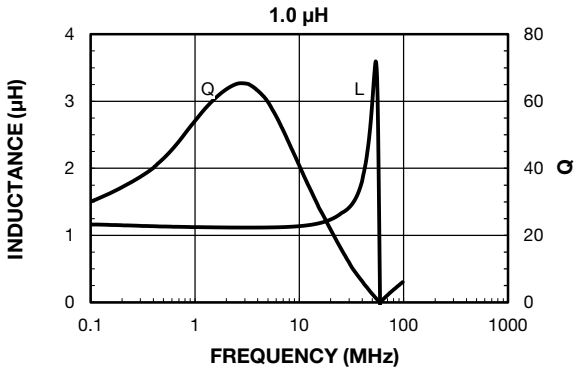


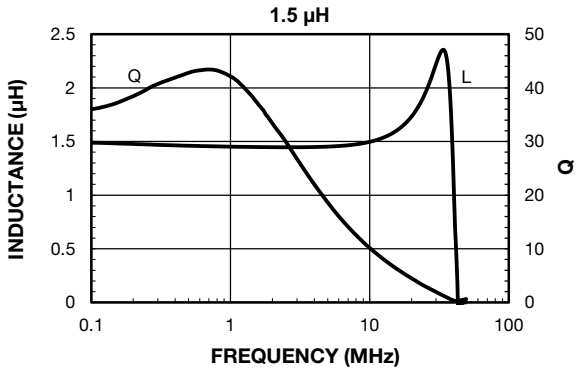












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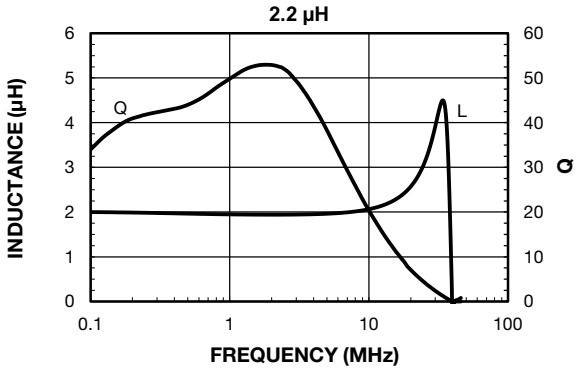

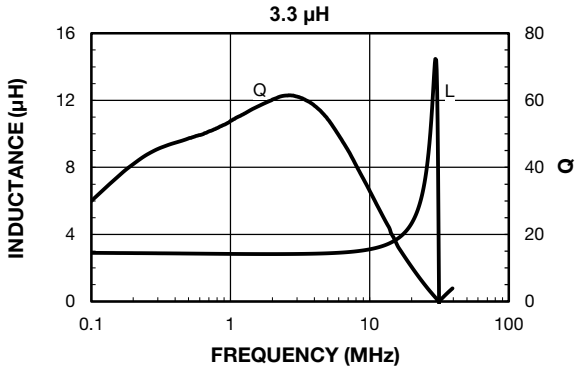

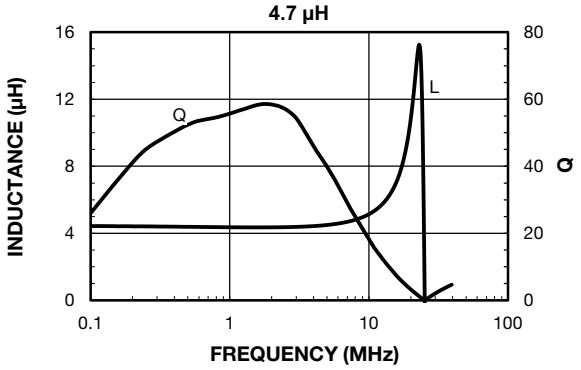

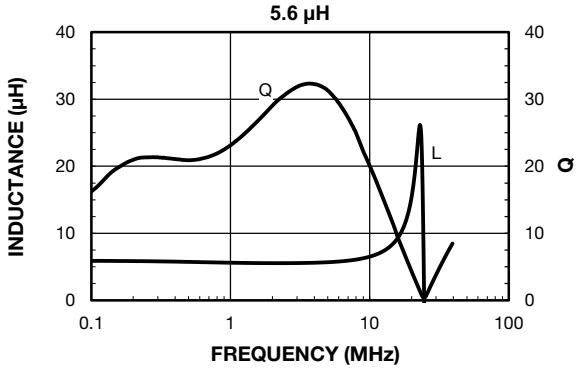

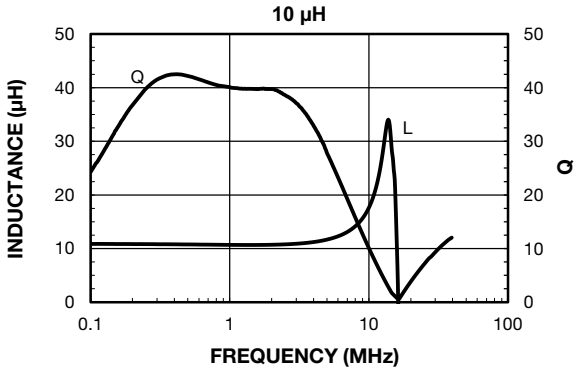

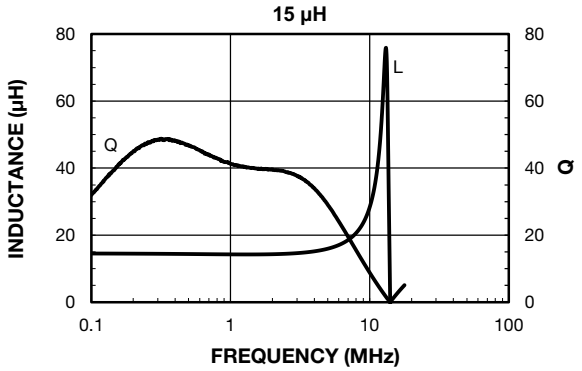

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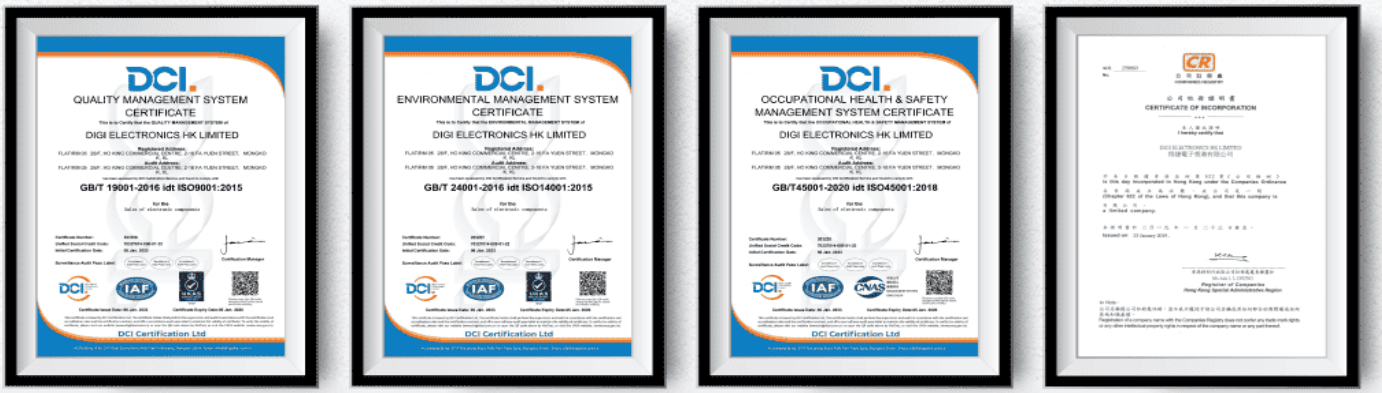

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