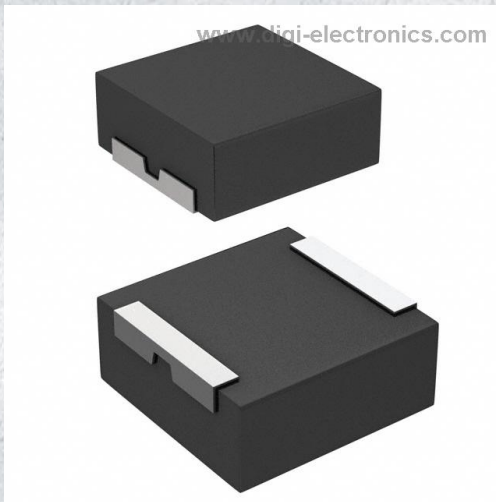


# IHLP6767DZERR33M01 Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	IHLP6767DZERR33M01-DG
Manufacturer	<a href="#">Vishay Dale</a>
Manufacturer Product Number	IHLP6767DZERR33M01
Description	FIXED IND 330NH 56A 1.28MOHM SMD
Detailed Description	330 nH Shielded Molded Inductor 56 A 1.28mOhm Max Nonstandard



Tel: +00 852-30501935

RFQ Email: [Info@DiGi-Electronics.com](mailto:Info@DiGi-Electronics.com)

DiGi is a global authorized distributor of electronic components.

## Purchase and inquiry

Manufacturer Product Number:

IHLP6767DZERR33M01

Series:

IHLP-6767DZ-01

Type:

Molded

Inductance:

330 nH

Current Rating (Amps):

56 A

Shielding:

Shielded

Q @ Freq:

-

Ratings:

-

Inductance Frequency - Test:

100 kHz

Package / Case:

Nonstandard

Size / Dimension:

0.675" L x 0.675" W (17.15mm x 17.15mm)

Manufacturer:

Vishay Dale

Product Status:

Active

Material - Core:

-

Tolerance:

±20%

Current - Saturation (Isat):

82A

DC Resistance (DCR):

1.28mOhm Max

Frequency - Self Resonant:

-

Operating Temperature:

-55°C ~ 125°C

Mounting Type:

Surface Mount

Supplier Device Package:

-

Height - Seated (Max):

0.157" (4.00mm)

## Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8504.50.4000

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

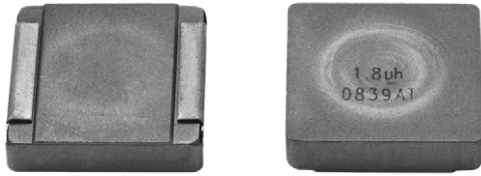



[www.vishay.com](http://www.vishay.com)

IHLP-6767DZ-01

Vishay Dale

## IHLP® Commercial Inductors, High Saturation Series



### LINKS TO ADDITIONAL RESOURCES



### FEATURES

- Shielded construction
- Frequency range up to 2.0 MHz
- Lowest DCR/ $\mu$ H, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- IHLP design;  
PATENT(S): [www.vishay.com/patents](http://www.vishay.com/patents)
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



**RoHS**  
COMPLIANT  
**HALOGEN**  
**FREE**  
**GREEN**  
(5-2008)

### APPLICATIONS

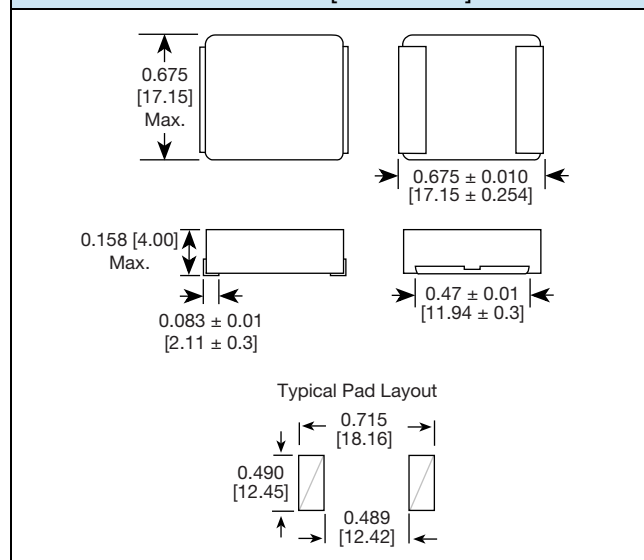
- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for Field Programmable Gate Array (FPGA)

STANDARD ELECTRICAL SPECIFICATIONS				
$L_0$ INDUCTANCE $\pm 20\%$ AT 100 kHz, 0.25 V, 0 A ( $\mu$ H)	DCR TYP. 25 °C (m $\Omega$ )	DCR MAX. 25 °C (m $\Omega$ )	HEAT RATING CURRENT DC TYP. (A) <sup>(1)</sup>	SATURATION CURRENT DC TYP. (A) <sup>(2)</sup>
0.22	0.80	0.88	75.0	92.0
0.33	1.16	1.28	56.0	82.0
0.47	1.31	1.38	49.0	77.0
0.56	1.45	1.52	47.0	62.0
0.68	1.90	2.00	41.0	60.0
0.82	2.17	2.28	38.5	51.0
1.0	2.53	2.66	31.5	58.0
1.5	4.50	4.73	23.5	40.0
2.2	6.10	6.40	19.0	30.0
3.3	9.06	9.51	18.5	28.0
4.7	10.70	11.20	16.0	27.0
5.6	13.40	14.10	14.0	26.0
6.8	15.20	16.00	13.2	21.0
8.2	16.80	17.60	11.5	20.0
10.0	24.40	25.60	10.5	19.5

#### Notes

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- Rated operating voltage (across inductor) = 75 V
- (1) DC current (A) that will cause an approximate  $\Delta T$  of 40 °C
- (2) DC current (A) that will cause  $L_0$  to drop approximately 20 %

### DIMENSIONS in inches [millimeters]



### DESCRIPTION

IHLP-6767DZ-01	4.7 $\mu$ H	$\pm 20\%$	ER	e3
MODEL	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC® LEAD (Pb)-FREE STANDARD

### GLOBAL PART NUMBER

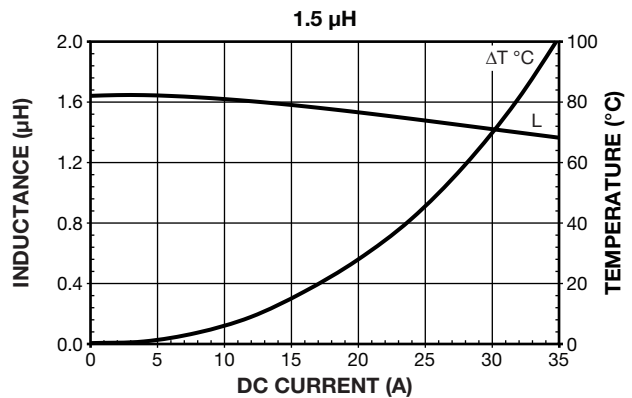
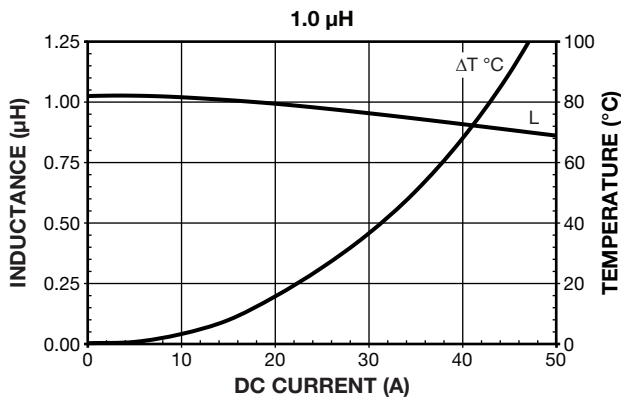
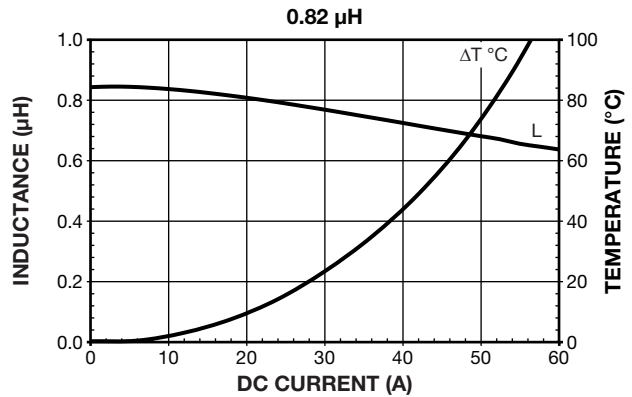
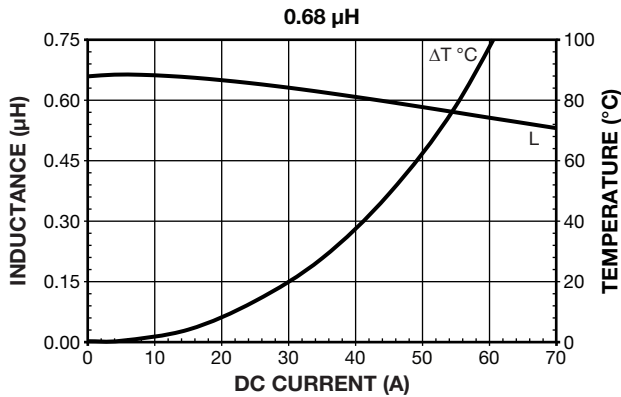
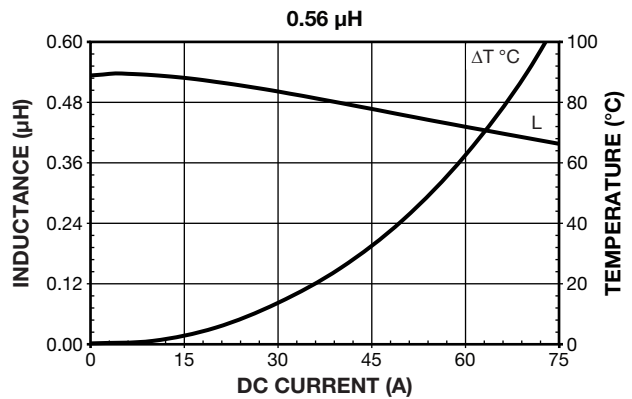
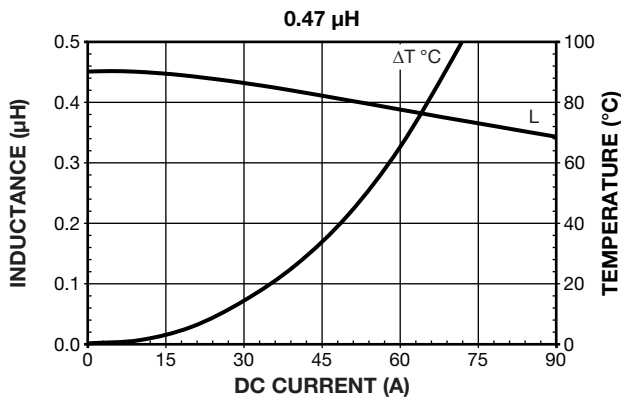
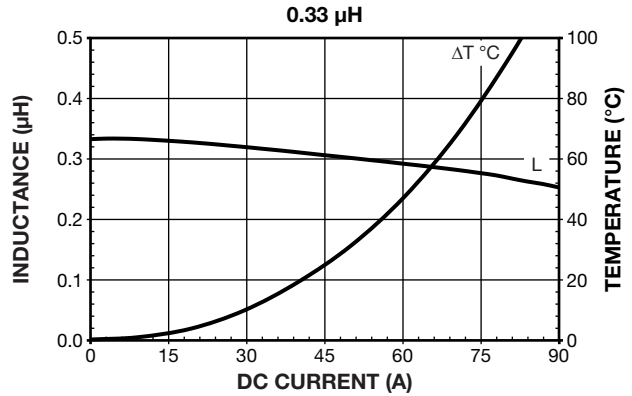
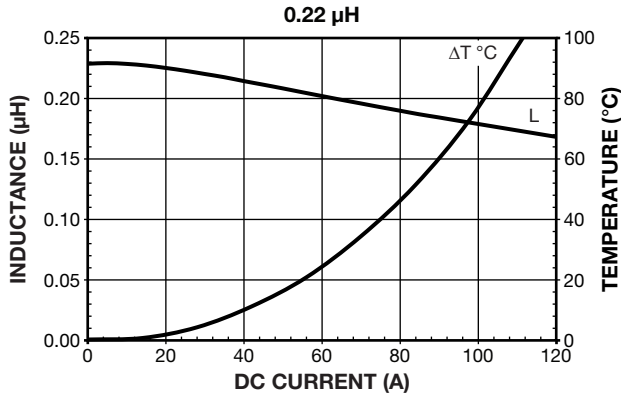
I	H	L	P	6	7	6	7	D	Z	E	R	4	R	7	M	0	1
PRODUCT FAMILY				SIZE				PACKAGE CODE		INDUCTANCE VALUE		TOL.	SERIES				

PATENT(S): [www.vishay.com/patents](http://www.vishay.com/patents)

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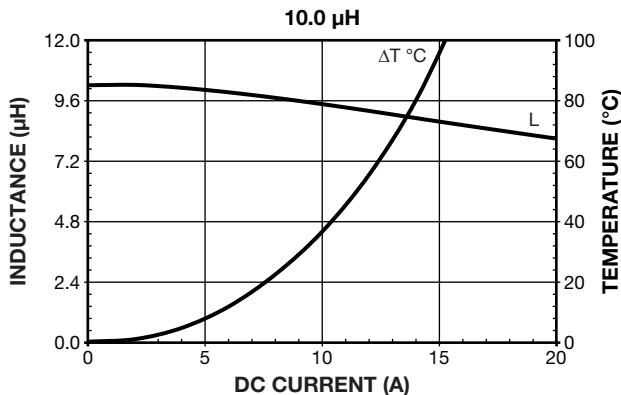
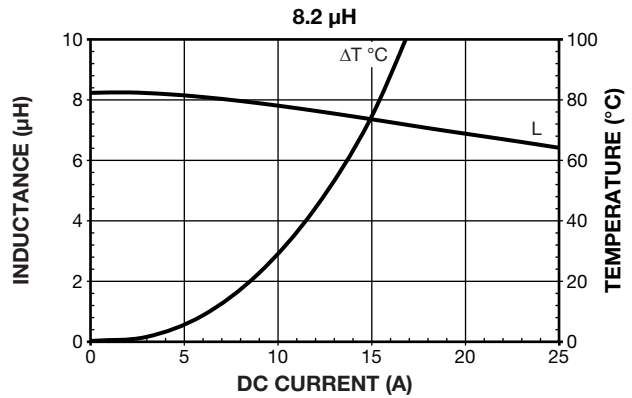
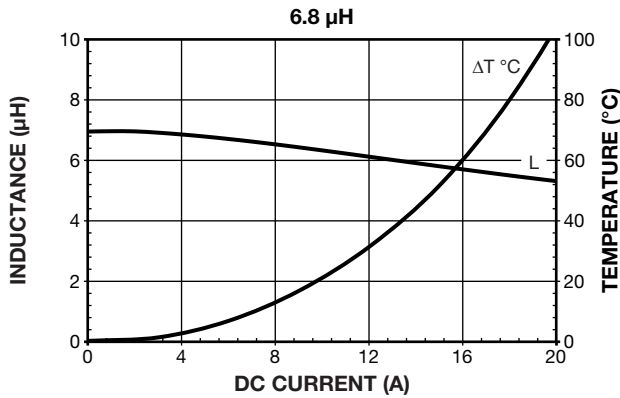
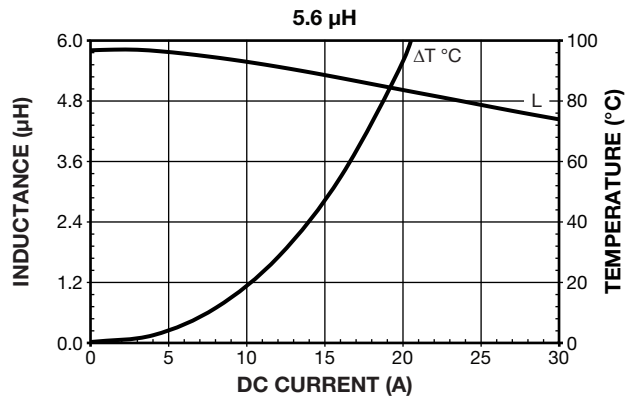
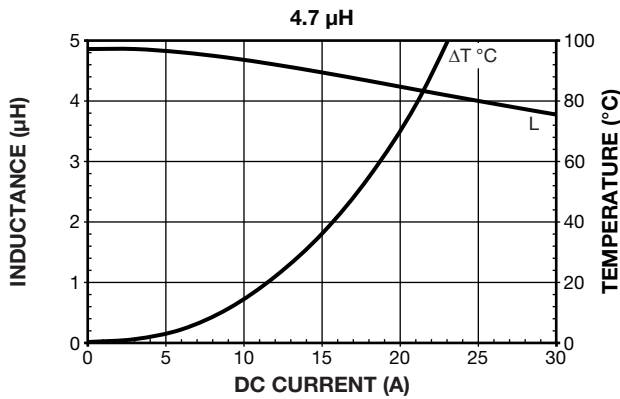
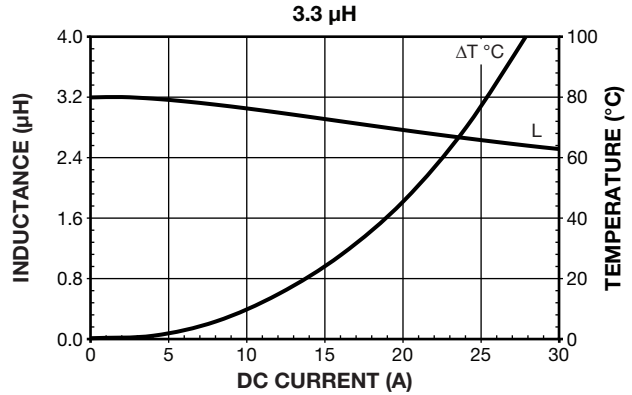
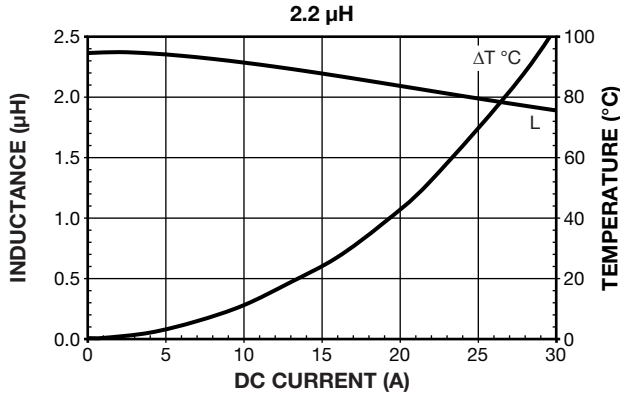


**PERFORMANCE GRAPHS**





**PERFORMANCE GRAPHS**





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