# IHLW4040CFERR56M11 Datasheet

| | |
|---|---|
| DiGi Electronics Part Number | IHLW4040CFERR56M11-DG |
| Manufacturer | Vishay Dale |
| Manufacturer Product Number | IHLW4040CFERR56M11 |
| Description | FIXED IND 560NH 30.5A 1.81 MOHM |
| Detailed Description | 560 nH Shielded Inductor 30.5 A 1.81mOhm Max Nonstandard |

https://www.DiGi-Electronics.com

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## Purchase and inquiry

| Manufacturer Product Number: | Manufacturer: |
| --- | --- |
| IHLW4040CFERR56M11 | Vishay Dale |
| Series: | Product Status: |
| IHLW-4040CF-11 | Active |
| Type: | Material - Core: |
| - | - |
| Inductance: | Tolerance: |
| 560 nH | ±20% |
| Current Rating (Amps): | Current - Saturation (Isat): |
| 30.5 A | 23A |
| Shielding: | DC Resistance (DCR): |
| Shielded | 1.81mOhm Max |
| Q @ Freq: | Frequency - Self Resonant: |
| - | - |
| Ratings: | Operating Temperature: |
| - | -55°C ~ 125°C |
| Inductance Frequency - Test: | Mounting Type: |
| 100 kHz | Surface Mount |
| Package / Case: | Supplier Device Package: |
| Nonstandard | - |
| Size / Dimension: | Height - Seated (Max): |
| 0.400" L x 0.400" W (10.16mm x 10.16mm) | 0.142" (3.60mm) |

## Environmental & Export classification

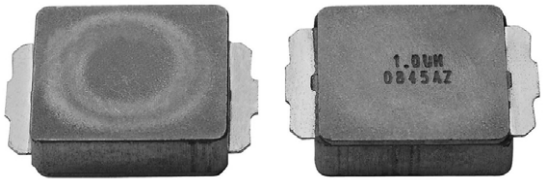
| RoHS Status: | Moisture Sensitivity Level (MSL): |
| --- | --- |
| ROHS3 Compliant | 1 (Unlimited) |
| REACH Status: | ECCN: |
| REACH Unaffected | EAR99 |
| HTSUS: | |
| 8504.50.4000 | |

VISHAY
www.vishay.com

# IHLW-4040CF-11

Vishay Dale

## Low Profile, High Current Inductor - Winged Terminals




### FEATURES

- Shielded construction
- Frequency range up to 1.0 MHz
- Lowest DCR/µH, in this package size
- Handles high transient current spikes without saturation
- Ultra low buzz noise, due to composite construction
- IHLP design; PATENT(S): www.vishay.com/patents
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Pb-free
RoHS COMPLIANT
HALOGEN FREE

### LINKS TO ADDITIONAL RESOURCES

3D Models
Calculators

### APPLICATIONS

- PDA / notebook / desktop / server applications
- High current POL converters
- Low profile, high current power supplies
- Battery powered devices
- DC/DC converters in distributed power systems
- DC/DC converter for field programmable gate array (FPGA)

### STANDARD ELECTRICAL SPECIFICATIONS

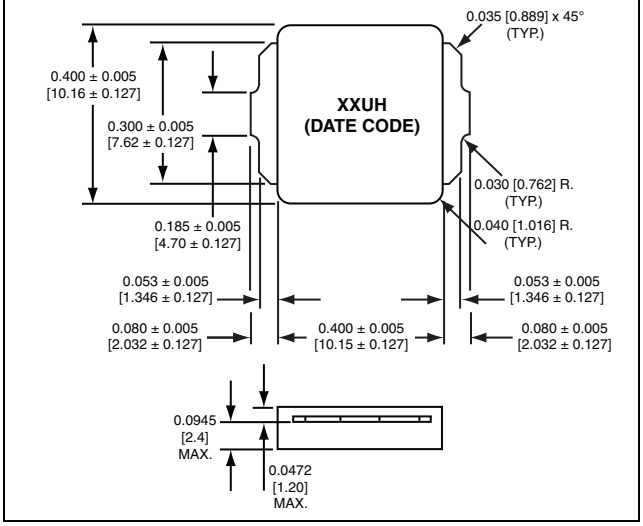
| $L_0$ INDUCTANCE ± 20 % AT 100 kHz, 0.25 V, 0 A (µH) | DCR TYP. 25 °C (mΩ) | DCR MAX. 25 °C (mΩ) | HEAT RATING CURRENT DC TYP. (A) (1) | SATURATION CURRENT DC TYP. (A) (2) |
|---|---|---|---|---|
| 0.10 | 0.70 | 0.80 | 46 | 48 |
| 0.15 | 0.75 | 0.85 | 55 | 38 |
| 0.22 | 0.83 | 0.90 | 35.5 | 36 |
| 0.33 | 1.09 | 1.18 | 33.5 | 26 |
| 0.47 | 1.60 | 1.69 | 31 | 22 |
| 0.56 | 1.71 | 1.81 | 30.5 | 23 |
| 0.68 | 2.05 | 2.16 | 29 | 20 |
| 0.82 | 2.46 | 2.60 | 24 | 19 |
| 1.0 | 2.67 | 2.82 | 24 | 18 |
| 1.5 | 4.20 | 4.43 | 20 | 14.5 |
| 2.2 | 6.83 | 7.21 | 16 | 14 |

**Notes**

- All test data is referenced to 25 °C ambient
- Operating temperature range -55 °C to +125 °C
- The part temperature (ambient + temp. rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component placement, PWB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application
- Rated operating voltage (across inductor) = 50 V

(1) DC current (A) that will cause an approximate ΔT of 40 °C
(2) DC current (A) that will cause $L_0$ to drop approximately 20 %

### DIMENSIONS in inches [millimeters]




### DESCRIPTION

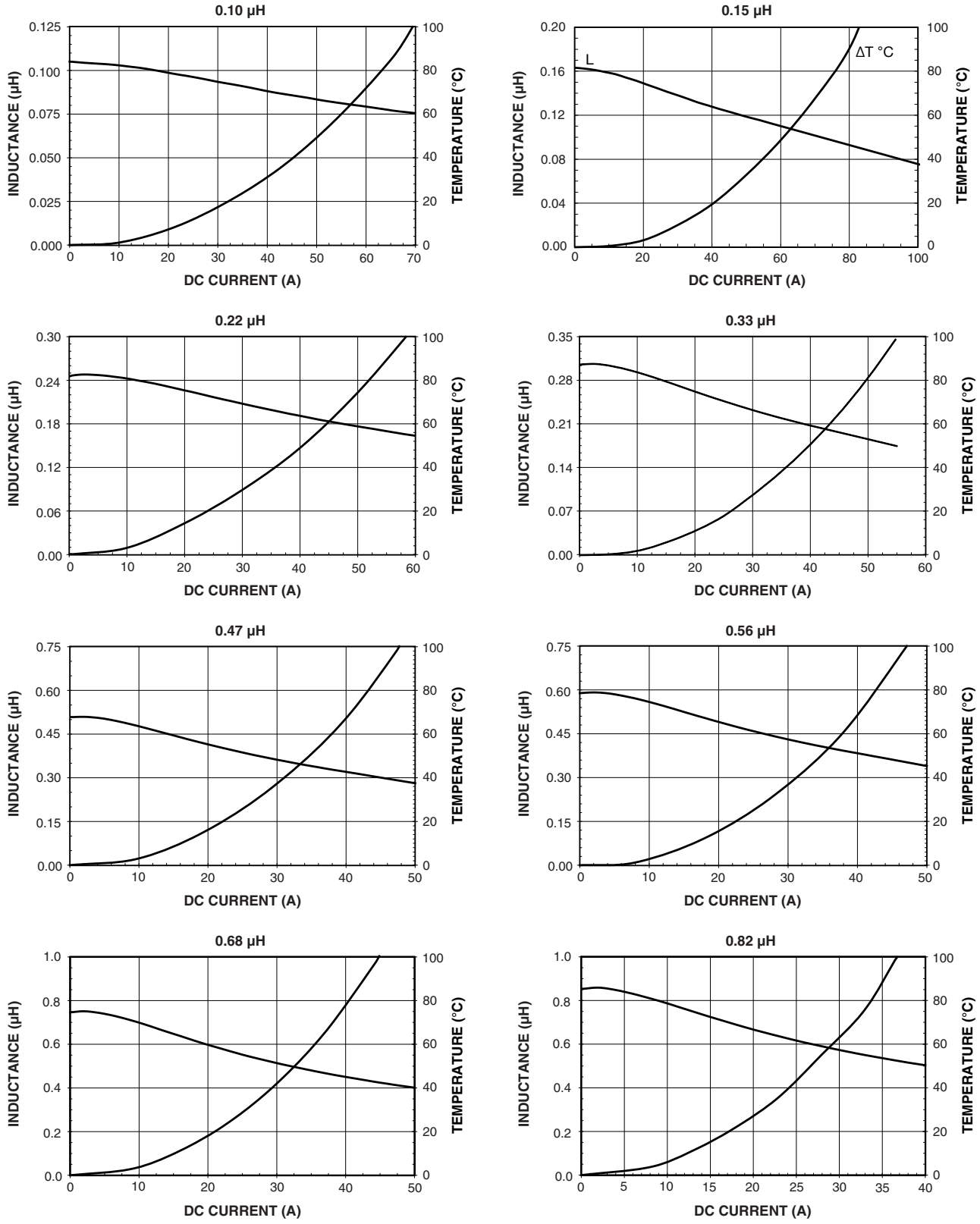
| IHLW-4040CF-11 | 1.0 µH | ± 20 % | ER | e3 |
|---|---|---|---|---|
| MODEL | INDUCTANCE VALUE | INDUCTANCE TOLERANCE | PACKAGE CODE | JEDEC® LEAD (Pb)-FREE STANDARD |

### GLOBAL PART NUMBER

| I | H | L | W | 4 | 0 | 4 | 0 | C | F | E | R | 1 | R | 0 | M | 1 | 1 |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| PRODUCT FAMILY | | | | SIZE | | | | | | PACKAGE CODE | | INDUCTANCE VALUE | | | TOL. | SERIES | |

**PATENT(S): www.vishay.com/patents**
**This Vishay product is protected by one or more United States and international patents.**

## PERFORMANCE GRAPHS



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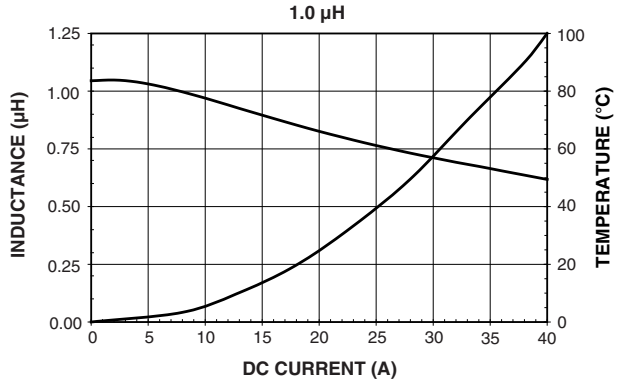


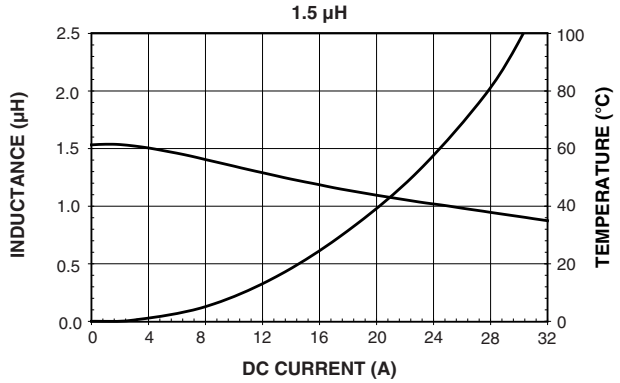


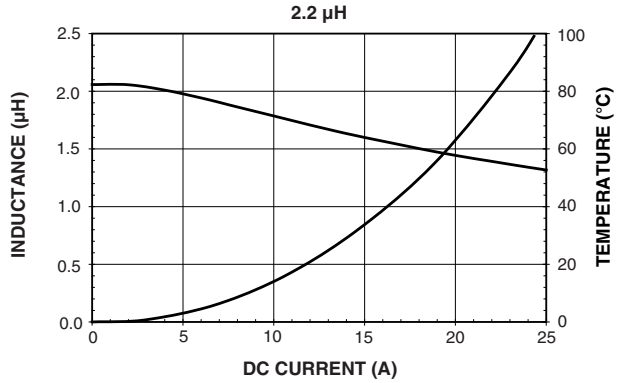

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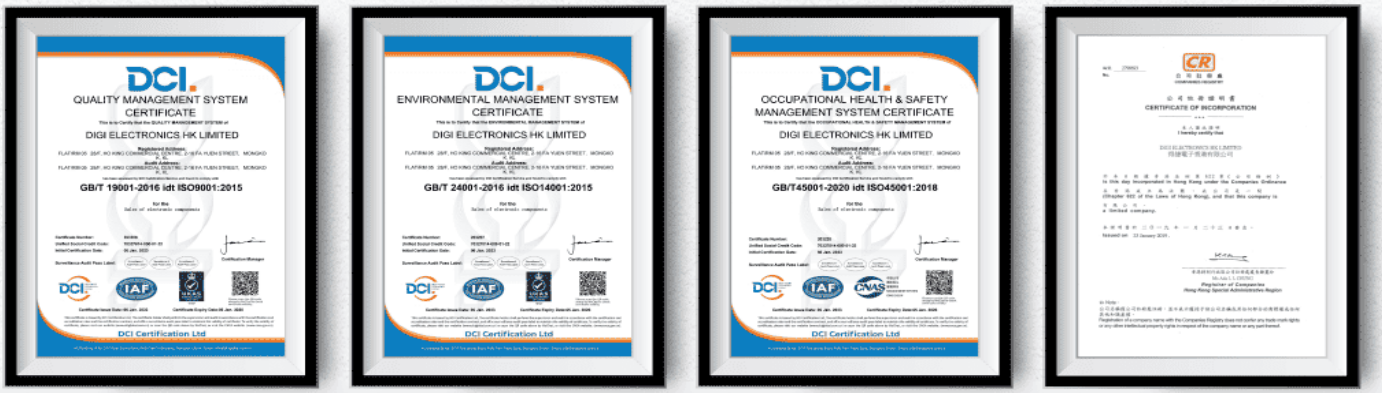



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