

# VS-VSKV71/10 Datasheet



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DiGi Electronics Part Number	VS-VSKV71/10-DG
Manufacturer	<a href="#">Vishay General Semiconductor - Diodes Division</a>
Manufacturer Product Number	VS-VSKV71/10
Description	MODULE THYRISTOR 75A ADD-A-PAK
Detailed Description	SCR Module 1 kV 115 A Common Anode - All SCRs C hassis Mount ADD-A-PAK (3 + 4)

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## Purchase and inquiry

Manufacturer Product Number:

VS-VSKV71/10

Series:

-

Structure:

Common Anode - All SCRs

Voltage - Off State:

1 kV

Current - On State (It (RMS)) (Max):

115 A

Current - Gate Trigger (Igt) (Max):

150 mA

Current - Hold (Ih) (Max):

250 mA

Mounting Type:

Chassis Mount

Base Product Number:

VSKV71

Manufacturer:

Vishay General Semiconductor - Diodes Division

Product Status:

Active

Number of SCRs, Diodes:

2 SCRs

Current - On State (It (AV)) (Max):

75 A

Voltage - Gate Trigger (Vgt) (Max):

2.5 V

Current - Non Rep. Surge 50, 60Hz (I<sub>tsm</sub>):

1300A, 1360A

Operating Temperature:

-40°C ~ 125°C (Tj)

Package / Case:

ADD-A-PAK (3 + 4)

## Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.30.0080

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99




## AAP Gen 7 (TO-240AA) Power Modules Thyristor/Thyristor, 75 A



ADD-A-PAK

RoHS  
COMPLIANT

### FEATURES

- High voltage
- Industrial standard package
- Low thermal resistance
- UL approved file E78996 
- Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)

### BENEFITS

- Excellent thermal performances obtained by the usage of exposed direct bonded copper substrate
- Up to 1600 V
- High surge capability
- Easy mounting on heatsink

PRIMARY CHARACTERISTICS	
$I_{T(AV)}$	75 A
Type	Modules - thyristor, standard
Package	AAP Gen 7 (TO-240AA)

### MECHANICAL DESCRIPTION

The AAP Gen 7 (TO-240AA), new generation of AAP module, combines the excellent thermal performances obtained by the usage of exposed direct bonded copper substrate, with advanced compact simple package solution and simplified internal structure with minimized number of interfaces.

### ELECTRICAL DESCRIPTION

These modules are intended for general purpose high voltage applications such as high voltage regulated power supplies, lighting circuits, temperature and motor speed control circuits, UPS and battery charger.

MAJOR RATINGS AND CHARACTERISTICS			
SYMBOL	CHARACTERISTICS	VALUES	UNITS
$I_{T(AV)}$	85 °C	75	A
$I_{T(RMS)}$		115	
$I_{TSM}$	50 Hz	1300	
	60 Hz	1360	
$I^2t$	50 Hz	8.45	kA <sup>2</sup> s
	60 Hz	7.68	
$I^2\sqrt{t}$		84.5	kA <sup>2</sup> √s
$V_{RRM}$	Range	400 to 1600	V
$T_{Stg}$		-40 to +125	°C
$T_J$		-40 to +125	°C



## ELECTRICAL SPECIFICATIONS

VOLTAGE RATINGS					
TYPE NUMBER	VOLTAGE CODE	V <sub>RRM</sub> , MAXIMUM REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>RSM</sub> , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	V <sub>DRM</sub> , MAXIMUM REPETITIVE PEAK OFF-STATE VOLTAGE, GATE OPEN CIRCUIT V	I <sub>RRM</sub> , I <sub>DRM</sub> AT 125 °C mA
VS-VSK.71	04	400	500	400	15
	08	800	900	800	
	12	1200	1300	1200	
	16	1600	1700	1600	

ON-STATE CONDUCTION						
PARAMETER	SYMBOL	TEST CONDITIONS			VALUES	UNITS
Maximum average on-state current	I <sub>T(AV)</sub>	180° conduction, half sine wave, T <sub>C</sub> = 85 °C			75	A
Maximum continuous RMS on-state current	I <sub>T(RMS)</sub>	DC			115	
		T <sub>C</sub>			80	°C
Maximum peak, one-cycle non-repetitive on-state current	I <sub>TSM</sub>	t = 10 ms	No voltage reapplied	Sinusoidal half wave, initial T <sub>J</sub> = T <sub>J</sub> maximum	1300	A
		t = 8.3 ms			1360	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		1093	
		t = 8.3 ms			1140	
Maximum I <sup>2</sup> t for fusing	I <sup>2</sup> t	t = 10 ms	No voltage reapplied	Initial T <sub>J</sub> = T <sub>J</sub> maximum	8.45	kA <sup>2</sup> s
		t = 8.3 ms			7.68	
		t = 10 ms	100 % V <sub>RRM</sub> reapplied		5.97	
		t = 8.3 ms			5.45	
Maximum I <sup>2</sup> √t for fusing	I <sup>2</sup> √t (1)	t = 0.1 ms to 10 ms, no voltage reapplied T <sub>J</sub> = T <sub>J</sub> maximum			84.5	kA <sup>2</sup> √s
Maximum value of threshold voltage	V <sub>T(TO)</sub> (2)	Low level (3)	T <sub>J</sub> = T <sub>J</sub> maximum		0.96	V
		High level (4)			1.08	
Maximum value of on-state slope resistance	r <sub>t</sub> (2)	Low level (3)	T <sub>J</sub> = T <sub>J</sub> maximum		3.28	mΩ
		High level (4)			2.86	
Maximum on-state voltage drop	V <sub>TM</sub>	I <sub>TM</sub> = π × I <sub>T(AV)</sub>	T <sub>J</sub> = 25 °C		1.72	V
Maximum non-repetitive rate of rise of turned on current	di/dt	T <sub>J</sub> = 25 °C, from 0.67 V <sub>DRM</sub> , I <sub>TM</sub> = π × I <sub>T(AV)</sub> , I <sub>g</sub> = 500 mA, t <sub>r</sub> < 0.5 μs, t <sub>p</sub> > 6 μs			150	A/μs
Maximum holding current	I <sub>H</sub>	T <sub>J</sub> = 25 °C, anode supply = 6 V, resistive load, gate open circuit			250	mA
Maximum latching current	I <sub>L</sub>	T <sub>J</sub> = 25 °C, anode supply = 6 V, resistive load			400	

## Notes

- (1) I<sup>2</sup>t for time t<sub>x</sub> = I<sup>2</sup>√t × √t<sub>x</sub>  
(2) Average power = V<sub>T(TO)</sub> × I<sub>T(AV)</sub> + r<sub>t</sub> × (I<sub>T(RMS)</sub>)<sup>2</sup>  
(3) 16.7 % × π × I<sub>AV</sub> < I < π × I<sub>AV</sub>  
(4) I > π × I<sub>AV</sub>



TRIGGERING					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum peak gate power	$P_{GM}$			12	W
Maximum average gate power	$P_{G(AV)}$			3.0	
Maximum peak gate current	$I_{GM}$			3.0	A
Maximum peak negative gate voltage	$-V_{GM}$			10	V
Maximum gate voltage required to trigger	$V_{GT}$	$T_J = -40\text{ }^\circ\text{C}$	Anode supply = 6 V resistive load	4.0	
		$T_J = 25\text{ }^\circ\text{C}$		2.5	
		$T_J = 125\text{ }^\circ\text{C}$		1.7	
Maximum gate current required to trigger	$I_{GT}$	$T_J = -40\text{ }^\circ\text{C}$	Anode supply = 6 V resistive load	270	mA
		$T_J = 25\text{ }^\circ\text{C}$		150	
		$T_J = 125\text{ }^\circ\text{C}$		80	
Maximum gate voltage that will not trigger	$V_{GD}$	$T_J = 125\text{ }^\circ\text{C}$ , rated $V_{DRM}$ applied		0.25	V
Maximum gate current that will not trigger	$I_{GD}$	$T_J = 125\text{ }^\circ\text{C}$ , rated $V_{DRM}$ applied		6	mA

BLOCKING					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum peak reverse and off-state leakage current at $V_{RRM}$ , $V_{DRM}$	$I_{RRM}$ , $I_{DRM}$	$T_J = 125\text{ }^\circ\text{C}$ , gate open circuit		15	mA
Maximum RMS insulation voltage	$V_{INS}$	50 Hz		3000 (1 min) 3600 (1 s)	V
Maximum critical rate of rise of off-state voltage	$dV/dt$	$T_J = 125\text{ }^\circ\text{C}$ , linear to $0.67 V_{DRM}$		1000	V/ $\mu$ s

THERMAL AND MECHANICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Junction operating and storage temperature range	$T_J$ , $T_{Stg}$			-40 to +125	$^\circ\text{C}$
Maximum internal thermal resistance, junction to case per leg	$R_{thJC}$	DC operation		0.29	$^\circ\text{C/W}$
Typical thermal resistance, case to heatsink per module	$R_{thCS}$	Mounting surface flat, smooth and greased		0.1	
Mounting torque $\pm 10\%$	to heatsink	A mounting compound is recommended and the torque should be rechecked after a period of 3 hours to allow for the spread of the compound.		4	Nm
	busbar			3	
Approximate weight				75	g
				2.7	oz.
Case style			JEDEC®	AAP Gen 7 (TO-240AA)	

$\Delta R$ CONDUCTION PER JUNCTION											
DEVICES	SINE HALF WAVE CONDUCTION					RECTANGULAR WAVE CONDUCTION					UNITS
	180°	120°	90°	60°	30°	180°	120°	90°	60°	30°	
VSK.71..	0.052	0.062	0.079	0.116	0.197	0.037	0.064	0.085	0.121	0.200	$^\circ\text{C/W}$

**Note**

- Table shows the increment of thermal resistance  $R_{thJC}$  when devices operate at different conduction angles than DC



# VS-VSKU71.., VS-VSKV71.. Series

Vishay Semiconductors

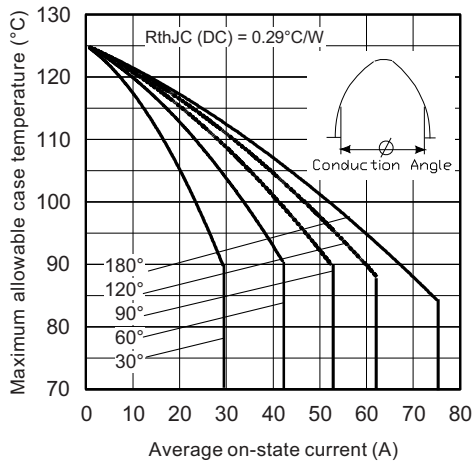


Fig. 1 - Current Ratings Characteristics

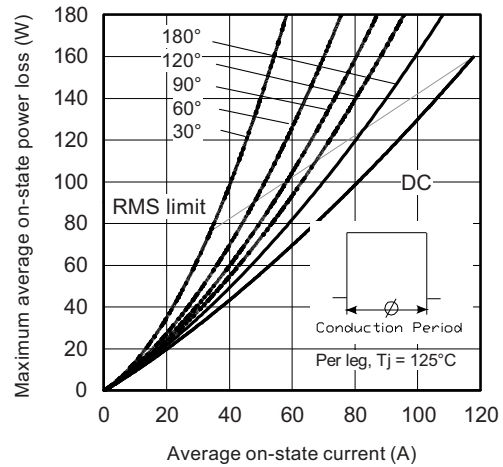


Fig. 4 - On-State Power Loss Characteristics

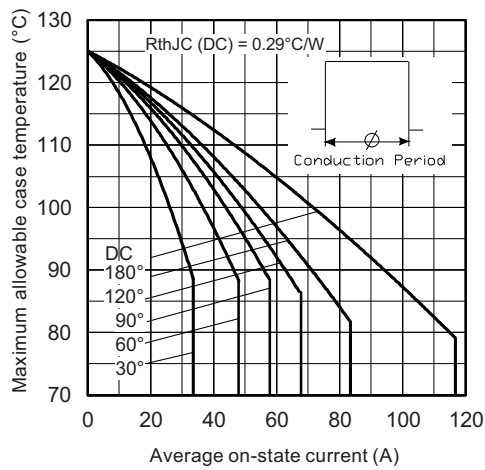


Fig. 2 - Current Ratings Characteristics

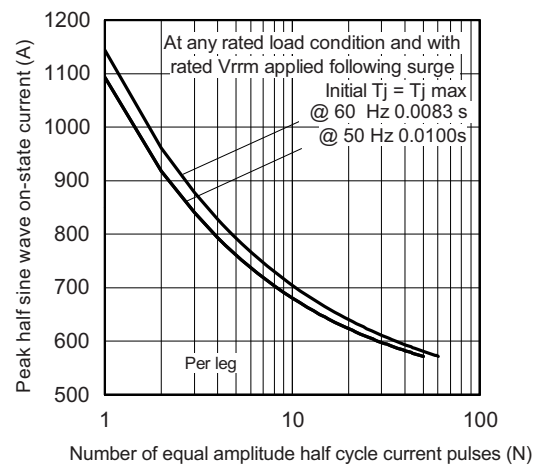


Fig. 5 - Maximum Non-Repetitive Surge Current

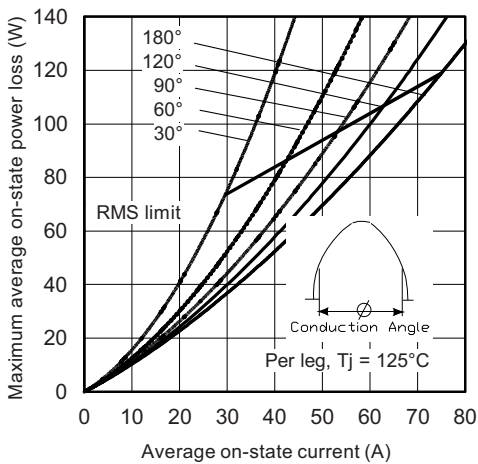


Fig. 3 - On-State Power Loss Characteristics

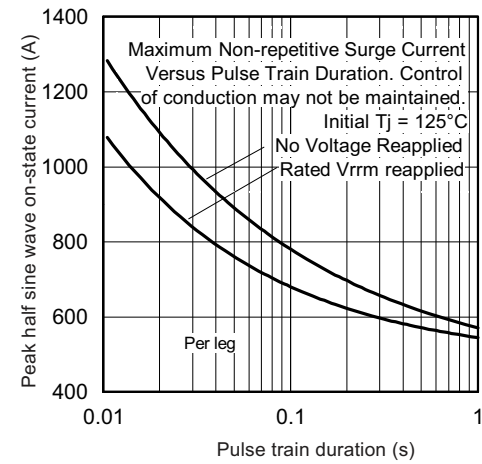


Fig. 6 - Maximum Non-Repetitive Surge Current



# VS-VSKU71., VS-VSKV71.. Series

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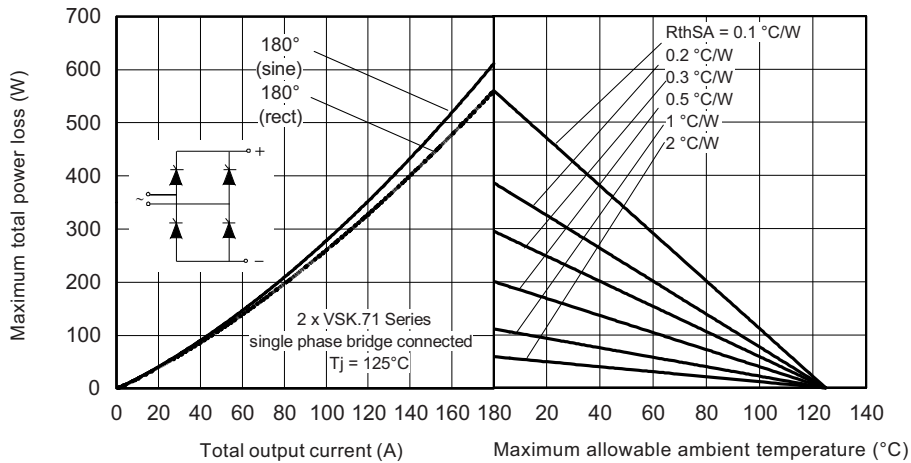


Fig. 7 - On-State Power Loss Characteristics

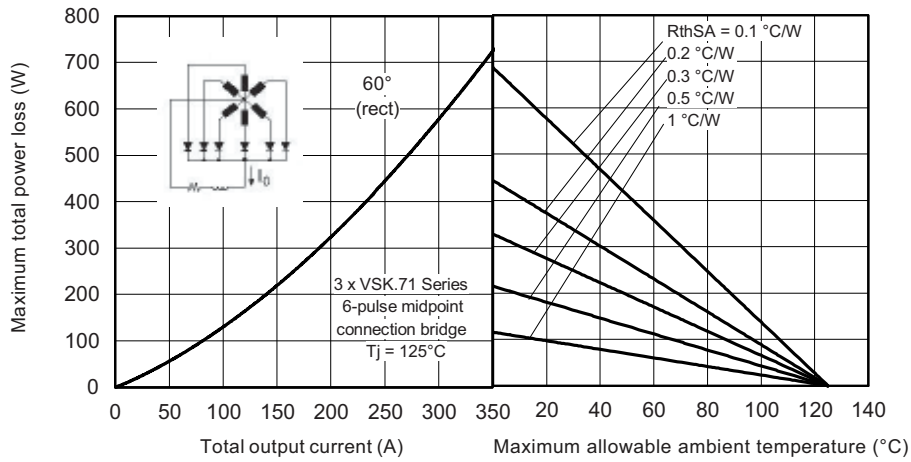


Fig. 8 - On-State Power Loss Characteristics

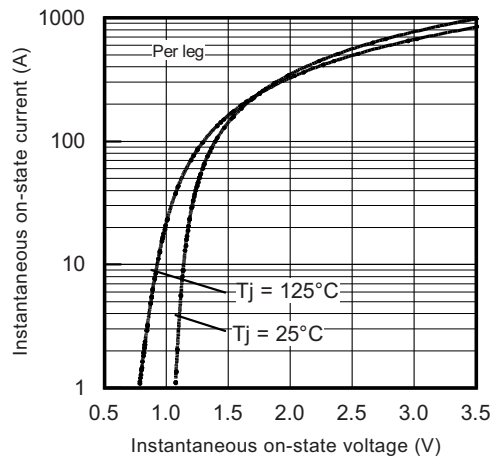


Fig. 9 - On-State Voltage Characteristics

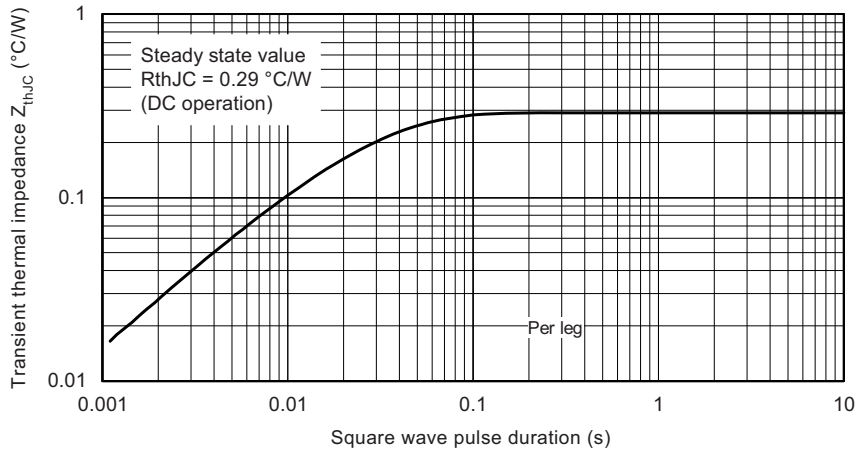


Fig. 10 - Thermal Impedance  $Z_{thJC}$  Characteristics

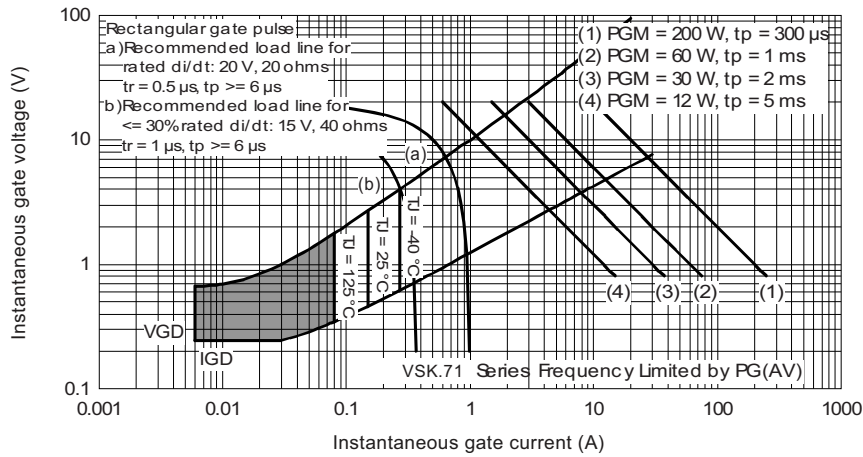


Fig. 11 - Gate Characteristics

**ORDERING INFORMATION TABLE**

Device code	<b>VS-VS</b>	<b>K</b>	<b>U</b>	<b>71</b>	<b>/</b>	<b>16</b>
	①	②	③	④		⑤
	<b>1</b>	<b>2</b>	<b>3</b>	<b>4</b>	<b>5</b>	
	-	-	-	-	-	
	Vishay Semiconductors product					
	Module type					
	Circuit configuration (see Circuit Configuration table)					
	Current code (75 A)					
	Voltage code (see Voltage Ratings table)					

**Note**

- To order the optional hardware go to [www.vishay.com/doc?95172](http://www.vishay.com/doc?95172)



www.vishay.com

# VS-VSKU71.., VS-VSKV71.. Series

Vishay Semiconductors

CIRCUIT CONFIGURATION		
CIRCUIT DESCRIPTION	CIRCUIT CONFIGURATION CODE	CIRCUIT DRAWING
Two SCRs common cathodes	U	<p><b>VSKU</b></p>
Two SCRs common anodes	V	<p><b>VSKV</b></p>
LINKS TO RELATED DOCUMENTS		
Dimensions	<a href="http://www.vishay.com/doc?95368">www.vishay.com/doc?95368</a>	

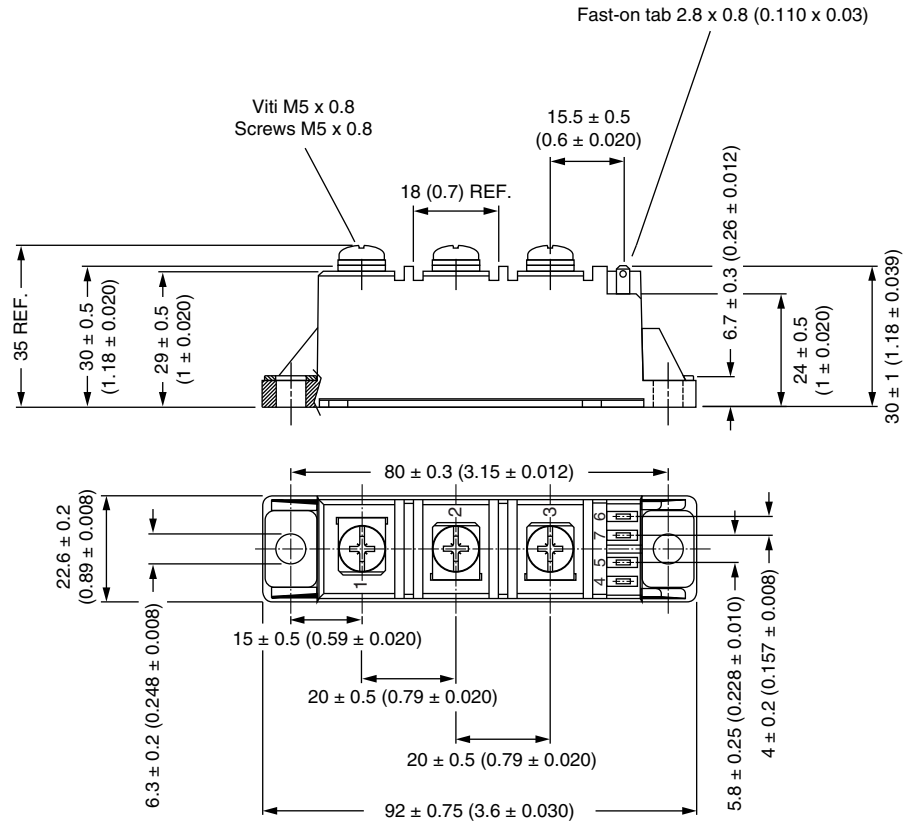


# Outline Dimensions

Vishay Semiconductors

## ADD-A-PAK Generation VII - Thyristor

**DIMENSIONS** in millimeters (inches)





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