

2N7002AQ-7 Datasheet



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DiGi Electronics Part Number	2N7002AQ-7-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	2N7002AQ-7
Description	MOSFET N-CH 60V 180MA SOT23
Detailed Description	N-Channel 60 V 180mA (Ta) 370mW (Ta) Surface Mount SOT-23-3



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Purchase and inquiry

Manufacturer Product Number:

2N7002AQ-7

Series:

-

FET Type:

N-Channel

Drain to Source Voltage (Vdss):

60 V

Drive Voltage (Max Rds On, Min Rds On):

5V

Vgs(th) (Max) @ Id:

2V @ 250µA

Input Capacitance (Ciss) (Max) @ Vds:

23 pF @ 25 V

Power Dissipation (Max):

370mW (Ta)

Grade:

Automotive

Mounting Type:

Surface Mount

Package / Case:

TO-236-3, SC-59, SOT-23-3

Manufacturer:

Diodes Incorporated

Product Status:

Active

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

180mA (Ta)

Rds On (Max) @ Id, Vgs:

50hm @ 115mA, 10V

Vgs (Max):

±20V

FET Feature:

-

Operating Temperature:

-55°C ~ 150°C (TJ)

Qualification:

AEC-Q101

Supplier Device Package:

SOT-23-3

Base Product Number:

2N7002

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.21.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

Product Summary

$V_{(BR)DSS}$	$R_{DS(ON) \max}$	$I_D \max$ $T_A = +25^\circ\text{C}$
60V	$6\Omega @ V_{GS} = 5V$	200mA

Description and Applications

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

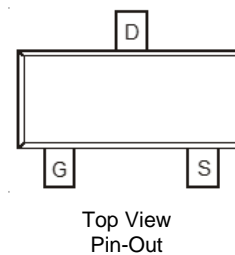
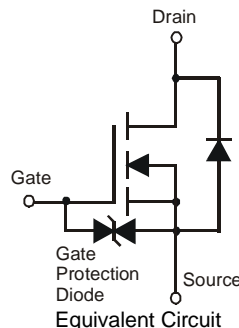
- Motor Control
- Power Management Functions



ESD PROTECTED TO 1.2kV



Top View

Top View
Pin-Out

Features and Benefits

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Small Surface Mount Package
- ESD Protected Gate, 1.2kV HBM
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen- and Antimony-Free. "Green" Device (Note 3)**
- **The 2N7002AQ is suitable for automotive applications requiring specific change control; this part is AEC-Q101 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**

<https://www.diodes.com/quality/product-definitions/>

Mechanical Data

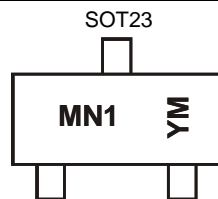
- Case: SOT23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—Matte Tin Annealed over Alloy 42 Lead-Frame. Solderable per MIL-STD-202, Method 208 ⁽³⁾
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)

Ordering Information (Note 4)

Part Number	Case	Packaging
2N7002AQ-7	SOT23	3,000/Tape & Reel
2N7002AQ-13	SOT23	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



MN1 = Product Type Marking Code
 YM = Date Code Marking
 Y or \bar{Y} = Year (ex: H = 2020)
 M = Month (ex: 9 = September)

Date Code Key

Year	2015	...	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029
Code	C	...	H	I	J	K	L	M	N	O	P	R
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D


Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V_{DSS}	60	V
Gate-Source Voltage			V_{GSS}	± 20	V
Continuous Drain Current (Note 5) $V_{GS} = 10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	180	mA
		$T_A = +85^\circ\text{C}$		130	
		$T_A = +100^\circ\text{C}$		115	
Continuous Drain Current (Note 6) $V_{GS} = 10\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	220	mA
		$T_A = +85^\circ\text{C}$		160	
		$T_A = +100^\circ\text{C}$		140	
Maximum Continuous Body Diode Forward Current (Note 6)			I_S	220	mA
Pulsed Drain Current (10 μs pulse, duty cycle = 1%)			I_{DM}	800	mA

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation	(Note 5)	P_D	370	mW
	(Note 6)		540	
Thermal Resistance, Junction to Ambient	(Note 5)	$R_{\theta JA}$	348	$^\circ\text{C/W}$
	(Note 6)		241	
Thermal Resistance, Junction to Case	(Note 6)	$R_{\theta JC}$	91	$^\circ\text{C/W}$
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	70	—	V	$V_{GS} = 0\text{V}, I_D = 10\mu\text{A}$
Zero Gate Voltage Drain Current @ $T_C = +25^\circ\text{C}$ @ $T_C = +125^\circ\text{C}$	I_{DSS}	—	—	1.0 500	μA	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$
Gate-Body Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(th)}$	1.2	—	2.0	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance @ $T_J = +25^\circ\text{C}$ @ $T_J = +125^\circ\text{C}$	$R_{DS(ON)}$	—	3.5 3.0	6 5	Ω	$V_{GS} = 5.0\text{V}, I_D = 0.115\text{A}$ $V_{GS} = 10\text{V}, I_D = 0.115\text{A}$
Forward Transconductance	g_{FS}	80	—	—	mS	$V_{DS} = 10\text{V}, I_D = 0.115\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	23	—	pF	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	3.4	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	1.4	—	pF	
Gate Resistance	R_G	—	260	400	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1.0\text{MHz}$
SWITCHING CHARACTERISTICS (Note 8)						
Turn-On Delay Time	$t_{D(ON)}$	—	10	—	ns	$V_{DD} = 30\text{V}, I_D = 0.115\text{A}, R_L = 150\Omega, V_{GEN} = 10\text{V}, R_{GEN} = 25\Omega$
Turn-Off Delay Time	$t_{D(OFF)}$	—	33	—	ns	

- Notes:
- Device mounted on FR-4 PCB, with minimum recommended pad layout.
 - Device mounted on 1" x 1" FR-4 PCB with high coverage 2oz. Copper, single sided.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.



2N7002AQ

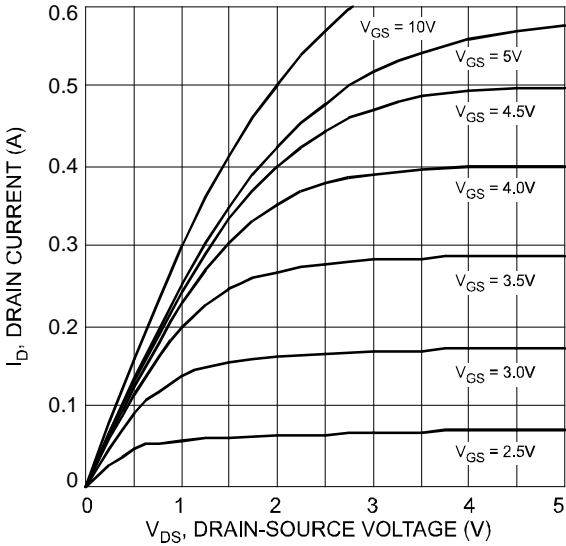


Fig. 1 Typical Output Characteristic

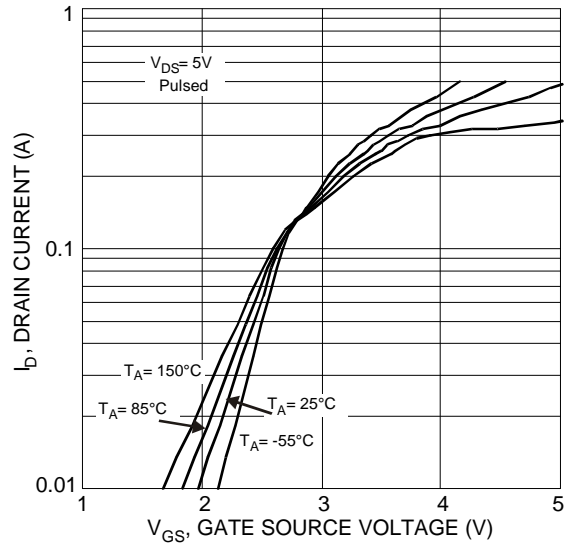


Fig. 2 Typical Transfer Characteristics

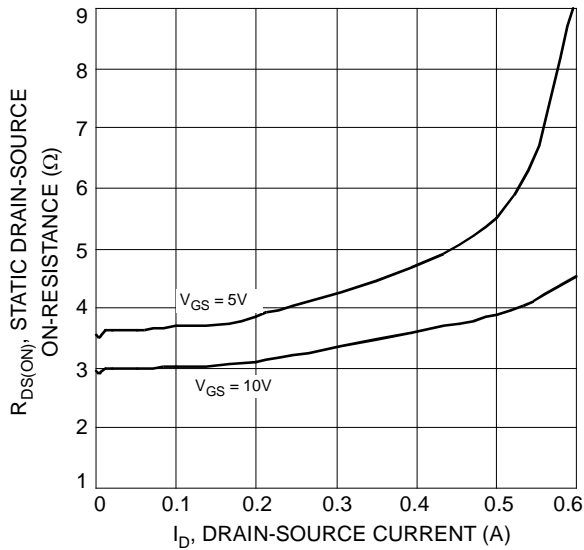


Fig. 3 On-Resistance vs. Drain Current & Gate Voltage

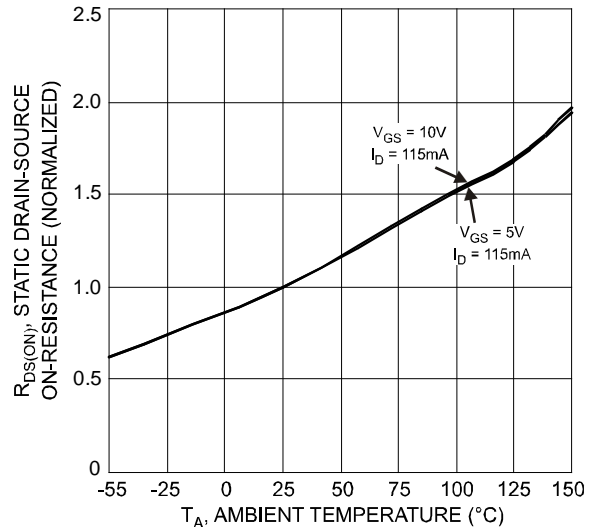


Fig. 4 Normalized Static Drain-Source On-Resistance vs. Ambient Temperature

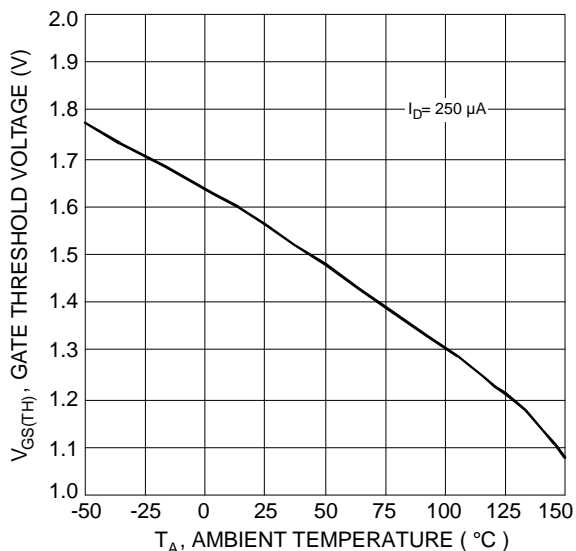


Fig. 5 Gate Threshold Variation vs. Ambient Temperature

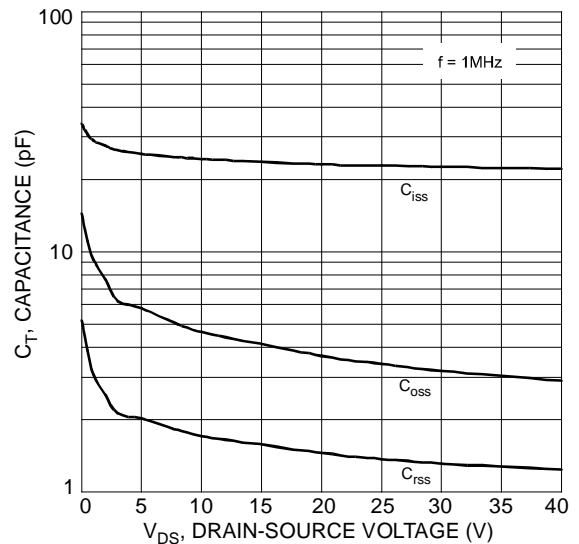


Fig. 6 Typical Total Capacitance



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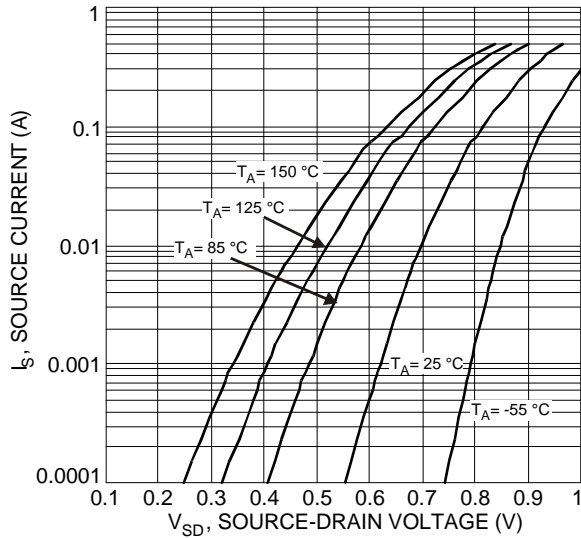
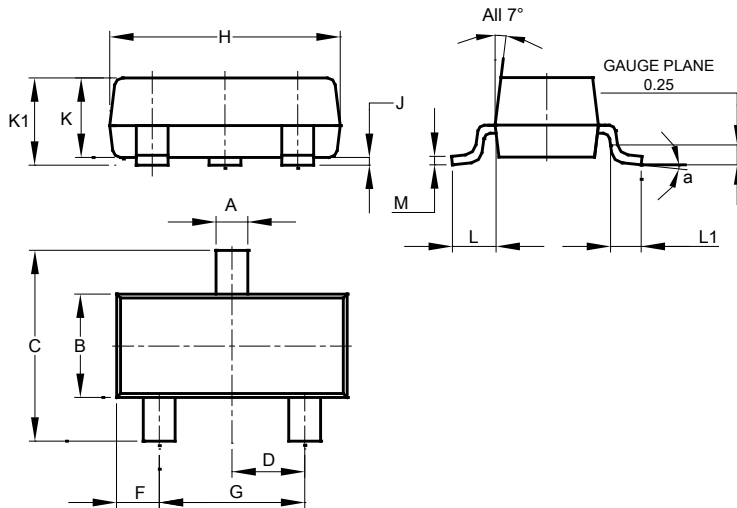


Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

Package Outline Dimensions

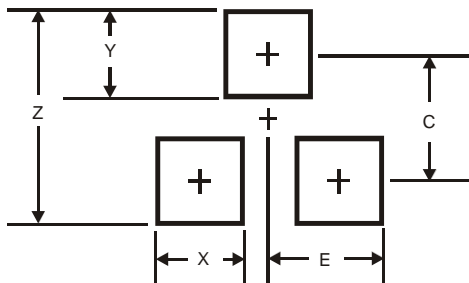
Please see <http://www.diodes.com/package-outlines.html> for the latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	8°		
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35



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