

74AHCT1G04W5-7 Datasheet



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DiGi Electronics Part Number 74AHCT1G04W5-7-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number 74AHCT1G04W5-7

Description IC INVERTER 1CH 1-INP SOT25

Detailed Description Inverter IC 1 Channel SOT-25



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:			
74AHCT1G04W5-7	Diodes Incorporated			
Series:	Product Status:			
74AHCT	Active			
Logic Type:	Number of Circuits:			
Inverter	1			
Number of Inputs:	Features:			
1				
Voltage - Supply:	Current - Quiescent (Max):			
4.5V ~ 5.5V	1 μΑ			
Current - Output High, Low:	Input Logic Level - Low:			
8mA, 8mA	0.8V			
Input Logic Level - High:	Max Propagation Delay @ V, Max CL:			
2V	7.7ns @ 5V, 50pF			
Operating Temperature:	Mounting Type:			
-40°C ~ 125°C	Surface Mount			
Supplier Device Package:	Package / Case:			
SOT-25	SC-74A, SOT-753			
Base Product Number:				
74AHCT1G04				

Environmental & Export classification

8542.39.0001

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



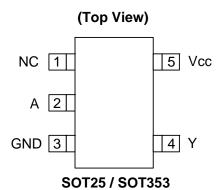
SINGLE INVERTER GATE

Description

The 74AHCT1G04 is a single inverter gate with a standard totem pole output. The device is designed for operation with a power supply range of 4.5V to 5.5V. The gate performs the positive Boolean function:

$$Y=\overline{\boldsymbol{A}}$$

Pin Assignments



Features

- Supply Voltage Range from 4.5V to 5.5V
- ± 8 mA Output Drive at 5.0V
- CMOS low power consumption
- Schmitt Trigger Action at All Inputs Make the Circuit Tolerant for Slower Input Rise and Fall Time.
- ESD Protection per JESD 22
 - o Exceeds 200-V Machine Model (A115-A)
 - o Exceeds 2000-V Human Body Model (A114-A)
 - o Exceeds 1000-V Charged Device Model (C101C)
- Latch-Up Exceeds 100mA per JESD 78, Class II
- SOT25 and SOT353: Assembled with "Green" Molding Compound (no Br, Sb)
- Lead Free Finish / RoHS Compliant (Note 1)

Applications

- General Purpose Logic
- Wide array of products such as:
 - o PCs, networking, notebooks, netbooks, PDAs
 - o Computer peripherals, hard drives, CD/DVD ROM
 - TV, DVD, DVR, set top box
 - Phones, Personal Navigation / GPS
 - o MP3 players ,Cameras, Video Recorders

Notes: 1. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied. Please visit our website at http://www.diodes.com/products/lead_free.html.

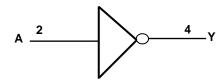


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Pin Descriptions

Pin Name	Pin No	Description			
NC	1	No Connection			
Α	2	Data Input			
GND	3	Ground			
Y	4	Data Output			
V _{CC}	5	Supply Voltage			

Logic Diagram



Function Table

Inputs	Output
Α	Υ
Н	L
L	Н



SINGLE INVERTER GATE

Absolute Maximum Ratings (Note 2)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
V _{CC}	Supply Voltage Range	-0.5 to 6.5	V
V_{I}	Input Voltage Range	-0.5 to 6.5	V
Vo	Voltage applied to output in high or low state	-0.5 to V _{CC} +0.5	V
l _{IK}	Input Clamp Current V _I <0	-20	mA
I _{OK}	Output Clamp Current (V _O < 0 or V _O > V _{CC})	±20	mA
Io	Continuous output current ($V_O = 0$ to V_{CC})	±25	mA
I _{CC}	Continuous current through V _{CC}	50	mA
I _{GND}	Continuous current through GND	-50	mA
TJ	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C

Notes: 2. Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

Recommended Operating Conditions (Note 3)

Symbol		Parameter	Min	Max	Unit
V _{CC}	Operating Voltage		4.5	5.5	V
V_{IH}	High-level Input Voltage		2.0		V
V _{IL}	Low-level input voltage			0.8	V
VI	Input Voltage		0	5.5	V
Vo	Output Voltage		0	V_{CC}	V
I _{OH}	High-level output current			-8	mA
I _{OL}	Low-level output current			8	mA
Δt/ΔV	Input transition rise or fall rate			20	ns/V
T _A	Operating free-air temperature		-40	125	°C

Notes: 3. Unused inputs should be held at V_{CC} or Ground.



SINGLE INVERTER GATE

Electrical Characteristics

Cumhal	Devementes	Test	V		25°C		-40°C t	o 85ºC	-40°C to	125°C	11:0:4
Symbol	Parameter	Conditions	V _{CC}	Min	Тур.	Max	Min	Max	Min	Max	Unit
.,	High Level	$I_{OH} = -50\mu A$	4.5V	4.4	4.5		4.4		4.4		.,
V _{OH}	Output Voltage	$I_{OH} = -8mA$	4.5V	3.94			3.8		3.70		V
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Low Level	$I_{OL} = 50\mu A$	4.5V		0	0.1		0.1		0.1	V
V _{OL}	Output Voltage	$I_{OL} = 8mA$	4.5V			0.36		0.44		0.55	V
II	Input Current	$V_I = 5.5V$ or GND	0 to 5.5V			± 0.1		± 1		± 2	μΑ
Icc	Supply Current	$V_I = 5.5V$ or GND $I_O=0$	5.5V			1		10		40	μA
Ci	Input Capacitance	$V_I = V_{CC} - or$ GND	5.5V		2.0	10		10		10	pF
Δl _{CC}	Additional Supply Current	Input at 3.4 V	5.5V			1.35		1.5			mA
θ_{JA}	Thermal Resistance	SOT25	(Note 4)		204						°C/W
ОЈД	Junction-to- Ambient	SOT353	(11016-4)		371						C/VV
0	Thermal Resistance	SOT25	(NI=4= 4)		52						°C/W
θ _{JC}	Junction-to- Case	SOT353	(Note 4)		143						C/VV

Note: 4. Test conditions for SOT25, and SOT353: Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

Switching Characteristics

 $V_{CC} = 5V \pm 0.5V$ (see Figure 1)

Porton From		то	то		25°C		-40°C to 85°C		-40°C to 125°C		Unit
Parameter	(Input)	(OUTPUT)		Min	Тур.	Max	Min	Max	Min	Max	Unit
4	^	V	C _L =15pF	0.6	3.4	6.7	0.6	7.5	0.6	8.5	ns
^Т рd	A	ľ	C _L =50pF	0.6	4.9	7.7	0.6	8.5	0.6	10.05	ns

Operating Characteristics

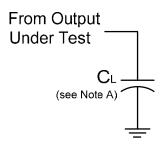
 $T_A = 25$ °C

Parameter		Test Conditions	V _{CC} = 5 V Typ.	Unit
C_pd	Power dissipation capacitance	f = 1 MHz No Load	15	pF

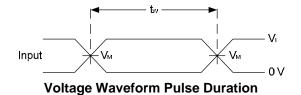


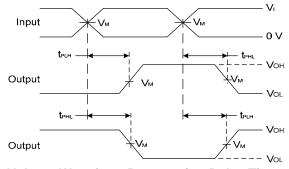
SINGLE INVERTER GATE

Parameter Measurement Information



V	In	Inputs		C
V _{CC}	VI	t _r /t _f	V _M	CL
5V±0.5V	3V	≤3ns	1.5V	15pF
5V±0.5V	3V	≤3ns	1.5V	50pF





Voltage Waveform Propagation Delay Times Inverting and Non Inverting Outputs

Notes: A. Includes test lead and test apparatus capacitance.

B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.

C. Inputs are measured separately one transition per measurement.

D. t_{PLH} and t_{PHL} are the same as t_{pd.}



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Ordering Information

Logic Device Function Package Packing
74 : Logic Prefix 04 : 1-Input W5 : SOT25 7 : Tape & Reel

SE: SOT353

AHCT: 2 to 5.5V Family with TTL

Family with 11 input level

1G: One gate

	Davisa	Package	Package Packaging		7" Tape and Reel	
	Device	Code	(Note 5)	Quantity	Part Number Suffix	
Pb ,	74AHCT1G04W5-7	W5	SOT25	3000/Tape & Reel	-7	
@ ,	74AHCT1G04SE-7	SE	SOT353	3000/Tape & Reel	-7	

Notes: 5. Pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf.

Inverter -Gate

Marking Information

(Top View)

5 <u>XX</u>: Identification code <u>Y</u>: Year 0~9

 $\underline{XX} \underline{Y} \underline{W} \underline{X}$ \underline{W} : Week: A $^{\sim}Z$: 1 $^{\sim}26$ week;

a~z: 27~52 week; z represents 52 and 53 week

2 3 X: A~Z: Internal code

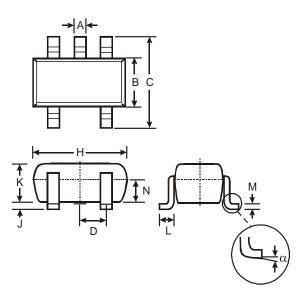
Part Number	Package	Identification Code
74AHCT1G04W5	SOT25	ZT
74AHCT1G04SE	SOT353	ZT



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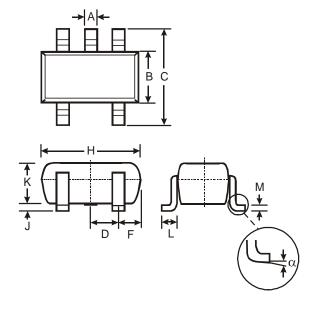
Package Outline Dimensions (All Dimensions in mm)

(1) Package Type: SOT25



SOT25				
Dim	Min	Max	Тур	
Α	0.35	0.50	0.38	
В	1.50	1.70	1.60	
C	2.70	3.00	2.80	
D	_	_	0.95	
Н	2.90	3.10	3.00	
J	0.013	0.10	0.05	
K	1.00	1.30	1.10	
L	0.35	0.55	0.40	
M	0.10	0.20	0.15	
Ν	0.70	0.80	0.75	
α	0°	8°	_	
All Dimensions in mm				

(2) Package Type: SOT353



SOT353				
Dim	Min	Max		
Α	0.10	0.30		
В	1.15	1.35		
C	2.00	2.20		
D	0.65 Typ			
F	0.40	0.45		
H	1.80	2.20		
J	0	0.10		
K	0.90	1.00		
L	0.25	0.40		
М	0.10	0.22		
α	0°	8°		
All Dimensions in mm				



SINGLE INVERTER GATE

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