

# DDTB142JC-7-F Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number DDTB142JC-7-F-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number DDTB142JC-7-F

Description TRANS PREBIAS PNP 50V SOT23-3

Detailed Description Pre-Biased Bipolar Transistor (BJT) PNP - Pre-Biase d 50 V 500 mA 200 MHz 200 mW Surface Mount SOT

-23-3



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
DDTB142JC-7-F	Diodes Incorporated
Series:	Product Status:
	Obsolete
Transistor Type:	Current - Collector (Ic) (Max):
PNP - Pre-Biased	500 mA
Voltage - Collector Emitter Breakdown (Max):	Resistor - Base (R1):
50 V	470 Ohms
Resistor - Emitter Base (R2):	DC Current Gain (hFE) (Min) @ Ic, Vce:
10 kOhms	56 @ 50mA, 5V
Vce Saturation (Max) @ lb, lc:	Current - Collector Cutoff (Max):
300mV @ 2.5mA, 50mA	500nA
Frequency - Transition:	Power - Max:
200 MHz	200 mW
Mounting Type:	Package / Case:
Surface Mount	TO-236-3, SC-59, SOT-23-3
Supplier Device Package:	Base Product Number:
SOT-23-3	DDTB142

# **Environmental & Export classification**

8541.21.0075

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	



#### **PART OBSOLETE - CONTACT US**



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#### PNP PRE-BIASED 500mA TRANSISTOR

#### **Features**

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTD)
- · Built-In Biasing Resistors
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

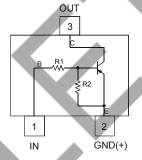
https://www.diodes.com/quality/product-definitions/

### **Mechanical Data**

- Case: SOT-23
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208 Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe).
- Marking Information: See Table Below & Page 3
- Ordering Information: See Page 3
- Weight: 0.008 grams (Approximate)

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	SOT-23	
Dim	Min	Max
Α	0.37	0.51
В	1.20	1.40
С	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
Н	2.80	3.00
J	0.013	0.10
K	0.903	1.10
	0.45	0.61
М	0.085	0.180
α	0°	8°
All Dim	ensions	in mm



Schematic and Pin Diagram

P/N	R1 (NOM)	R2 (NOM)	Type Code
DDTB122LC	$0.22$ k $\Omega$	10kΩ	P75
DDTB142JC	$0.47$ k $\Omega$	10kΩ	P76
DDTB122TC	$0.22$ k $\Omega$	OPEN	P77
DDTB142TC	$0.47$ k $\Omega$	OPEN	P78

#### **Maximum Ratings** @T<sub>A</sub> = +25°C, unless otherwise specified.

	Characteristic		Symbol	Value	Unit
Supply Voltage, (3) to (2			Vcc	-50	V
Input Voltage, (1) to (2)		DDTB122LC DDTB142JC	V <sub>IN</sub>	+5 to -6 +5 to -6	V
Input Voltage, (2) to (1)		DDTB122TC DDTB142TC	V <sub>EBO (MAX)</sub>	-5	V
Output Current		All	Ic	-500	mA
Power Dissipation		(Note 4)	P <sub>D</sub>	200	mW
Thermal Resistance, Jur	action to Ambient Air	(Note 4)	$R_{ hetaJA}$	625	°C/W
Operating and Storage T	emperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Mounted on FR4 PC Board with recommended pad layout at http://www.diodes.com/package-outlines.html.



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<b>Electrical Characteristics</b>	$@T_A = +25^{\circ}C$ , unless otherwise specified.	R1, R2 Types
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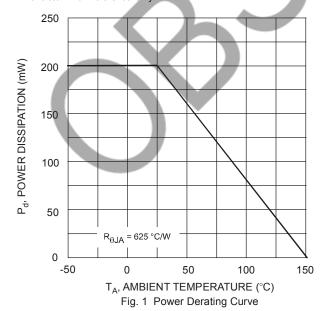
Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Input Voltage	DDTB122LC DDTB142JC	V <sub>I(off)</sub>	-0.3 -0.3	_	_	٧	V <sub>CC</sub> = -5V, I <sub>O</sub> = -100μA
	DDTB122LC DDTB142JC	$V_{I(on)}$			-2.0 -2.0		$V_O = -0.3V$ , $I_O = -20mA$ $V_O = -0.3V$ , $I_O = -20mA$
Output Voltage		$V_{O(on)}$	_	_	-0.3V	V	$I_{O}/I_{I} = -50$ mA/-2.5mA
Input Current	DDTB122LC DDTB142JC	l <sub>l</sub>	_	_	-28 -13	mA	V <sub>I</sub> = -5V
Output Current		I <sub>O(off)</sub>		_	-0.5	μА	$V_{CC} = -50V, V_{I} = 0V$
DC Current Gain	DDTB122LC DDTB142JC	G <sub>l</sub>	56 56			_	V <sub>O</sub> = -5V, I <sub>O</sub> = -50mA
Gain-Bandwidth Product*		f <sub>T</sub>		200		MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = -5mA, f = 100MHz

<sup>\*</sup> Transistor - For Reference Only

# Electrical Characteristics @TA = 25°C unless otherwise specified R1- Only Types

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
Collector-Base Breakdown Voltage		BV <sub>CBO</sub>	-50	7		٧	$I_{C} = -50 \mu A$
Collector-Emitter Breakdown Voltage		BV <sub>CEO</sub>	-40			V	I <sub>C</sub> = -1mA
Emitter-Base Breakdown Voltage	DDTB122TC DDTB142TC	BV <sub>EBO</sub>	-5	_		V	I <sub>E</sub> = -50μA I <sub>E</sub> = -50μA
Collector Cutoff Current		I <sub>CBO</sub>	1	_	-0.5	μА	V <sub>CB</sub> = -50V
Emitter Cutoff Current	DDTB122TC DDTB142TC	I <sub>EBO</sub>		>	-0.5 -0.5	μА	V <sub>EB</sub> = -4V
Collector-Emitter Saturation Voltage		V <sub>CE(sat)</sub>		_	-0.3	٧	I <sub>C</sub> = -50mA, I <sub>B</sub> = -2.5mA
DC Current Transfer Ratio	DDTB122TC DDTB142TC	h <sub>FE</sub>	100 100	250 250	600 600	_	I <sub>C</sub> = -5mA, V <sub>CE</sub> = -5V
Gain-Bandwidth Product*		fτ	_	200	_	MHz	V <sub>CE</sub> = -10V, I <sub>E</sub> = 5mA, f = 100MHz

<sup>\*</sup> Transistor - For Reference Only





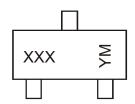
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# Ordering Information (Note 5)

Part Number	Packaging	Shipping
DDTB122LC-7-F	SOT-23	3000/Tape & Reel
DDTB142JC-7-F	SOT-23	3000/Tape & Reel
DDTB122TC-7-F	SOT-23	3000/Tape & Reel
DDTB142TC-7-F	SOT-23	3000/Tape & Reel

Note: 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

# **Marking Information**



XXX = Product Type Marking Code, See Table on Page 1
YM = Date Code Marking
Y = Year ex: I = 2021
M = Month ex: 9 = September

#### Date Code Kev

Year	2006		2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	Т		I	J	K	L	M	N	0	Р	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec





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