

DDTC143FCA-7-F Datasheet



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DiGi Electronics Part Number	DDTC143FCA-7-F-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DDTC143FCA-7-F
Description	TRANS PREBIAS NPN 50V SOT23-3
Detailed Description	Pre-Biased Bipolar Transistor (BJT) NPN - Pre-Biased 50 V 100 mA 250 MHz 200 mW Surface Mount SOT-23-3



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Purchase and inquiry

Manufacturer Product Number:

DDTC143FCA-7-F

Series:

-

Transistor Type:

NPN - Pre-Biased

Voltage - Collector Emitter Breakdown (Max):

50 V

Resistor - Emitter Base (R2):

22 kOhms

Vce Saturation (Max) @ Ib, Ic:

300mV @ 500µA, 10mA

Frequency - Transition:

250 MHz

Mounting Type:

Surface Mount

Supplier Device Package:

SOT-23-3

Manufacturer:

Diodes Incorporated

Product Status:

Active

Current - Collector (Ic) (Max):

100 mA

Resistor - Base (R1):

4.7 kOhms

DC Current Gain (hFE) (Min) @ Ic, Vce:

68 @ 10mA, 5V

Current - Collector Cutoff (Max):

500nA

Power - Max:

200 mW

Package / Case:

TO-236-3, SC-59, SOT-23-3

Base Product Number:

DDTC143

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.21.0075

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



DDTC (R1≠R2 SERIES) CA

NPN PRE-BIASED SMALL SIGNAL SURFACE MOUNT TRANSISTOR

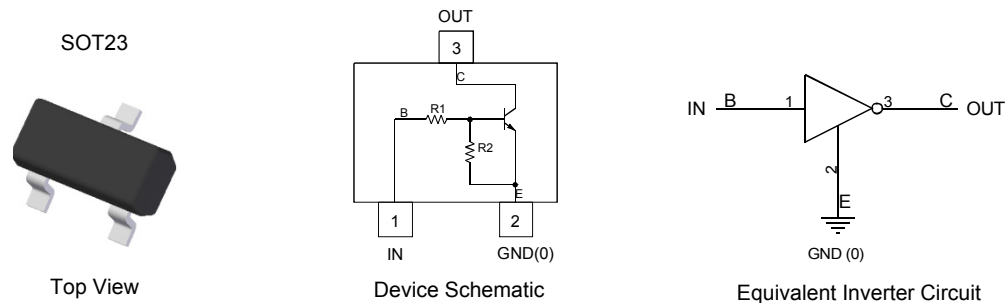
Features

- Epitaxial Planar Die Construction
- Complementary PNP Types Available (DDTA)
- Built-In Biasing Resistors, R1≠R2
- **Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)**
- **Halogen and Antimony Free "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Part Number	R1 (NOM)	R2 (NOM)
DDTC113ZCA	1KΩ	10KΩ
DDTC123YCA	2.2KΩ	10KΩ
DDTC123JCA	2.2KΩ	47KΩ
DDTC143XCA	4.7KΩ	10KΩ
DDTC143FCA	4.7KΩ	22KΩ
DDTC143ZCA	4.7KΩ	47KΩ
DDTC114YCA	10KΩ	47KΩ
DDTC114WCA	10KΩ	4.7KΩ
DDTC124XCA	22KΩ	47KΩ
DDTC144VCA	47KΩ	10KΩ
DDTC144WCA	47KΩ	22KΩ

Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish – Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.008 grams (approximate)



Ordering Information (Notes 3 & 4)

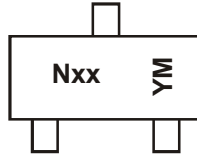
Product	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
DDTC113ZCA-7-F	AEC-Q101	N02	7	8	3,000
DDTC123YCA-7-F	AEC-Q101	N05	7	8	3,000
DDTC123JCA-7-F	AEC-Q101	N06	7	8	3,000
DDTC143XCA-7-F	AEC-Q101	N09	7	8	3,000
DDTC143FCA-7-F	AEC-Q101	N10	7	8	3,000
DDTC143ZCA-7-F	AEC-Q101	N11	7	8	3,000
DDTC143ZCAQ-7-F	Automotive	N11	7	8	3,000
DDTC143ZCAQ-13-F	Automotive	N11	13	8	10,000
DDTC114YCA-7-F	AEC-Q101	N14	7	8	3,000
DDTC114YCAQ-7-F	Automotive	N14	7	8	3,000
DDTC114YCAQ-13-F	Automotive	N14	13	8	10,000
DDTC114WCA-7-F	AEC-Q101	N15	7	8	3,000
DDTC124XCA-7-F	AEC-Q101	N18	7	8	3,000
DDTC144VCA-7-F	AEC-Q101	N21	7	8	3,000
DDTC144WCA-7-F	AEC-Q101	N22	7	8	3,000

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/quality/product_compliance_definitions/.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.



DDTC (R1≠R2 SERIES) CA

Marking Information



Nxx = Product Type Marking Code (See Table Above)
 YM = Date Code Marking
 Y = Year (ex: T = 2006)
 M = Month (ex: 9 = September)

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009	2010	2011	2012	2013	2014	2015	2016	2017
Code	N	P	R	S	T	U	V	W	X	Y	Z	A	B	C	D	E
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec				
Code	1	2	3	4	5	6	7	8	9	O	N	D				

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Supply Voltage, <Pin: (3) to (2)>	V _{CC}	50	V
Input Voltage, <Pin: (1) to (2)>	V _{IN}	DDTC113ZCA	-5 to +10
		DDTC123YCA	-5 to +12
		DDTC123JCA	-5 to +12
		DDTC143XCA	-7 to +20
		DDTC143FCA	-6 to +30
		DDTC143ZCA	-5 to +30
		DDTC114YCA	-6 to +40
		DDTC114WCA	-10 to +30
		DDTC124XCA	-10 to +40
		DDTC144VCA	-15 to +40
DDTC144WCA	-10 to +40		
Output Current	I _O	DDTC113ZCA	100
		DDTC123YCA	100
		DDTC123JCA	100
		DDTC143XCA	100
		DDTC143FCA	100
		DDTC143ZCA	100
		DDTC114YCA	70
		DDTC114WCA	100
		DDTC124XCA	50
		DDTC144VCA	30
DDTC144WCA	30		
Output Current	I _{C(MAX)}	100	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P _D	200	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Notes: 6. Mounted on FR4 PC Board with minimum recommended pad layout


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Typ	Max	Unit	Test Condition	
Input Voltage	DDTC113ZCA DDTC123YCA DDTC123JCA DDTC143XCA DDTC143FCA DDTC143ZCA DDTC114YCA DDTC114WCA DDTC124XCA DDTC144VCA DDTC144WCA	V _{I(OFF)}	0.3 0.3 0.5 0.3 0.3 0.5 0.3 0.8 0.4 1.0 0.8	—	—	—	V	V _{CC} = 5V, I _O = 100μA
	DDTC113ZCA DDTC123YCA DDTC123JCA DDTC143XCA DDTC143FCA DDTC143ZCA DDTC114YCA DDTC114WCA DDTC124XCA DDTC144VCA DDTC144WCA		V _{I(ON)}	—	—	3.0 3.0 1.1 2.5 1.3 1.3 1.4 3.0 2.5 5.0 4.0		
Output Voltage		V _{O(ON)}	—	0.1	0.3	V	I _O /I _I = 5mA/0.25mA DDTC123JCA I _O /I _I = 5mA/0.25mA DDTC143ZCA I _O /I _I = 5mA/0.25mA DDTC114YCA I _O /I _I = 10mA/0.5mA All Others	
Input Current	DDTC113ZCA DDTC123YCA DDTC123JCA DDTC143XCA DDTC143FCA DDTC143ZCA DDTC114YCA DDTC114WCA DDTC124XCA DDTC144VCA DDTC144WCA	I _I	—	—	7.2 3.8 3.6 1.8 1.8 1.8 0.88 0.88 0.36 0.16 0.16	mA	V _I = 5V	
Output Current		I _{O(OFF)}	—	—	0.5	μA	V _{CC} = 50V, V _I = 0V	
DC Current Gain	DDTC113ZCA DDTC123YCA DDTC123JCA DDTC143XCA DDTC143FCA DDTC143ZCA DDTC114YCA DDTC114YCAQ DDTC114WCA DDTC114WCA DDTC124XCA DDTC144VCA DDTC144WCA	G _I	33 33 80 30 68 80 68 80 24 68 33 56	—	—	—	V _O = 5V, I _O = 5mA V _O = 5V, I _O = 10mA V _O = 5V, I _O = 10mA V _O = 5V, I _O = 10mA V _O = 5V, I _O = 10mA V _O = 5V, I _O = 10mA V _O = 5V, I _O = 5mA V _O = 5V, I _O = 5mA V _O = 5V, I _O = 10mA V _O = 5V, I _O = 5mA V _O = 5V, I _O = 5mA V _O = 5V, I _O = 5mA	
Input Resistor Tolerance		ΔR ₁	-30	—	+30	%	—	
Resistance Ratio Tolerance		ΔR ₂ /R ₁	-20	—	+20	%	—	
Gain-Bandwidth Product (Note 7)		f _T	—	250	—	MHz	V _{CE} = 10V, I _E = 5mA, f = 100MHz	

Note: 7. Transistor - For Reference Only

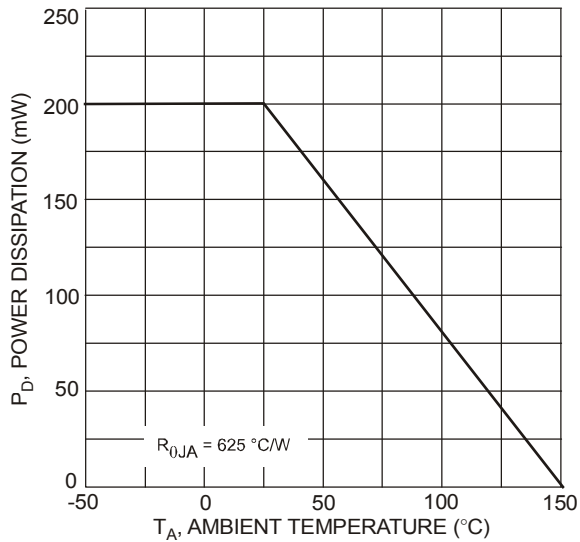
Typical Curves – DDTC123JCA (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)


Fig. 1 Power Dissipation vs. Ambient Temperature

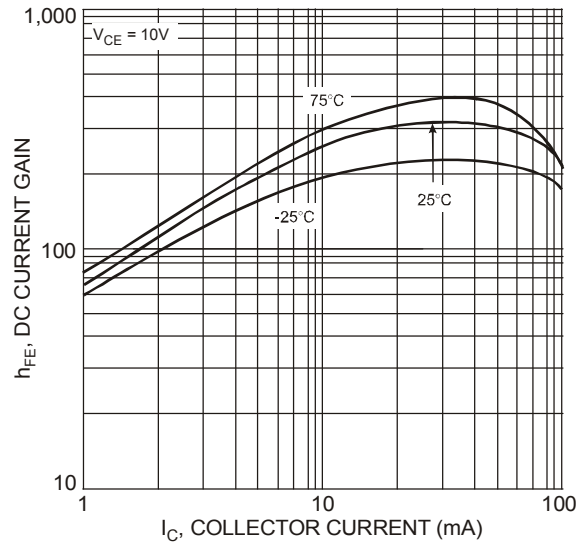


Fig. 2 Typical DC Current Gain vs. Collector Current

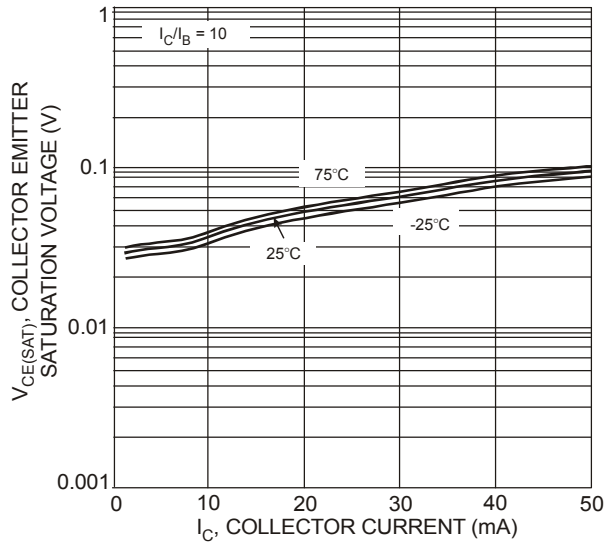


Fig. 3 Typical Collector Emitter Saturation Voltage vs. Collector Current

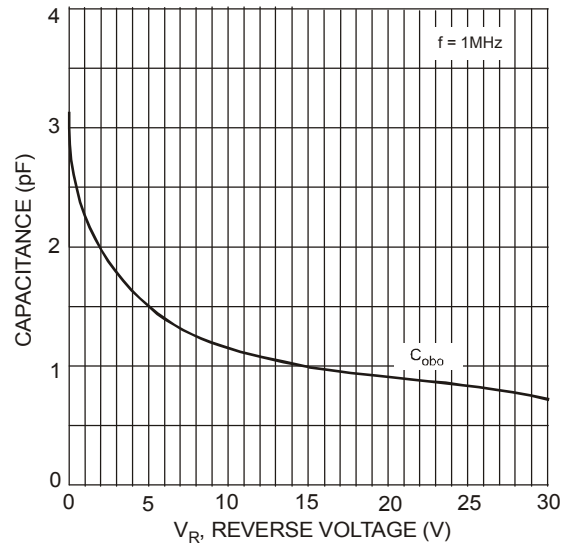


Fig. 4 Typical Capacitance Characteristics

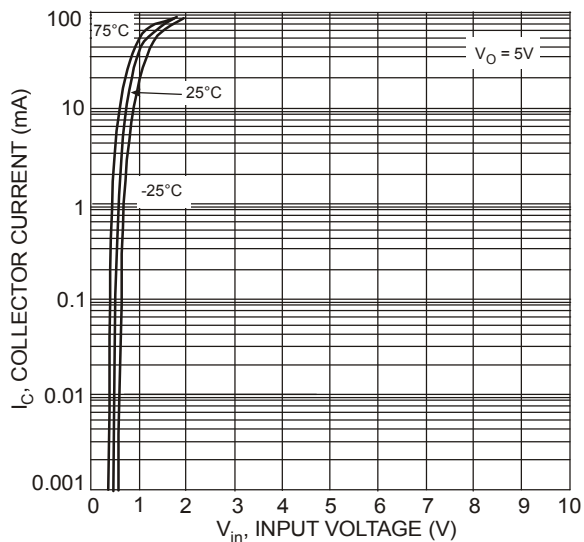


Fig. 5 Collector Current vs. Input Voltage

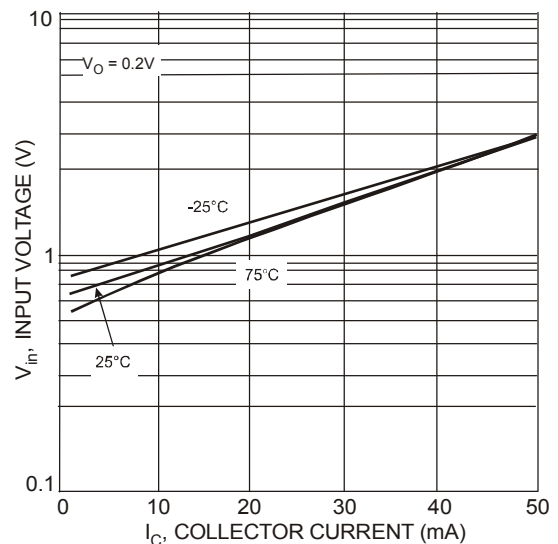
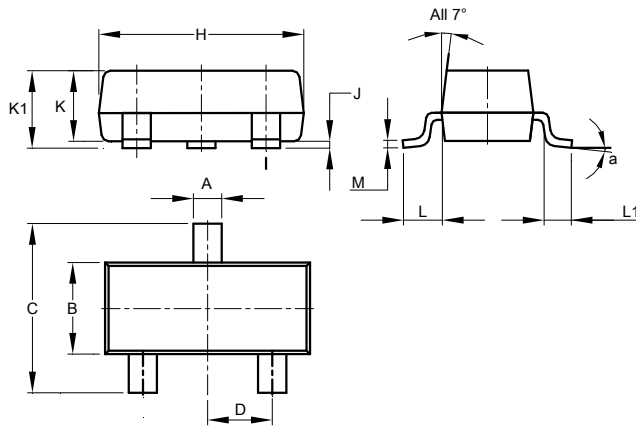


Fig. 6 Input Voltage vs. Collector Current

Package Outline Dimensions

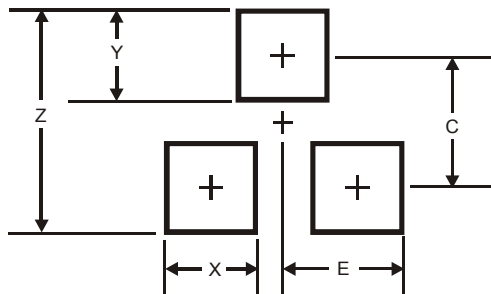
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



SOT23			
Dim	Min	Max	Typ
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	8°		
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	2.9
X	0.8
Y	0.9
C	2.0
E	1.35

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