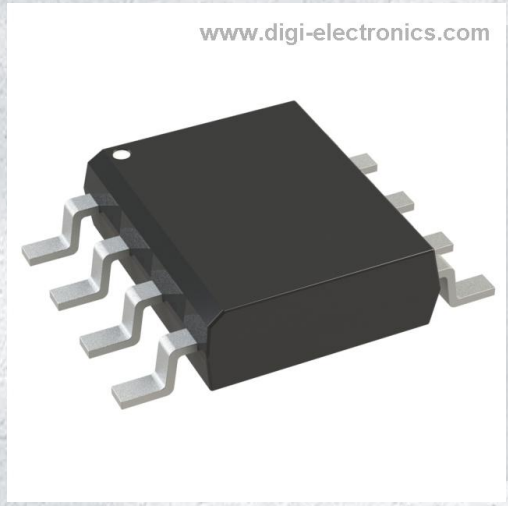
DMC3021LSD-13 Datasheet



DiGi Electronics Part Number	DMC3021LSD-13-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMC3021LSD-13
Description	MOSFET N/P-CH 30V 8.5A/7A 8SO
Detailed Description	Mosfet Array 30V 8.5A, 7A 2.5W Surface Mount 8-SO

https://www.DiGi-Electronics.com

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
DMC3021LSD-13	Diodes Incorporated
Series:	Product Status:
-	Active
Technology:	Configuration:
MOSFET (Metal Oxide)	N and P-Channel
FET Feature:	Drain to Source Voltage (Vdss):
Logic Level Gate	30V
Current - Continuous Drain (Id) @ 25°C:	Rds On (Max) @ Id, Vgs:
8.5A, 7A	21mOhm @ 7A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
2.1V @ 250µA	16.1nC @ 10V
Input Capacitance (Ciss) (Max) @ Vds:	Power - Max:
767pF @ 10V	2.5W
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Package / Case:	Supplier Device Package:
8-SOIC (0.154", 3.90mm Width)	8-SO
Base Product Number:	
DMC3021	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	

Lead-free Green

DMC3021LSD

COMPLEMENTARY PAIR ENHANCEMENT MODE MOSFET

Product Summary

Device	BV_{DSS}	$R_{DS(ON)}$ max	I_D Max T_A = +25°C
Q2	30V	21mΩ @ V_{GS} = 10V	8.5A
		32mΩ @ V_{GS} = 4.5V	7.2A
Q1	-30V	39mΩ @ V_{GS} = -10V	-7A
		53mΩ @ V_{GS} = -4.5V	-5.6A

Features

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Complementary Pair MOSFET
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at https://www.diodes.com/products/automotive/automotive-products/.**
- **This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. https://www.diodes.com/quality/product-definitions/**
- **An Automotive-Compliant Part is Available Under Separate Datasheet (DMC3021LSDQ)**

Description

This MOSFET has been designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

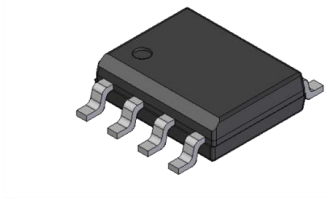
Applications

- Power Management Functions
- Analog Switch
- Load Switch

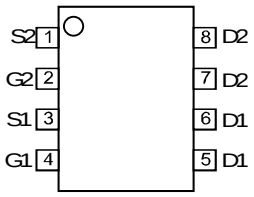
Mechanical Data

- Case: SO-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram
- Terminals: Finish - Matte Tin Annealed over Copper Lead Frame. Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.072 grams (Approximate)

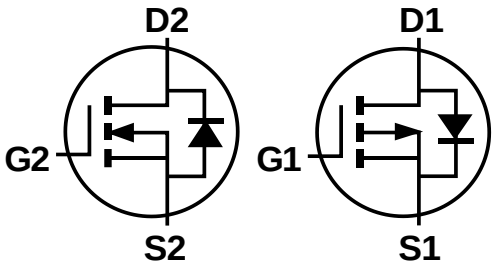
SO-8



Top View



Top View



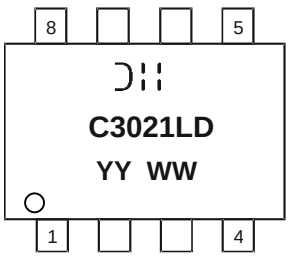
N-Channel MOSFET P-Channel MOSFET

Ordering Information (Note 4)

Part Number	Case	Packaging
DMC3021LSD-13	SO-8	2500/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



= Manufacturer's Marking
C3021LD = Product Type Marking Code
YYWW = Date Code Marking
YY or YY = Year (ex: 20 = 2020)
WW = Week (01 to 53)

Maximum Ratings N-CHANNEL – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 5)	Steady State	T_A = +25°C T_A = +85°C	I_D	8.5 7.1	A
Pulsed Drain Current (Note 6)			I_{DM}	40	A

Maximum Ratings P-CHANNEL – Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Continuous Drain Current (Note 5)	Steady State	T_A = +25°C T_A = +85°C	I_D	-7.0 -4.5	A
Pulsed Drain Current (Note 6)			I_{DM}	-30	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	2.5	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	50	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics N-CHANNEL – Q2 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	30	—	—	V	V_{GS} = 0V, I_D = 250μA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	1.0	μA	V_{DS} = 30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	1.45	2.1	V	V_{DS} = V_{GS}, I_D = 250μA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	— —	14 18	21 32	mΩ	V_{GS} = 10V, I_D = 7A V_{GS} = 4.5V, I_D = 5.6A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	8.1	—	S	V_{DS} = 5V, I_D = 7A
Diode Forward Voltage (Note 7)	V_{SD}	—	0.7	1.0	V	V_{GS} = 0V, I_S = 1A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	767	—	pF	V_{DS} = 10V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	110	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	105	—	pF	
Gate Resistance	R_g	—	1.4	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = 4.5V)	Q_g	—	7.8	—	nC	V_{DS} = 15V, I_D = 9A
Total Gate Charge (V_{GS} = 10V)	Q_g	—	16.1	—	nC	
Gate-Source Charge	Q_{gs}	—	1.8	—	nC	
Gate-Drain Charge	Q_{gd}	—	2.5	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	5.0	—	ns	V_{GS} = 10V, V_{DS} = 15V, R_G = 6Ω, I_D = 1A
Turn-On Rise Time	t_R	—	4.5	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	26.3	—	ns	
Turn-Off Fall Time	t_F	—	8.55	—	ns	

Notes: 5. Device mounted on FR-4 PCB, with minimum recommended pad layout.
6. Repetitive rating, pulse width limited by junction temperature.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

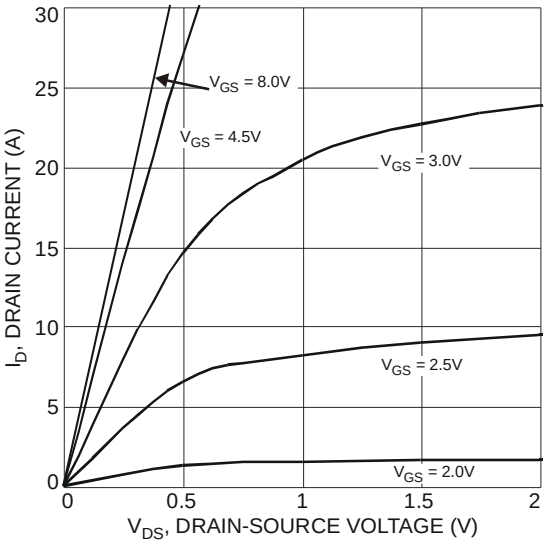


Fig. 1 Typical Output Characteristics

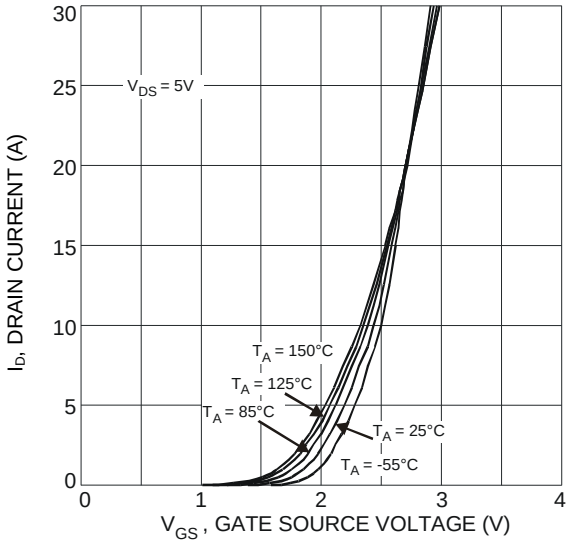


Fig. 2 Typical Transfer Characteristics

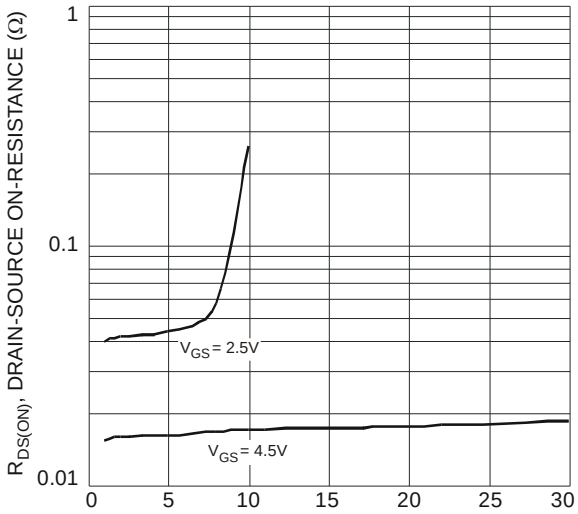


Fig. 3 Typical On-Resistance
vs. Drain Current and Gate Voltage

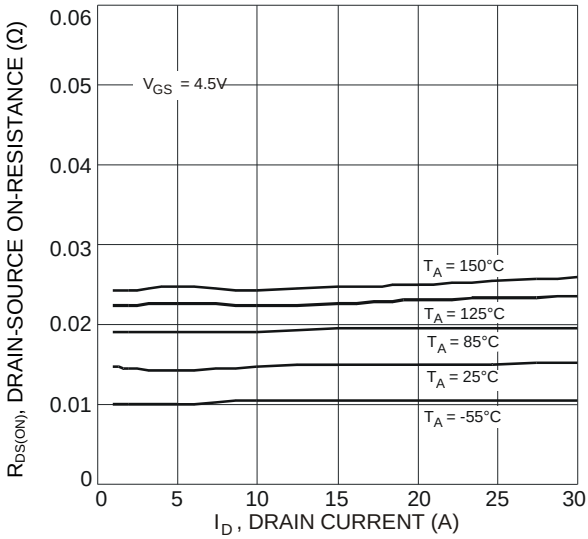


Fig. 4 Typical Drain-Source On-Resistance
vs. Drain Current and Temperature

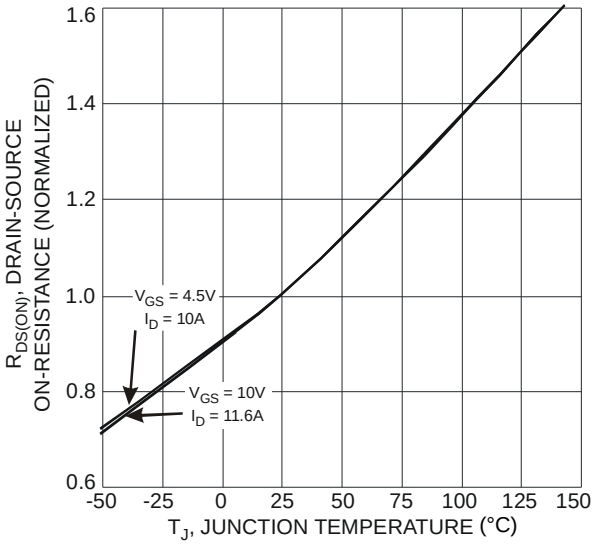


Fig. 5 On-Resistance Variation with Temperature

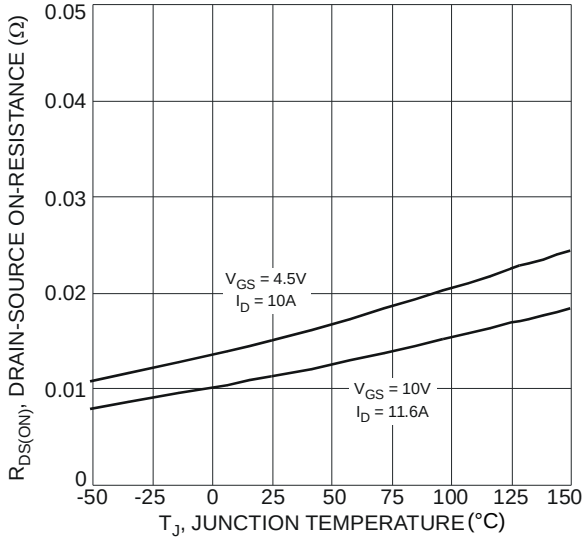


Fig. 6 On-Resistance Variation with Temperature

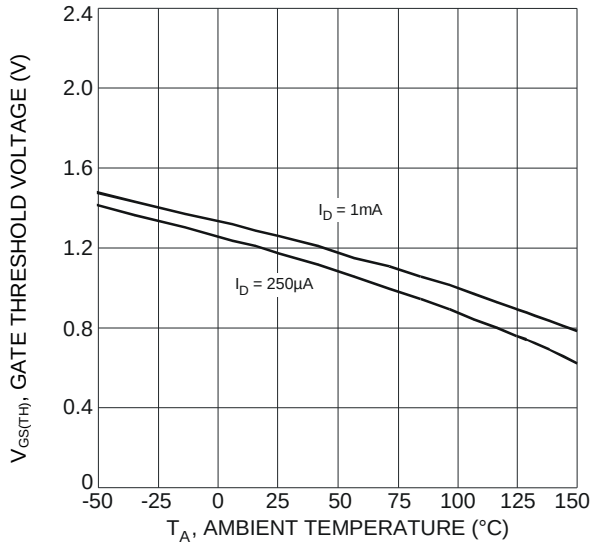


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

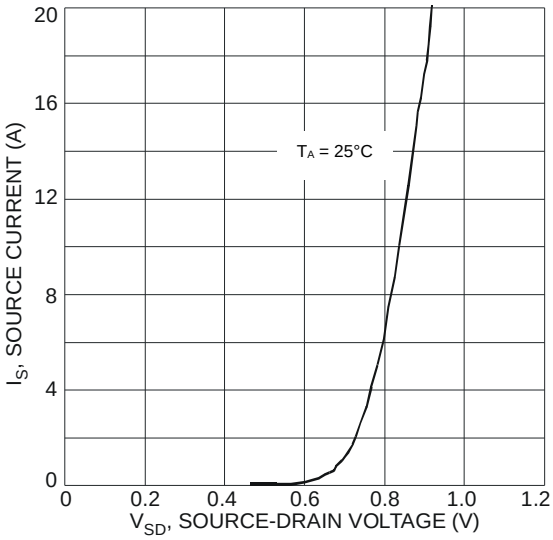


Fig. 8 Diode Forward Voltage vs. Current

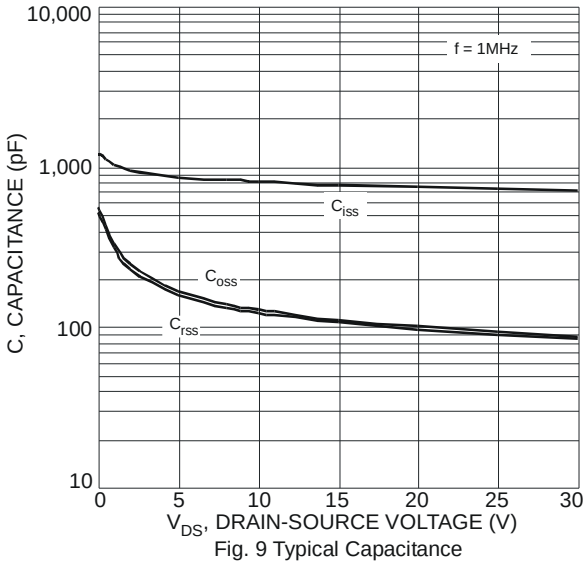


Fig. 9 Typical Capacitance

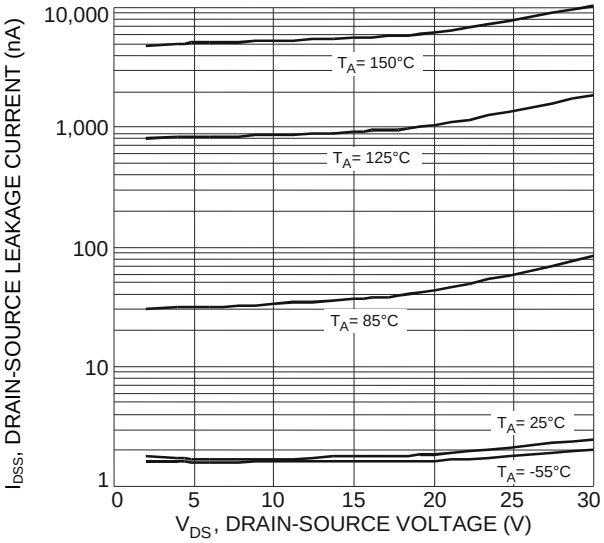


Fig. 10 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

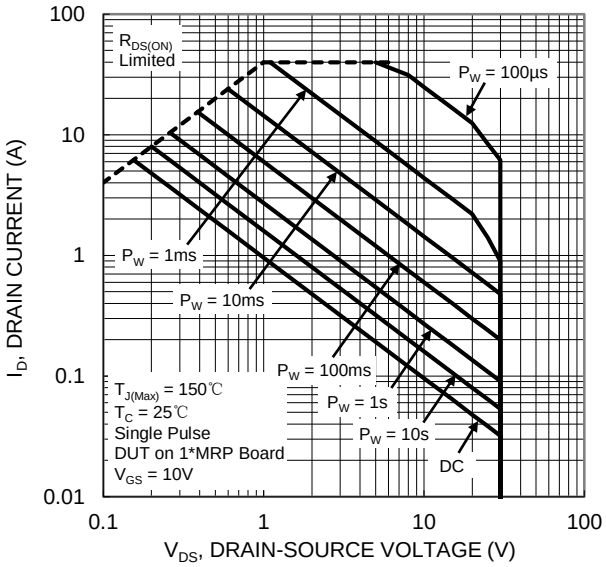


Fig. 11 SOA, Safe Operation Area

Electrical Characteristics P-CHANNEL – Q1 (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250μA
Zero Gate Voltage Drain Current T_J = +25°C	I_{DSS}	—	—	-1.0	μA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1	-1.7	-2.2	V	V_{DS} = V_{GS}, I_D = -250μA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	30	39	mΩ	V_{GS} = -10V, I_D = -4.3A
		—	42	53		V_{GS} = -4.5V, I_D = -3.7A
Forward Transfer Admittance	$\|Y_{fs}\|$	—	7	—	S	V_{DS} = -5V, I_D = -4.3A
Diode Forward Voltage (Note 7)	V_{SD}	—	-0.75	-1.0	V	V_{GS} = 0V, I_S = -1.7A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	1002	—	pF	V_{DS} = -10V, V_{GS} = 0V, f = 1.0MHz
Output Capacitance	C_{oss}	—	125	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	118	—	pF	
Gate Resistance	R_g	—	13	—	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	10.1	—	nC	V_{DS} = -15V, I_D = -6A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	21.1	—	nC	
Gate-Source Charge	Q_{gs}	—	2.8	—	nC	
Gate-Drain Charge	Q_{gd}	—	3.2	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	10.1	—	ns	V_{GS} = -10V, V_{DS} = -15V, R_G = 6Ω , I_D = -1A
Turn-On Rise Time	t_R	—	6.5	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	50.1	—	ns	
Turn-Off Fall Time	t_F	—	22.2	—	ns	

Notes: 7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to production testing.

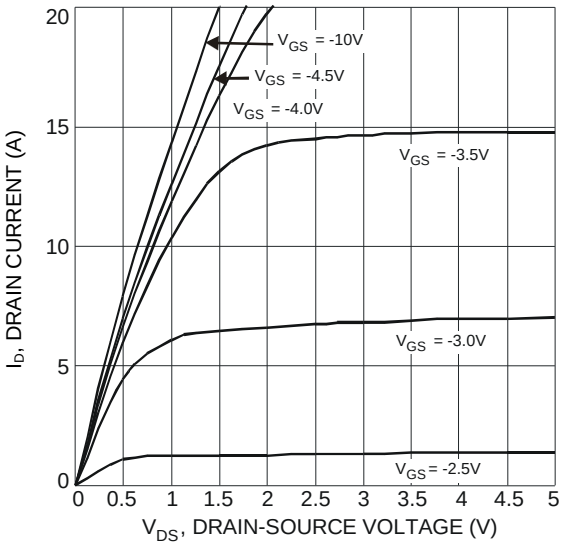


Fig. 12 Typical Output Characteristics

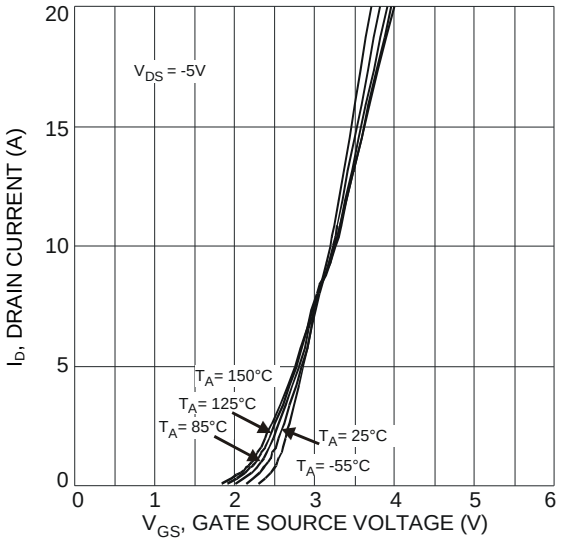


Fig. 13 Typical Transfer Characteristics

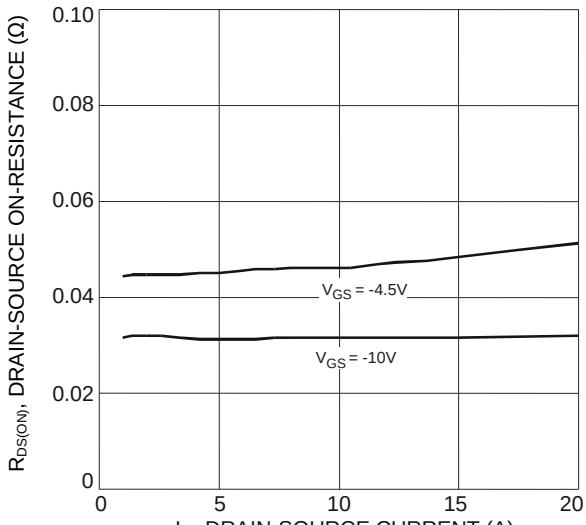


Fig. 14 Typical On-Resistance
vs. Drain Current and Gate Voltage

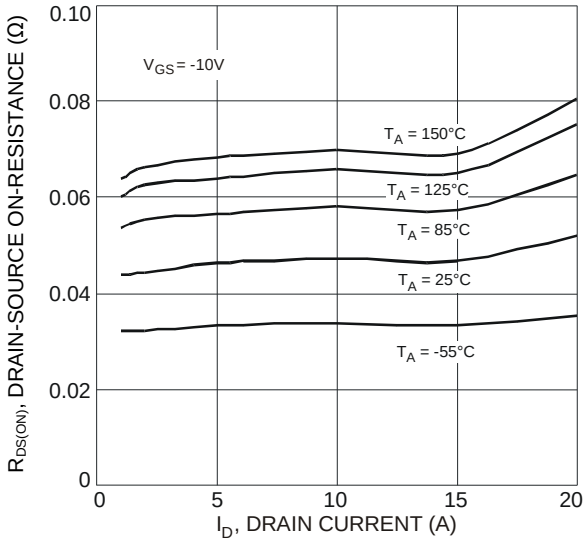


Fig. 15 Typical Drain-Source On-Resistance
vs. Drain Current and Temperature

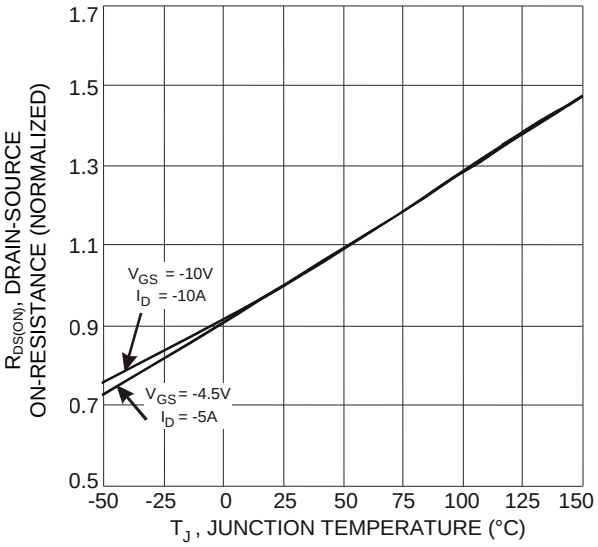


Fig. 16 On-Resistance Variation with Temperature

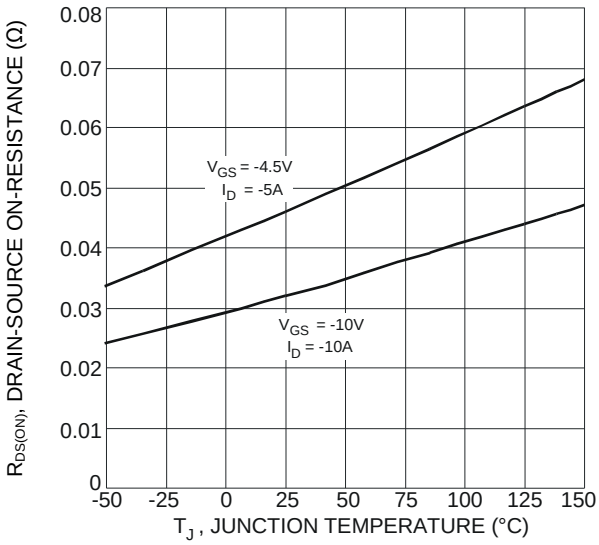


Fig. 17 On-Resistance Variation with Temperature

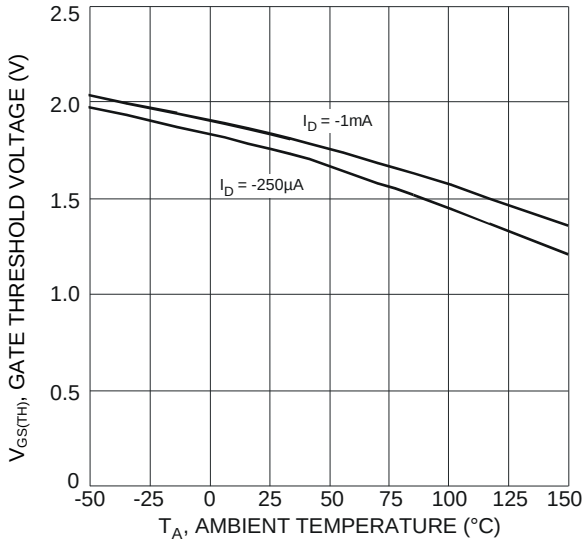


Fig. 18 Gate Threshold Variation vs. Ambient Temperature

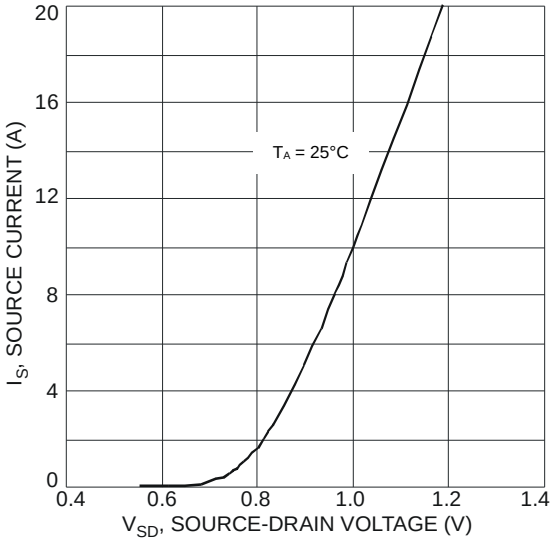


Fig. 19 Diode Forward Voltage vs. Current

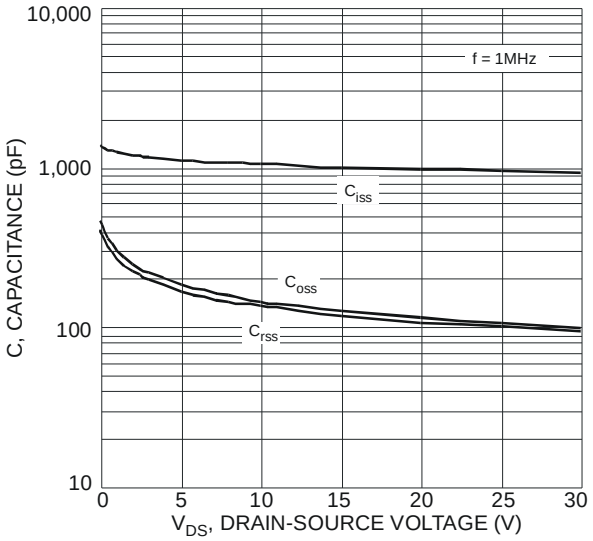


Fig. 20 Typical Capacitance

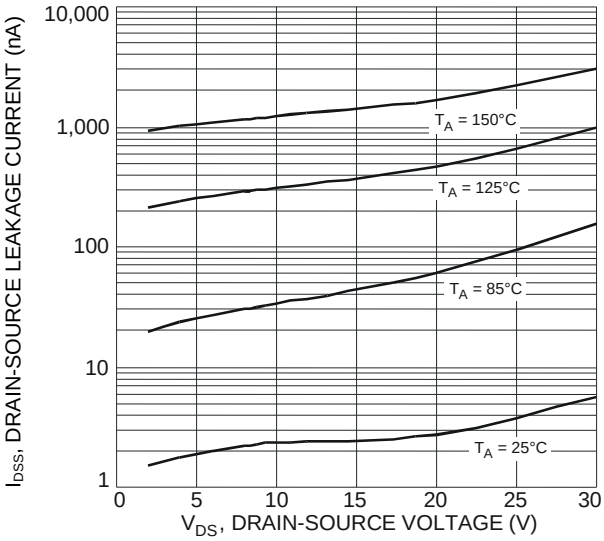


Fig. 21 Typical Drain-Source Leakage Current vs. Drain-Source Voltage

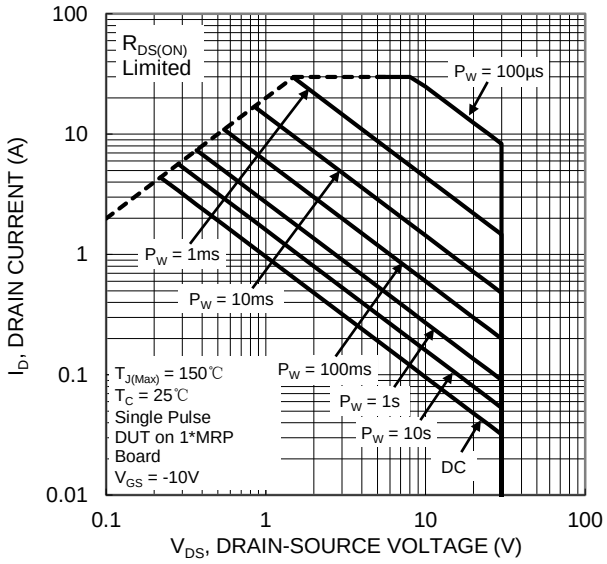
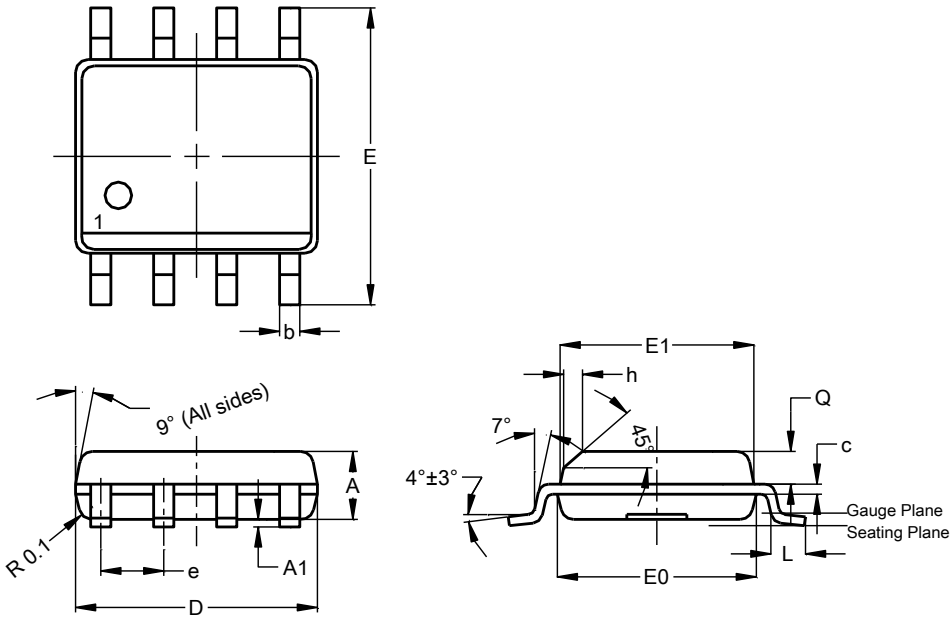


Fig. 22 SOA, Safe Operation Area

Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8

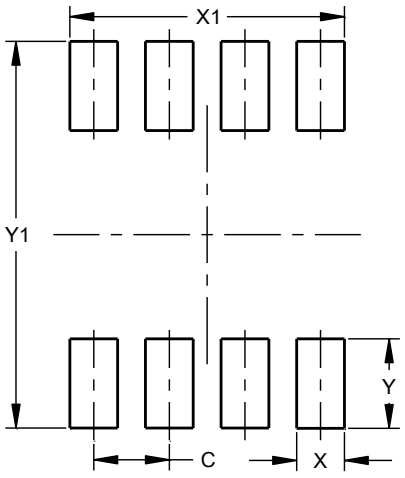


SO-8			
Dim	**Min**	**Max**	**Typ**
A	1.40	1.50	1.45
A1	0.10	0.20	0.15
b	0.30	0.50	0.40
c	0.15	0.25	0.20
D	4.85	4.95	4.90
E	5.90	6.10	6.00
E1	3.80	3.90	3.85
E0	3.85	3.95	3.90
e	--	--	1.27
h	--	--	0.35
L	0.62	0.82	0.72
Q	0.60	0.70	0.65
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8



Dimensions	Value (in mm)
C	1.27
X	0.802
X1	4.612
Y	1.505
Y1	6.50

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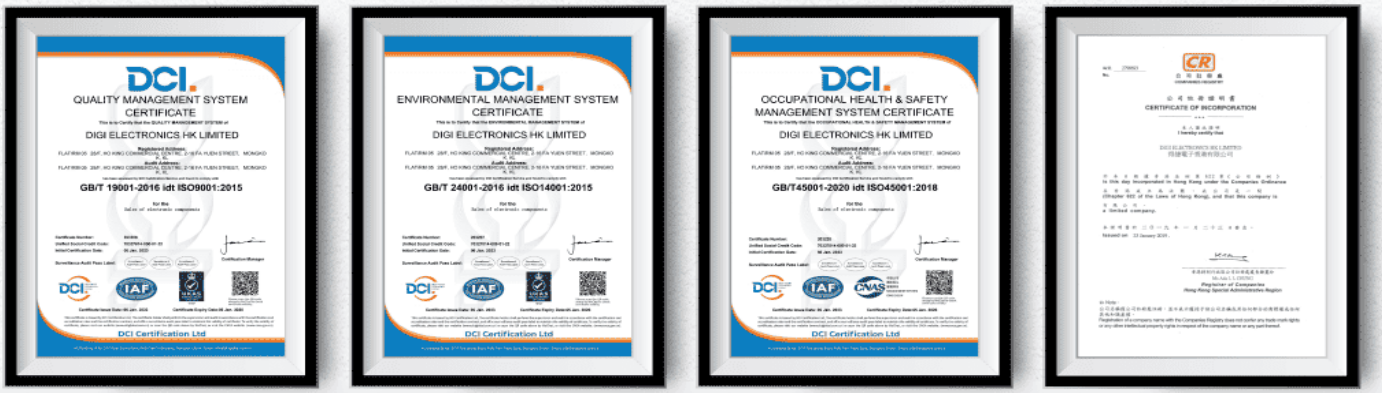

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