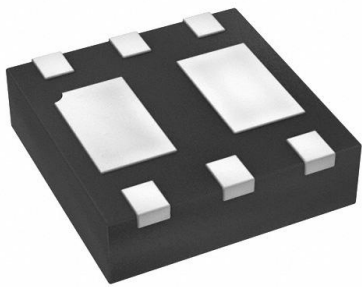


DMC67D8UFDBQ-13 Datasheet

www.digi-electronics.com



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	DMC67D8UFDBQ-13-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMC67D8UFDBQ-13
Description	MOSFET N/P-CH 60V 0.39A 6UDFN
Detailed Description	Mosfet Array 60V, 20V 390mA (Ta), 2.9A (Ta) 580mW (Ta) Surface Mount U-DFN2020-6 (Type B)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

DMC67D8UFDBQ-13

Series:

-

Technology:

MOSFET (Metal Oxide)

FET Feature:

-

Current - Continuous Drain (Id) @ 25°C:

390mA (Ta), 2.9A (Ta)

Vgs(th) (Max) @ Id:

2.5V @ 250µA, 1.25V @ 250µA

Input Capacitance (Ciss) (Max) @ Vds:

41pF @ 25V, 443pF @ 16V

Operating Temperature:

-55°C ~ 150°C (Tj)

Qualification:

AEC-Q101

Package / Case:

6-UDFN Exposed Pad

Base Product Number:

DMC67

Manufacturer:

Diodes Incorporated

Product Status:

Active

Configuration:

N and P-Channel Complementary

Drain to Source Voltage (Vdss):

60V, 20V

Rds On (Max) @ Id, Vgs:

40hm @ 500mA, 10V, 72mOhm @ 3.5A, 4.5V

Gate Charge (Qg) (Max) @ Vgs:

0.4pC @ 4.5V, 7.3nC @ 4.5V

Power - Max:

580mW (Ta)

Grade:

Automotive

Mounting Type:

Surface Mount

Supplier Device Package:

U-DFN2020-6 (Type B)

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.21.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99

Product Summary

Device	$V_{(BR)DSS}$	$R_{DS(ON) Max}$	$I_{D Max}$ $T_A = +25^{\circ}C$
Q1 N-Channel	60V	4.0 Ω @ $V_{GS} = 10V$	0.39A
		4.1 Ω @ $V_{GS} = 5V$	0.38A
		4.2 Ω @ $V_{GS} = 4V$	0.37A
Q2 P-Channel	-20V	72m Ω @ $V_{GS} = -4.5 V$	-2.9A
		108m Ω @ $V_{GS} = -2.7V$	-2.3A
		123m Ω @ $V_{GS} = -2.5V$	-2.2A

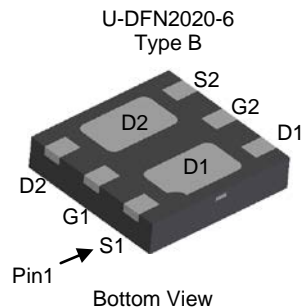
Description

This MOSFET is designed to meet the stringent requirements of Automotive applications. It is qualified to AEC-Q101, supported by a PPAP, and is ideal for use in:

- Load Switch



ESD PROTECTED

U-DFN2020-6
Type B

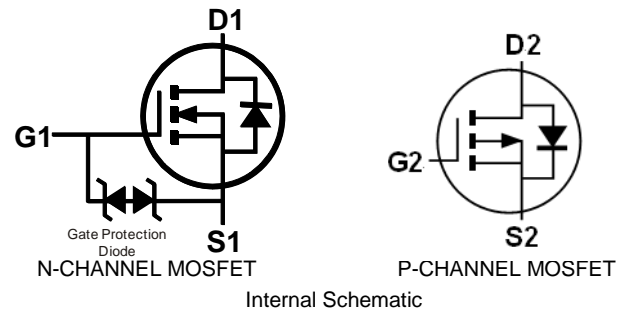
Bottom View

Features

- Low On-Resistance
- Low Input Capacitance
- Low Profile, 0.6mm Maximum Height
- **ESD Protected Gate**
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Mechanical Data

- Case: U-DFN2020-6 Type B
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish—NiPdAu over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E4)
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)



N-CHANNEL MOSFET

P-CHANNEL MOSFET

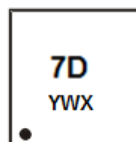
Internal Schematic

Ordering Information (Note 5)

Part Number	Case	Packaging
DMC67D8UFDBQ-7	U-DFN2020-6 Type B	3000/Tape & Reel
DMC67D8UFDBQ-13	U-DFN2020-6 Type B	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to <https://www.diodes.com/quality/>.
 5. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information



7D = Product Type Marking Code
 YWX = Date Code Marking
 Y = Year (ex: 9 = 2019)
 W = Week (ex: a = week 27; z represents week 52 and 53)
 X = Internal code (ex: U = Monday)

Date Code Key

Year	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	7	8	9	0	1	2	3	4	5

Week	1-26	27-52	53
Code	A-Z	a-z	z

Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat
Code	T	U	V	W	X	Y	Z



DMC67D8UFDBQ

Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic			Symbol	Q1 N-Channel	Q2 P-Channel	Unit
Drain-Source Voltage			V_{DSS}	60	-20	V
Gate-Source Voltage			V_{GSS}	± 20	± 12	V
Continuous Drain Current (Note 7) N-Channel: $V_{GS} = 10\text{V}$ P-Channel: $V_{GS} = -4.5\text{V}$	Steady State	$T_A = +25^\circ\text{C}$	I_D	0.39	-2.9	A
		$T_A = +70^\circ\text{C}$		0.31	-2.3	
Maximum Continuous Body Diode Forward Current (Note 7)			I_S	0.39	-2.9	A
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)			I_{DM}	0.8	-20	A
Pulsed Source Current (10 μs Pulse, Duty Cycle = 1%)			I_{SM}	-0.8	-20	A

Thermal Characteristics

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 6)	$T_A = +25^\circ\text{C}$	P_D	0.58	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	215	$^\circ\text{C/W}$
Total Power Dissipation (Note 7)	$T_A = +25^\circ\text{C}$	P_D	0.89	W
Thermal Resistance, Junction to Ambient (Note 7)	Steady State	$R_{\theta JA}$	140	$^\circ\text{C/W}$
Thermal Resistance, Junction to Case (Note 7)		$R_{\theta JC}$	35	
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics: Q1 N-Channel (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	—	—	V	$V_{GS} = 0\text{V}, I_D = 10\mu\text{A}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1.0	μA	$V_{DS} = 60\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	1.0	—	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	1.7	4.0	Ω	$V_{GS} = 10\text{V}, I_D = 0.5\text{A}$
			1.6	4.1		$V_{GS} = 5\text{V}, I_D = 0.2\text{A}$
			1.8	4.2		$V_{GS} = 4\text{V}, I_D = 0.2\text{A}$
Diode Forward Voltage	V_{SD}	—	0.8	1.1	V	$V_{GS} = 0\text{V}, I_S = 115\text{mA}$
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	—	41	—	pF	$V_{DS} = 25\text{V}, V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	4.4	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	2.6	—	pF	
Gate Resistance	R_g	—	900	—	Ω	$f = 1\text{MHz}, V_{GS} = 0\text{V}, V_{DS} = 0\text{V}$
Total Gate Charge	Q_g	—	0.4	—	pC	$V_{GS} = 4.5\text{V}, V_{DS} = 10\text{V},$ $I_D = 250\text{mA}$
Gate-Source Charge	Q_{gs}	—	0.2	—	pC	
Gate-Drain Charge	Q_{gd}	—	0.1	—	pC	
Turn-On Delay Time	$t_{D(ON)}$	—	3.7	—	ns	$V_{DD} = 30\text{V}, V_{GS} = 10\text{V},$ $R_g = 25\Omega, I_D = 200\text{mA}$
Turn-On Rise Time	t_R	—	3.6	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	102	—	ns	
Turn-Off Fall Time	t_F	—	22	—	ns	
Reverse Recovery Time	t_{RR}	—	20	—	ns	$I_F = 1\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Reverse Recovery Charge	Q_{RR}	—	7.9	—	nC	$I_F = 1\text{A}, di/dt = 100\text{A}/\mu\text{s}$


Electrical Characteristics: Q2 P-Channel (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	-20	—	—	V	$I_D = -250\mu\text{A}$, $V_{GS} = 0\text{V}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-1	μA	$V_{DS} = -20\text{V}$, $V_{GS} = 0\text{V}$
Gate-Body Leakage Current	I_{GSS}	—	—	± 100	nA	$V_{DS} = 0\text{V}$, $V_{GS} = \pm 12\text{V}$
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(th)}$	-0.6	—	-1.25	V	$V_{DS} = V_{GS}$, $I_D = -250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(on)}$	—	51	72	m Ω	$V_{GS} = -4.5\text{V}$, $I_D = -3.5\text{A}$
			87	108		$V_{GS} = -2.7\text{V}$, $I_D = -3.0\text{A}$
			99	123		$V_{GS} = -2.5\text{V}$, $I_D = -2.6\text{A}$
Diode Forward Voltage	V_{SD}	—	-0.79	-1.26	V	$I_S = -1.7\text{A}$, $V_{GS} = 0\text{V}$
DYNAMIC PARAMETERS (Note 9)						
Total Gate Charge	Q_g	—	7.3	—	nC	$V_{GS} = -4.5\text{V}$, $V_{DS} = -10\text{V}$, $I_D = -3.0\text{A}$
Gate-Source Charge	Q_{gs}	—	2.0	—	nC	$V_{GS} = -4.5\text{V}$, $V_{DS} = -10\text{V}$, $I_D = -3.0\text{A}$
Gate-Drain Charge	Q_{gd}	—	1.9	—	nC	$V_{GS} = -4.5\text{V}$, $V_{DS} = -10\text{V}$, $I_D = -3.0\text{A}$
Turn-On Delay Time	$t_{D(on)}$	—	12	—	ns	$V_{DS} = -10\text{V}$, $V_{GS} = -4.5\text{V}$, $R_L = 10\Omega$, $R_G = 6\Omega$
Turn-On Rise Time	t_r	—	20	—	ns	
Turn-Off Delay Time	$t_{D(off)}$	—	38	—	ns	
Turn-Off Fall Time	t_f	—	41	—	ns	
Input Capacitance	C_{iss}	—	443	—	pF	$V_{DS} = -16\text{V}$, $V_{GS} = 0\text{V}$ $f = 1.0\text{MHz}$
Output Capacitance	C_{oss}	—	128	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	101	—	pF	

- Notes:
6. Device mounted on FR-4 substrate PCB, 2oz copper, with minimum recommended pad layout.
 7. Device mounted on FR-4 substrate PCB, 2oz copper, with 1inch square copper plate.
 8. Short duration pulse test used to minimize self-heating effect.
 9. Guaranteed by design. Not subject to product testing.



DMC67D8UFDBQ

Typical Characteristics: N-Channel

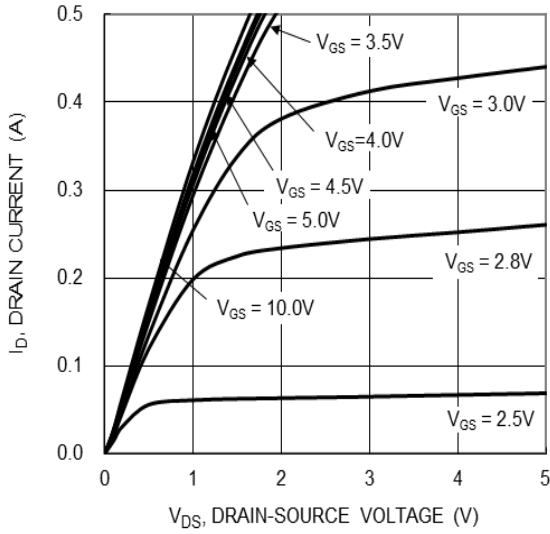


Figure 1. Typical Output Characteristic

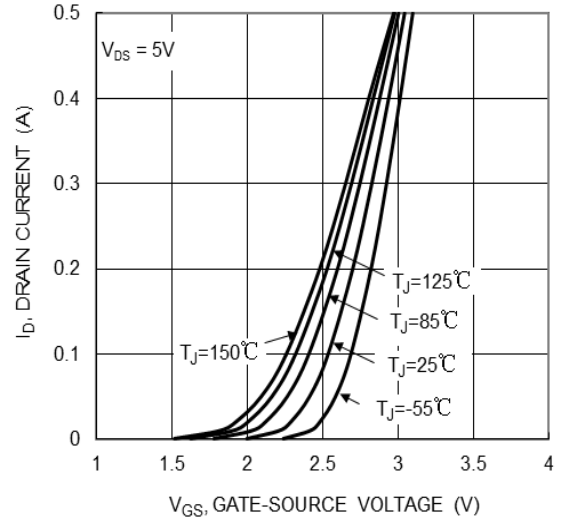


Figure 2. Typical Transfer Characteristic

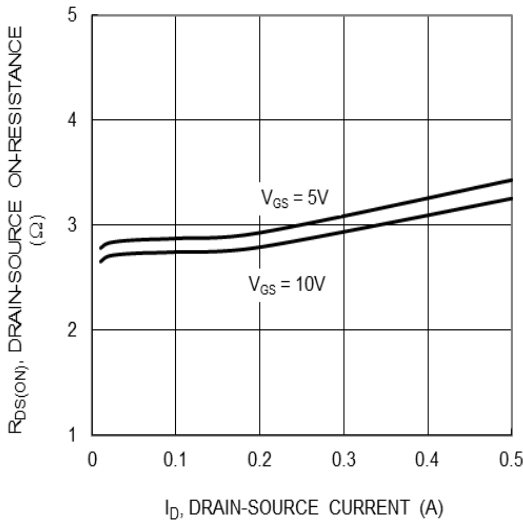


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

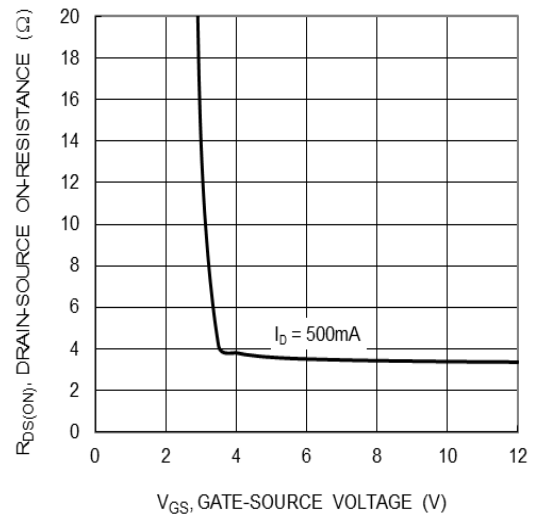


Figure 4. Typical Transfer Characteristic

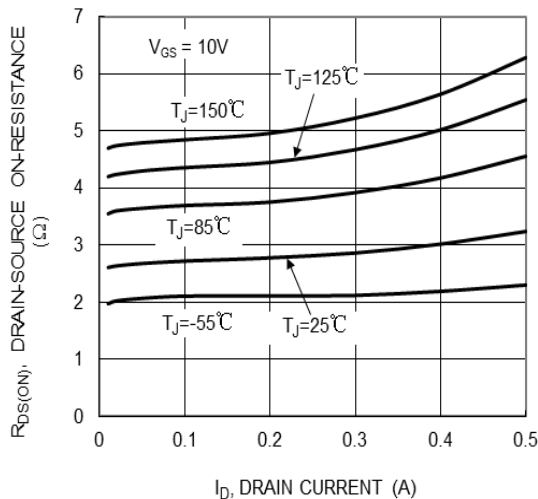


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

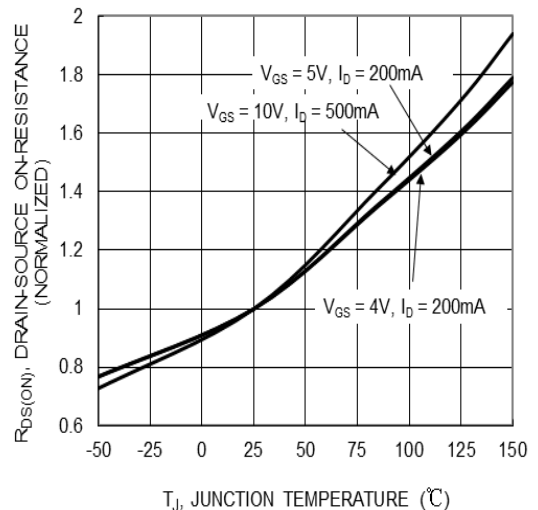


Figure 6. On-Resistance Variation with Junction Temperature



DMC67D8UFDBQ

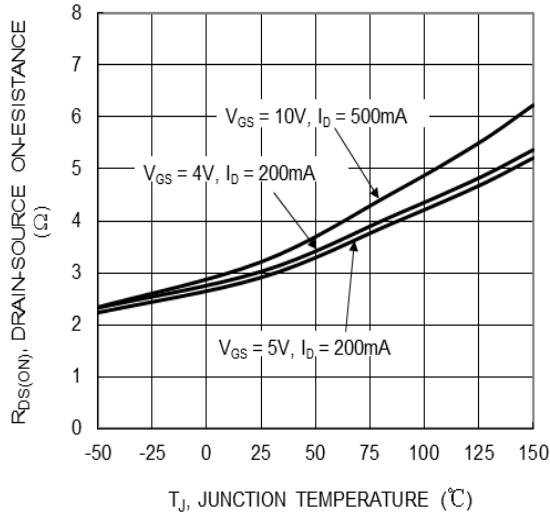


Figure 7. On-Resistance Variation with Junction Temperature

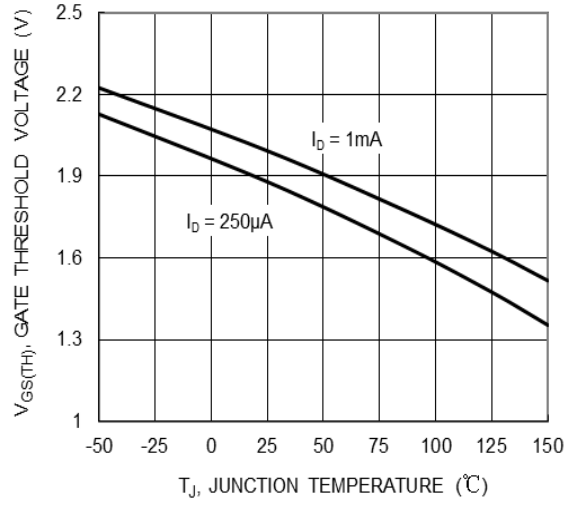


Figure 8. Gate Threshold Variation vs. Junction Temperature

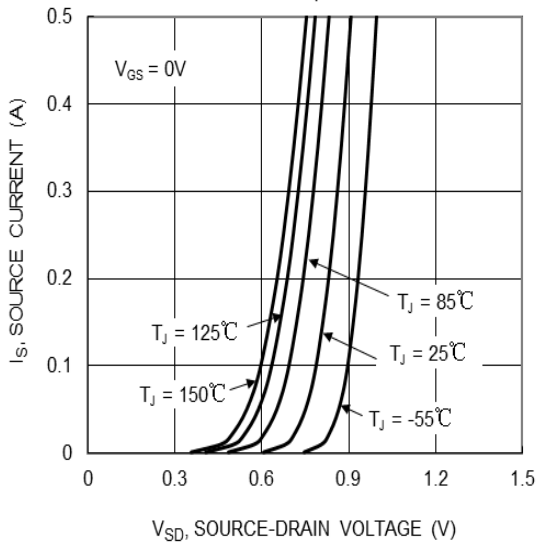


Figure 9. Diode Forward Voltage vs. Current

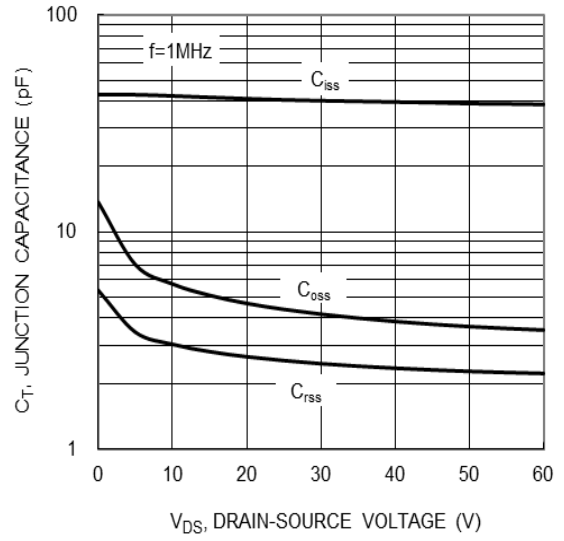


Figure 10. Typical Junction Capacitance

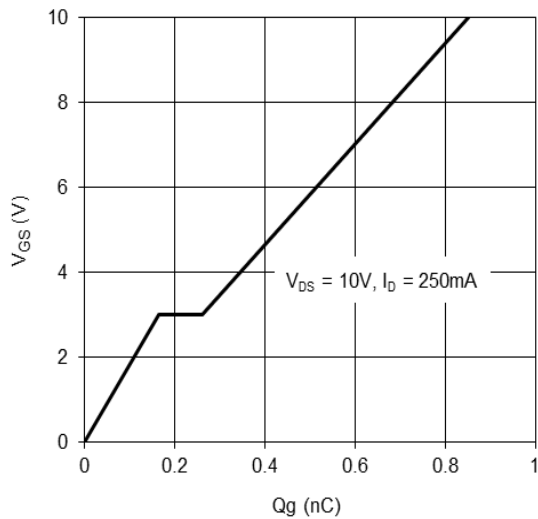


Figure 11. Gate Charge

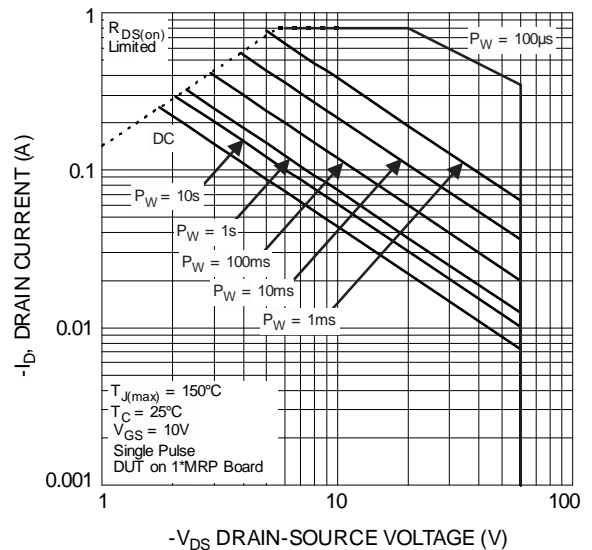


Figure 12 SOA, Safe Operation Area



DMC67D8UFDBQ

Typical Characteristics: P-Channel

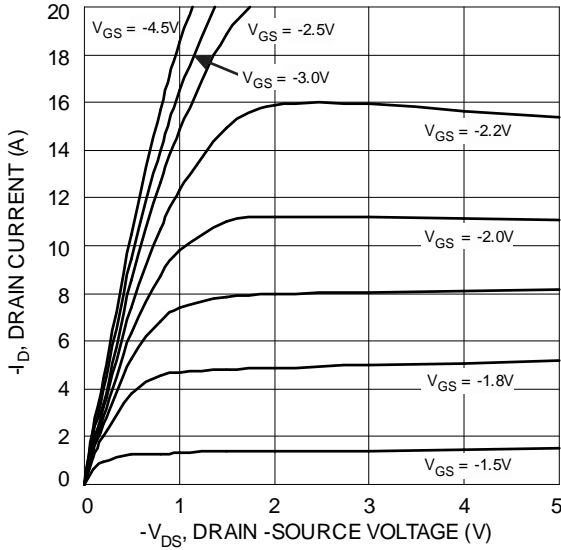


Figure 1 Typical Output Characteristics

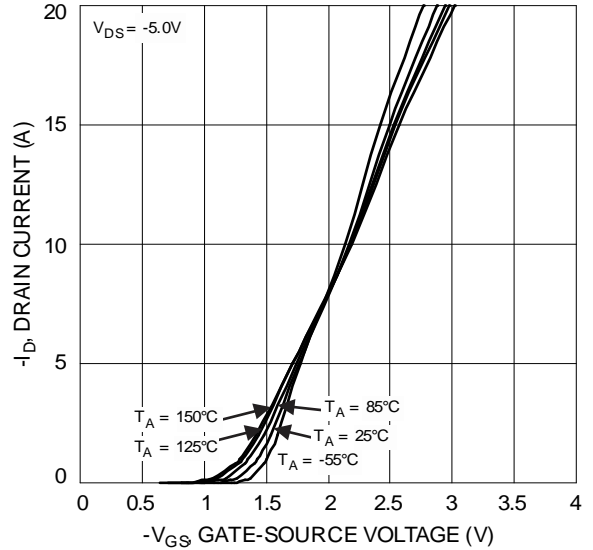


Figure 2 Typical Transfer Characteristics

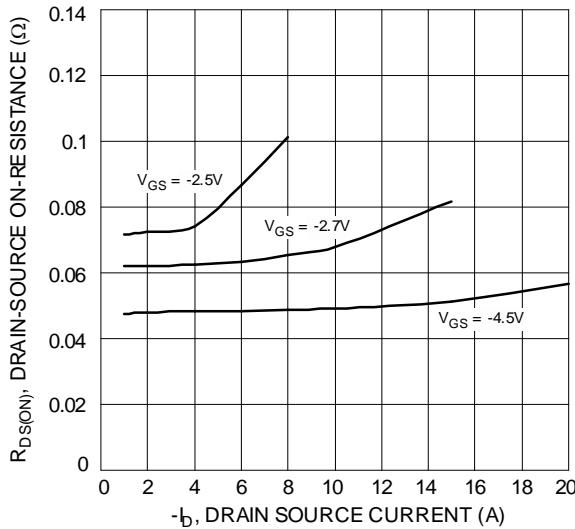


Figure 3 Typical On-Resistance vs. Drain Current and Gate Voltage

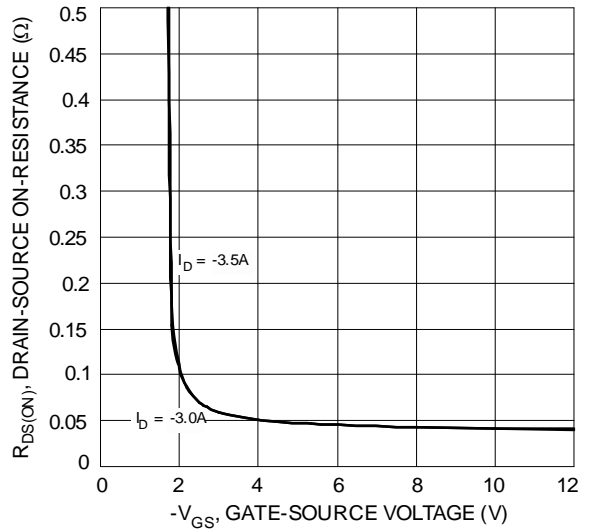


Figure 4 Typical Drain-Source On-Resistance vs. Gate-Source Voltage

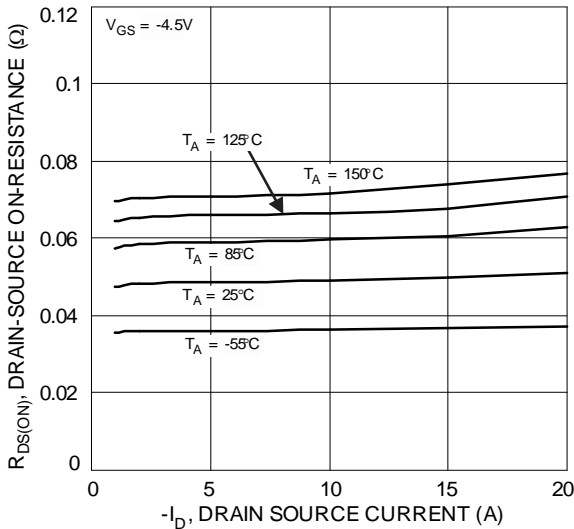


Figure 5 Typical On-Resistance vs. Drain Current and Temperature

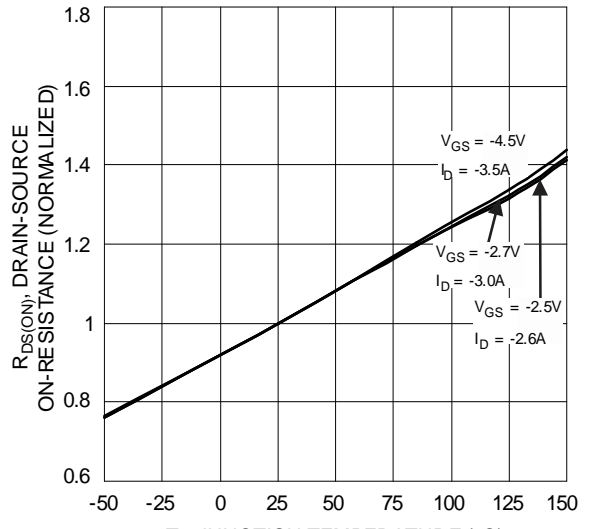


Figure 6 On-Resistance Variation with Temperature



DMC67D8UFDBQ

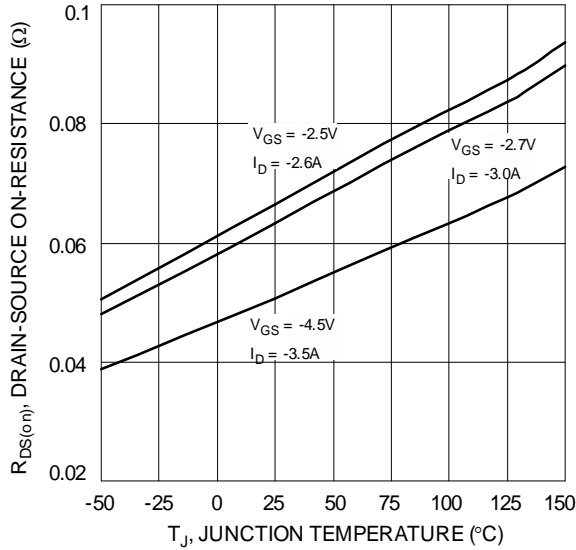


Figure 7 On-Resistance Variation with Temperature

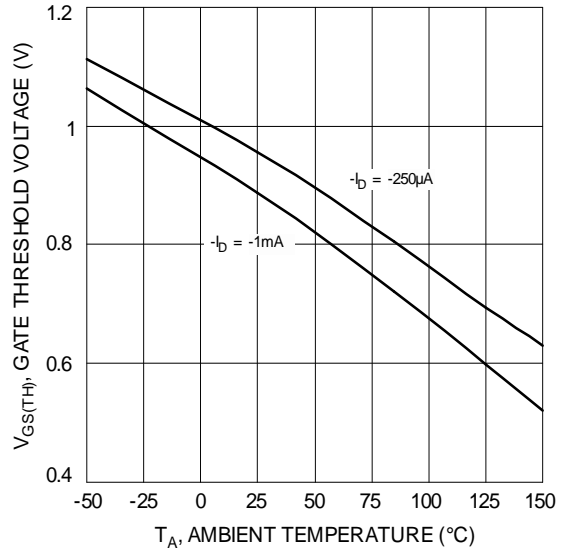


Figure 8 Gate Threshold Variation vs. Ambient Temperature

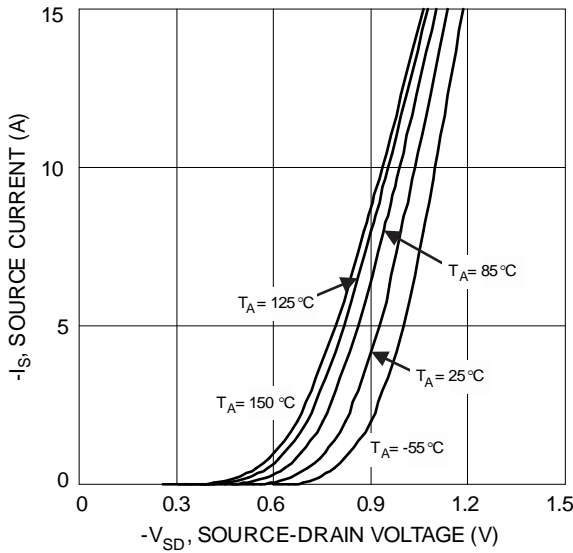


Figure 9 Diode Forward Voltage vs. Current

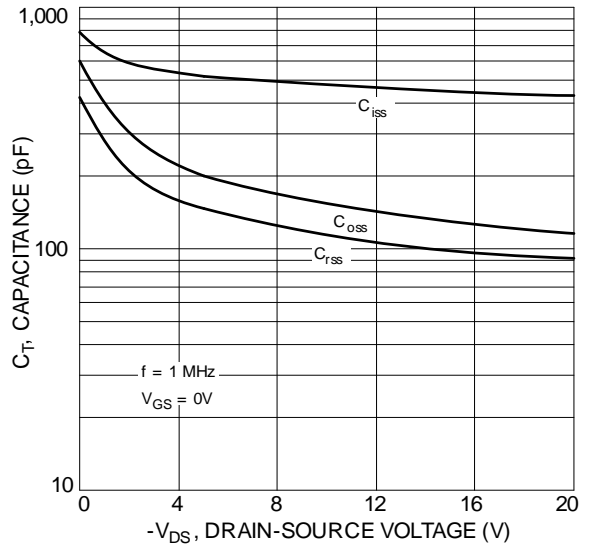


Figure 10 Typical Total Capacitance

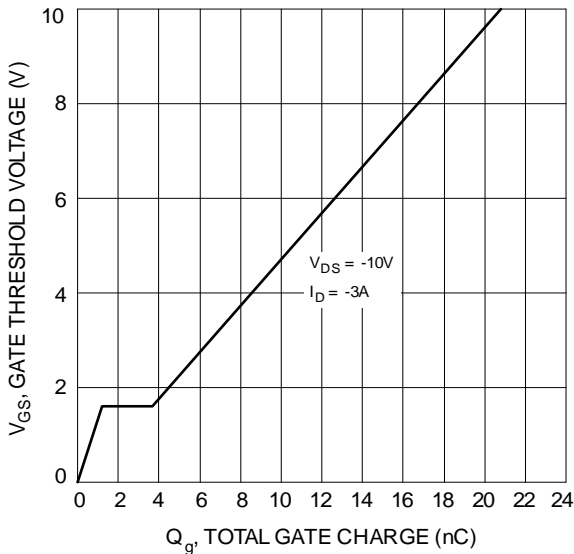


Figure 11 Gate Charge

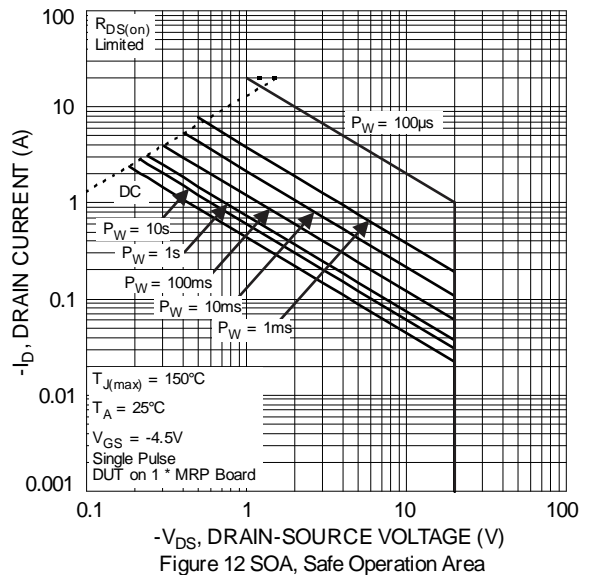
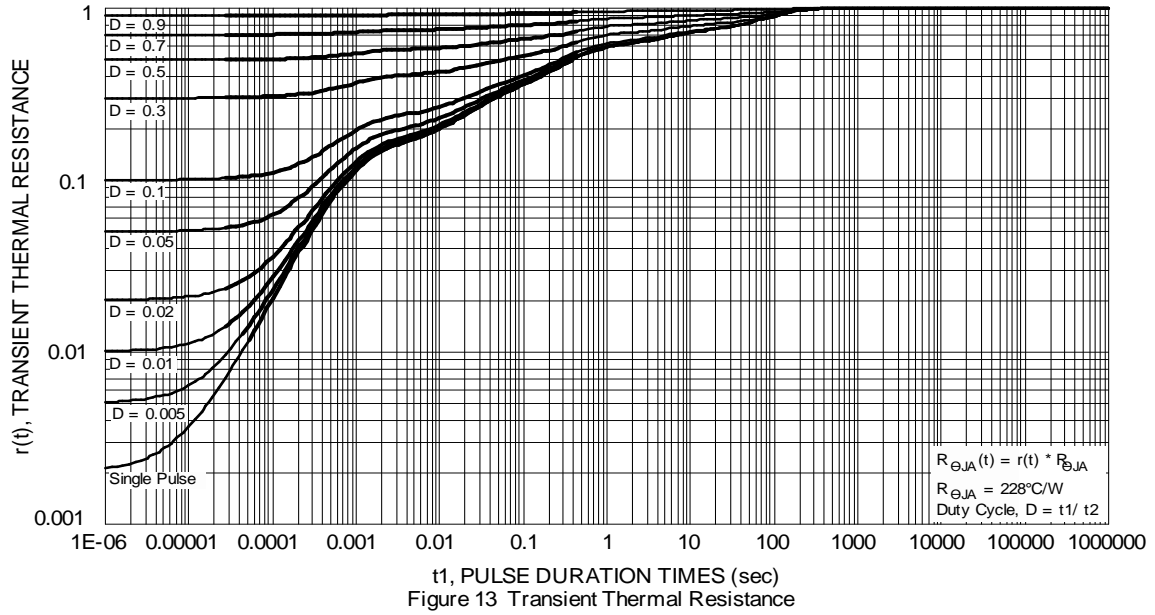


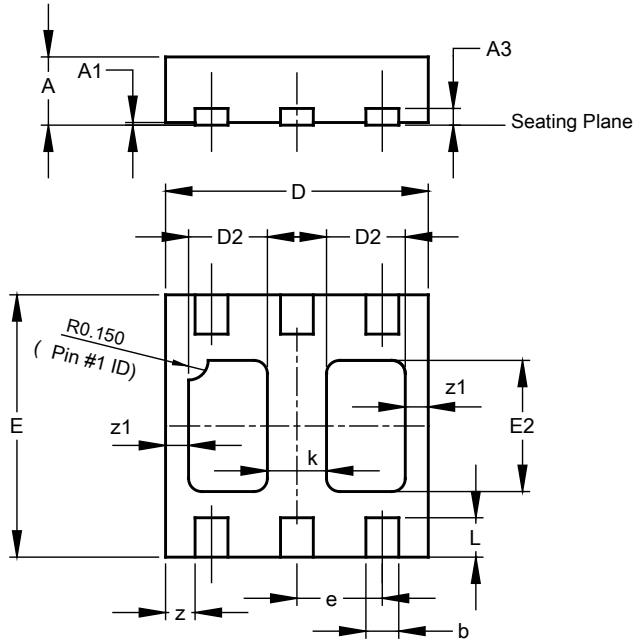
Figure 12 SOA, Safe Operation Area



Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type B)

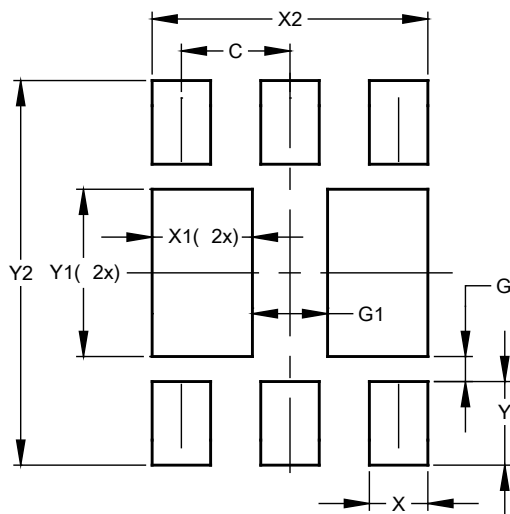


U-DFN2020-6 Type B			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
e	-	-	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	-	-	0.45
L	0.25	0.35	0.30
z	-	-	0.225
z1	-	-	0.175
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2020-6 (Type B)



Dimensions	Value (in mm)
C	0.650
G	0.150
G1	0.450
X	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300



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A. Life support devices or systems are devices or systems which:

1. are intended to implant into the body, or
2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

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