

DMG3415U-13 Datasheet



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DiGi Electronics Part Number DMG3415U-13-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number DMG3415U-13

Description MOSFET P-CH DFN-3

Detailed Description P-Channel 20 V 4A (Ta) 900mW (Ta) Surface Mount

SOT-23-



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RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

| Manufacturer Product Number: | Manufacturer: |
|-----------------------------------------|-----------------------------------------|
| DMG3415U-13 | Diodes Incorporated |
| Series: | Product Status: |
| | Active |
| FET Type: | Technology: |
| P-Channel | MOSFET (Metal Oxide) |
| Drain to Source Voltage (Vdss): | Current - Continuous Drain (Id) @ 25°C: |
| 20 V | 4A (Ta) |
| Drive Voltage (Max Rds On, Min Rds On): | Rds On (Max) @ ld, Vgs: |
| 1.8V, 4.5V | 42.5mOhm @ 4A, 4.5V |
| Vgs(th) (Max) @ ld: | Gate Charge (Qg) (Max) @ Vgs: |
| 1V @ 250μA | 9.1 nC @ 4.5 V |
| Vgs (Max): | Input Capacitance (Ciss) (Max) @ Vds: |
| ±8V | 294 pF @ 10 V |
| FET Feature: | Power Dissipation (Max): |
| | 900mW (Ta) |
| Operating Temperature: | Grade: |
| -55°C ~ 150°C (TJ) | Automotive |
| Qualification: | Mounting Type: |
| AEC-Q101 | Surface Mount |
| Supplier Device Package: | Package / Case: |
| SOT-23-3 | TO-236-3, SC-59, SOT-23-3 |
| Base Product Number: | |
| DMG3415 | |

Environmental & Export classification

8541.21.0095

| RoHS Status: | Moisture Sensitivity Level (MSL): |
|------------------|-----------------------------------|
| ROHS3 Compliant | 1 (Unlimited) |
| REACH Status: | ECCN: |
| REACH Unaffected | EAR99 |
| HTSUS: | |



NOT RECOMMENDED FOR NEW DESIGN USE DMP2045U



DMG3415U

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

| BV _{DSS} | R _{DS(ON) max} | I _D T _A = +25°C |
|-------------------|--------------------------------------------------|------------------------------------------|
| 001/ | $42.5 \text{m}\Omega$ @ $V_{GS} = -4.5 \text{V}$ | -4.0A |
| -20V | $71m\Omega @ V_{GS} = -1.8V$ | -2.0A |

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

Features

- Low On-Resistance
- Low Input Capacitance
- · Fast Switching Speed
- Low Input/Output Leakage
- ESD Protected Up To 3kV
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

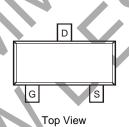
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
 Solderable per MIL-STD-202, Method 208 (a)
- Terminals Connections: See Diagram Below
- Weight: 0.008 grams (Approximate)



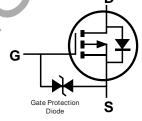


SOT23

Top View



Internal Schematic



Equivalent Circuit

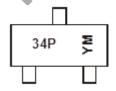
Ordering Information (Note 5)

| Part Number | Compliance | Case | Packaging |
|-------------|------------|-------|--------------------|
| DMG3415U-7 | Standard | SOT23 | 3,000/Tape & Reel |
| DMG3415U-13 | Standard | SOT23 | 10,000/Tape & Reel |

Notes: 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



34P = Product Type Marking Code YM or \overline{Y} M = Date Code Marking Y or \overline{Y} = Year (ex: F = 2018) M = Month (ex: 9 = September)

Date Code Key

| Year | 2018 | 8 | 2019 | | 2020 | 20 | 21 | 2022 | | 2023 | 2 | 2024 |
|-------|------|-----|------|-----|------|-----|-----|------|-----|------|-----|------|
| Code | F | | G | | Н | | | J | | K | | L |
| Month | Jan | Feb | Mar | Apr | May | Jun | Jul | Aug | Sep | Oct | Nov | Dec |
| Code | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | 0 | N | D |



| Characteristic | Symbol | Value | Unit | | | |
|-----------------------------------------------------------|----------------------------------------------------|--------------|------|--|---|--|
| Drain-Source Voltage | rain-Source Voltage V _{DSS} -20 V | | | | V | |
| Gate-Source Voltage | V _{GSS} | ±8 | V | | | |
| Continuous Drain Current (Note 6) V _{GS} = -4.5V | I _D | -4.0 -3.5 | А | | | |
| Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%) | Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%) | | | | | |

Thermal Characteristics ($@T_A = +25^{\circ}C$, unless otherwise specified.)

| Characteristic | Symbol | Value | Unit |
|--------------------------------------------------|-----------------------------------|-------------|------|
| Total Power Dissipation (Note 6) | PD | 0.9 | W |
| Thermal Resistance, Junction to Ambient (Note 6) | $R_{\theta JA}$ | 139 | °C/W |
| Thermal Resistance, Junction to Case (Note 6) | R ₀ JC | 32 | °C/W |
| Operating and Storage Temperature Range | T _J , T _{STG} | -55 to +150 | °C |

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

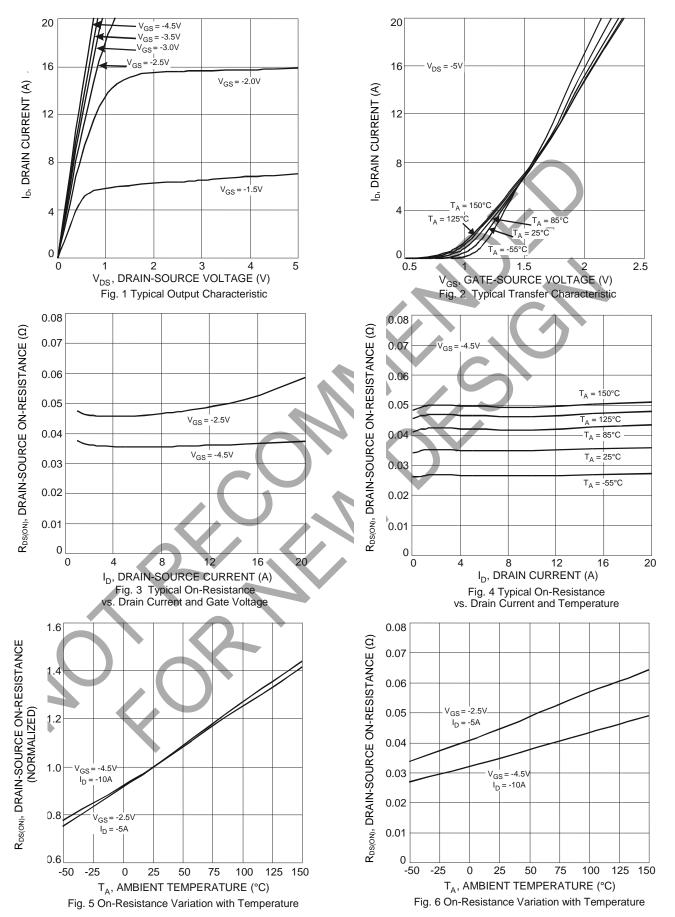
| Characteristic | Symbol | Min | Тур | Max | Unit | Test Condition | | | |
|------------------------------------|-----------------------------|------|-------|------|------|-----------------------------------------------------|--|--|--|
| OFF CHARACTERISTICS (Note 7) | | | | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | -20 | | | V | $V_{GS} = 0V, I_D = -250\mu A$ | | | |
| Zero Gate Voltage Drain Current | I _{DSS} | | _ | -1 | μΑ | V _{DS} = -20V, V _{GS} = 0V | | | |
| Gate-Source Leakage | I _{GSS} | _ | 14 | ±10 | μΑ | $V_{GS} = \pm 8.0V, V_{DS} = 0V$ | | | |
| ON CHARACTERISTICS (Note 7) | ON CHARACTERISTICS (Note 7) | | | | | | | | |
| Gate Threshold Voltage | V _{GS(TH)} | -0.3 | -0.55 | -1.0 | V | $V_{DS} = V_{GS}$, $I_D = -250\mu A$ | | | |
| | | | 31 | 42.5 | | $V_{GS} = -4.5V, I_D = -4.0A$ | | | |
| Static Drain-Source On-Resistance | R _{DS(ON)} | | 40 | 53 | mΩ | $V_{GS} = -2.5V, I_D = -3.5A$ | | | |
| | | | 51 | 71 | | $V_{GS} = -1.8V, I_D = -2.0A$ | | | |
| Forward Transfer Admittance | g _F s | | 3 | _ | S | $V_{DS} = -5V, I_{D} = -4A$ | | | |
| DYNAMIC CHARACTERISTICS (Note 8) | | | | | | | | | |
| Input Capacitance | C _{iss} | _ | 294 | _ | pF | | | | |
| Output Capacitance | Coss | _ | 104 | _ | pF | $V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz | | | |
| Reverse Transfer Capacitance | Crss | _ | 25 | _ | pF | 1 - 1.500112 | | | |
| Gate Resistance | R_g | _ | 250 | _ | Ω | $V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1.0MHz$ | | | |
| SWITCHING CHARACTERISTICS (Note 8) | | | | | | | | | |
| Total Gate Charge | Q_g | _ | 9.1 | _ | nC | 45,4,4,4,4,4,4,4,4,4,4,4,4,4,4,4,4,4,4, | | | |
| Gate-Source Charge | Q_{gs} | _ | 1.5 | _ | nC | $V_{GS} = -4.5V, V_{DS} = -10V$ $I_{D} = -4A$ | | | |
| Gate-Drain Charge | Q_{gd} | _ | 1.7 | _ | nC | 10 - 4A | | | |
| Turn-On Delay Time | t _{D(ON)} | _ | 71 | _ | ns | | | | |
| Turn-On Rise Time | t _R | | 117 | _ | ns | $V_{DS} = -10V, V_{GS} = -4.5V,$ | | | |
| Turn-Off Delay Time | t _{D(OFF)} | _ | 795 | _ | ns | $R_D = 2.5\Omega$, $R_G = 3.0\Omega$, $I_D = -1A$ | | | |
| Turn-Off Fall Time | t _F | _ | 393 | _ | ns | | | | |

Notes:

- 6. Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
- 7. Short duration pulse test used to minimize self-heating effect.

 8. Guaranteed by design. Not subject to production testing.





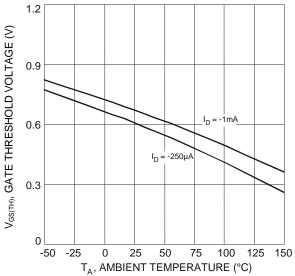
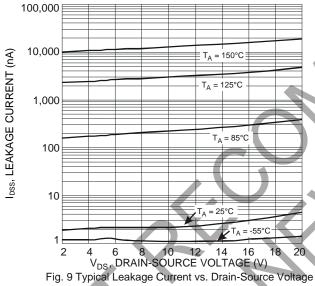
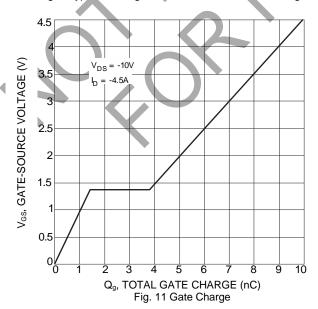
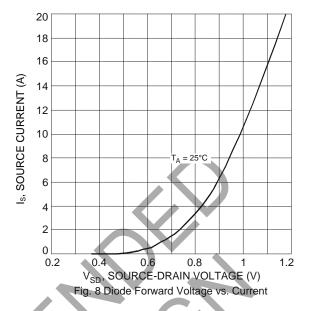
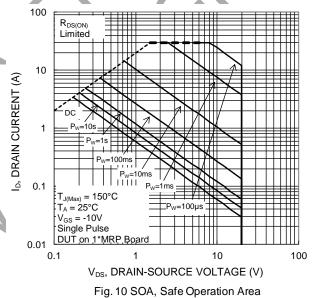


Fig. 7 Gate Threshold Variation vs. Ambient Temperature

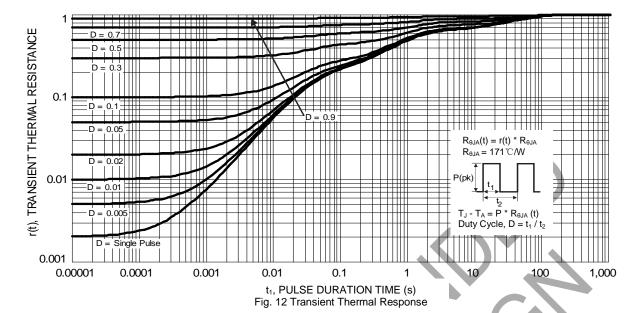










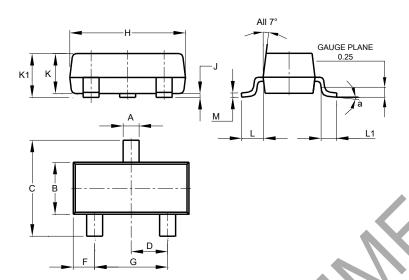




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

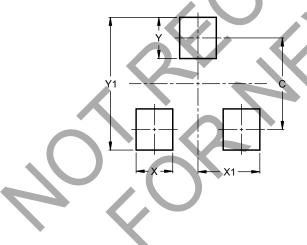


| SOT23 | | | | | | | |
|-------|--------|---------|-------|--|--|--|--|
| Dim | Min | Max | Тур | | | | |
| Α | 0.37 | 0.51 | 0.40 | | | | |
| В | 1.20 | 1.40 | 1.30 | | | | |
| С | 2.30 | 2.50 | 2.40 | | | | |
| D | 0.89 | 1.03 | 0.915 | | | | |
| F | 0.45 | 0.60 | 0.535 | | | | |
| G | 1.78 | 2.05 | 1.83 | | | | |
| H | 2.80 | 3.00 | 2.90 | | | | |
| J | 0.013 | 0.10 | 0.05 | | | | |
| K | 0.890 | 1.00 | 0.975 | | | | |
| K1 | 0.903 | 1.10 | 1.025 | | | | |
| L | 0.45 | 0.61 | 0.55 | | | | |
| L1 | 0.25 | 0.55 | 0.40 | | | | |
| М | 0.085 | 0.150 | 0.110 | | | | |
| а | 0° | 8° | | | | | |
| All | Dimens | ions in | mm | | | | |

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



| Dimensions | Value (in mm) |
|------------|---------------|
| С | 2.0 |
| Х | 0.8 |
| X1 | 1.35 |
| Y | 0.9 |
| Y1 | 2.9 |



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