

DMN2053UW-7 Datasheet

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DiGi Electronics Part Number	DMN2053UW-7-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMN2053UW-7
Description	MOSFET N-CH 20V 2.9A SOT323
Detailed Description	N-Channel 20 V 2.9A (Ta) 470mW (Ta) Surface Mou nt SOT-323

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
DMN2053UW-7	Diodes Incorporated
Series:	Product Status:
	Active
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
20 V	2.9A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ ld, Vgs:
1.5V, 4.5V	56mOhm @ 2A, 4.5V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
1V @ 250µA	3.6 nC @ 4.5 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±12V	369 pF @ 10 V
FET Feature:	Power Dissipation (Max):
	470mW (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
SOT-323	SC-70, SOT-323
Base Product Number:	
DMN2053	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0095	





Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D Max T _A = +25°C
20V	56mΩ @ V _{GS} = 4.5V	2.9A
	65mΩ @ V _{GS} = 2.5V	2.7A
	93mΩ @ V _{GS} = 1.8V	2.2A
	140mΩ @ V _{GS} = 1.5V	1.8A

Description and Applications

This MOSFET has been designed to minimize the on-state resistance $(R_{DS(ON)})$ and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- General Purpose Interfacing Switch
- Power Management Functions
- DC-DC Converters
- Analog Switch

20V N-CHANNEL ENHANCEMENT MODE MOSFET

Features and Benefits

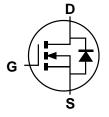
- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

Mechanical Data

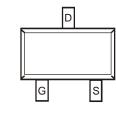
- Case: SOT323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (c3)
- Weight: 0.027 grams (Approximate)



Top View



Equivalent Circuit



Top View

Ordering Information (Note 4)

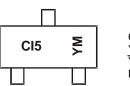
	Part Number	Case	Packaging
	DMN2053UW-7	SOT323	3,000/Tape & Reel
	DMN2053UW-13	SOT323	10,000/Tape & Reel
Notes:	1. No purposely added lead. Fully EU Direct	tive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/	863/EU (RoHS 3) compliant.

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and

Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



CI5 = Product Type Marking Code

- YM = Date Code Marking
- \overline{Y} = Year (ex: G = 2019)
- M = Month (ex: 9 = September)

Date Code I	Ke
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Year	2018	2	019	2020	2	2021	2022		2023	2024		2025
Code	F		G	Н			J		К	L		М
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Drain-Source Voltage		V _{DSS}	20	V	
Gate-Source Voltage		V _{GSS}	±12	V	
Continuous Drain Current (Note 6) V_{GS} = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	2.9 2.3	А
Pulsed Drain Current (10µs Pulse, Duty Cycle=1%)		I _{DM}	20	А	
Maximum Body Diode Forward Current (Note 5)			Is	1.0	А

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	0.47	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R _θ JA	268	°C/W
Total Power Dissipation (Note 6)		PD	0.7	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	178	°C/W
Operating and Storage Temperature Range		T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

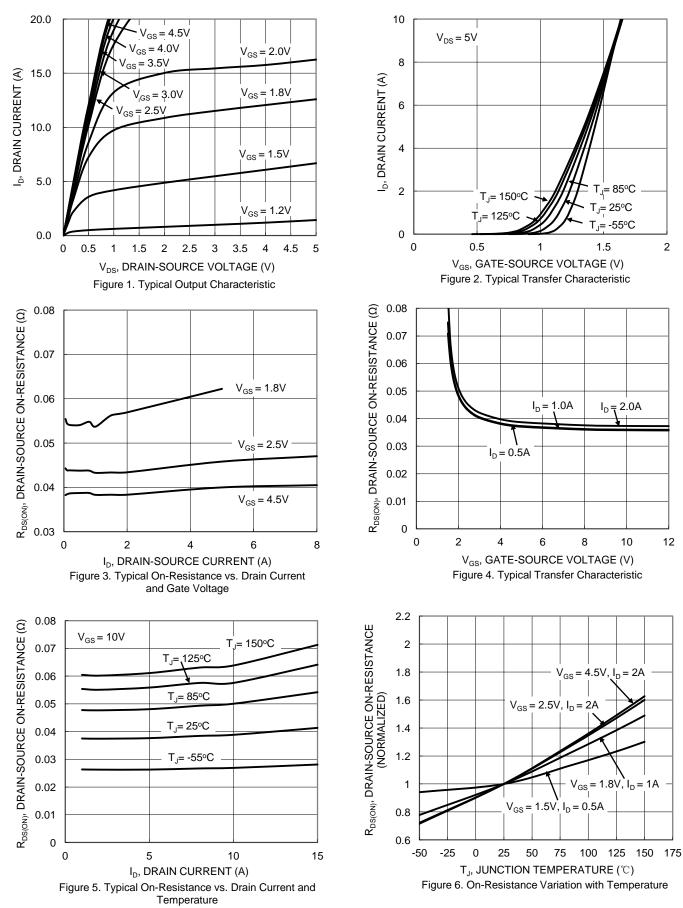
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						·
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 1mA$
Zero Gate Voltage Drain Current @T _C = +25°C	IDSS	_	_	1	μA	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Source Leakage	IGSS	—	_	±1	μA	$V_{GS} = \pm 10V, V_{DS} = 0V$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	V _{GS(TH)}	0.35		1.0	V	$V_{DS} = V_{GS}$, $I_D = 250 \mu A$
			39	56		$V_{GS} = 4.5V, I_D = 2A$
Static Drain-Source On-Resistance	Braker		45	65	mΩ	$V_{GS} = 2.5V, I_D = 2A$
	R _{DS(ON)}		51	93	11152	$V_{GS} = 1.8V, I_D = 1A$
			75	140		$V_{GS} = 1.5V, I_D = 0.5A$
Diode Forward Voltage	V _{SD}	_	0.7	1.0	V	$V_{GS} = 0V, I_S = 1A$
DYNAMIC CHARACTERISTICS (Note 8)	_					
Input Capacitance	Ciss	_	369		pF	
Output Capacitance	Coss	_	54		pF	$V_{DS} = 10V, V_{GS} = 0V,$ - f = 1.0MHz
Reverse Transfer Capacitance	Crss	_	32		pF	1 - 1.000112
Gate Resistance	Rg	—	4.1		Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$
Total Gate Charge	Qg	—	3.6	_	nC	
Gate-Source Charge	Q _{gs}	_	0.4	_	nC	$V_{GS} = 4.5V, V_{DS} = 10V, I_{D} = 6A$
Gate-Drain Charge	Q _{gd}	—	1.0	_	nC	
Turn-On Delay Time	t _{D(ON)}	_	2.6	_	ns	
Turn-On Rise Time	t _R	_	3.0	_	ns	$V_{DD} = 10V, V_{GS} = 5V,$
Turn-Off Delay Time	tD(OFF)	_	12.5		ns	$R_G = 6\Omega, I_D = 6A$
Turn-Off Fall Time	t _F		3.6		ns	<u>] </u>
Reverse Recovery Time	t _{RR}		6.0		ns	I _F = 1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q _{RR}	_	0.9		nC	I _F = 1.0A, di/dt = 100A/µs

Notes:

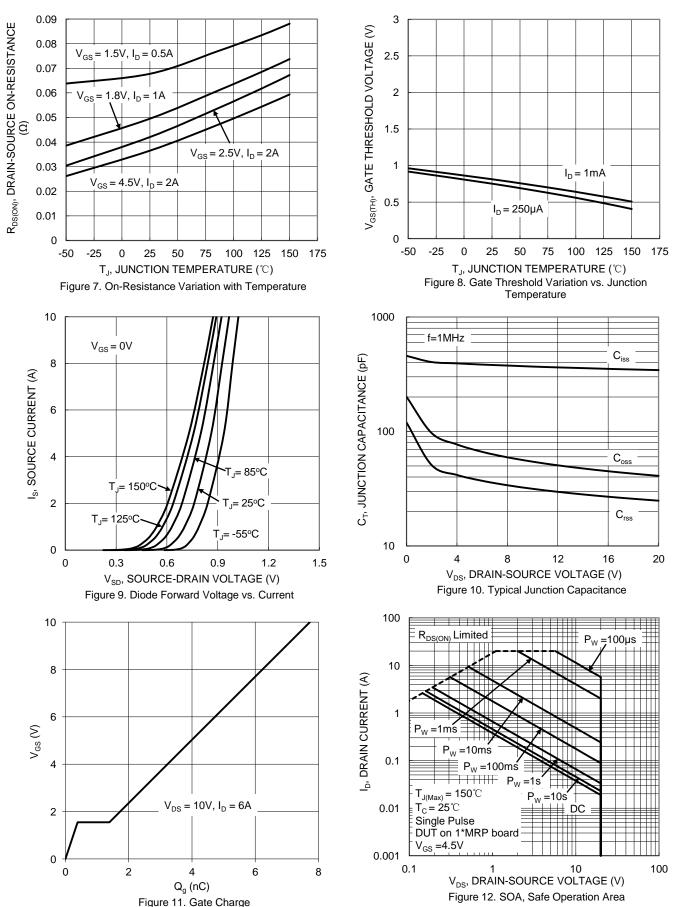
Device mounted on FR-4 substrate PC board, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.

Short duration pulse test used to minimize self-heating effect.
 Guaranteed by design. Not subject to product testing.



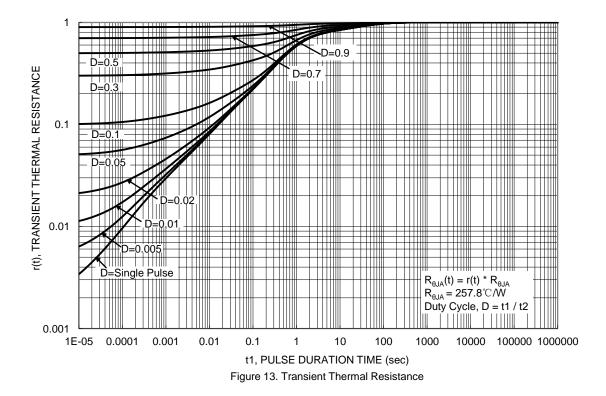






DMN2053UW Document number: DS41138 Rev. 2 - 2



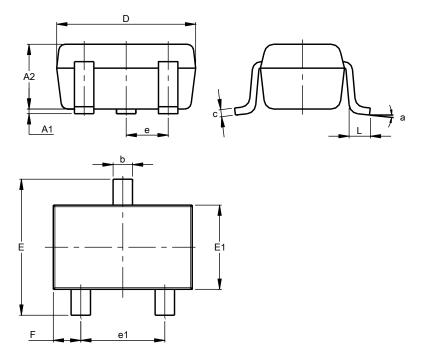




Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

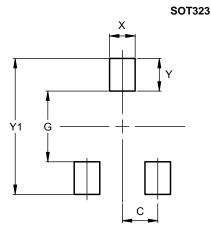
SOT323



SOT323							
Dim	Min	Max	Тур				
A1	0.00	0.10	0.05				
A2	0.90	1.00	0.95				
b	0.25	0.40	0.30				
С	0.10	0.18	0.11				
D	1.80	2.20	2.15				
Е	2.00	2.20	2.10				
E1	1.15	1.35	1.30				
е	C).650 B	SC				
e1	1.20	1.40	1.30				
F	0.375	0.475	0.425				
L	0.25	0.40	0.30				
а	0°	8°					
All	Dimen	sions i	in mm				

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



 Dimensions
 Value (in mm)

 C
 0.650

 G
 1.300

 X
 0.470

 Y
 0.600

 Y1
 2.500



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