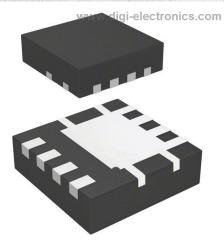


# DMN3029LFG-13 Datasheet



DiGi Electronics Part Number	DMN3029LFG-13-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMN3029LFG-13
Description	MOSFET N-CH 30V 5.3A PWRDI333-8
Detailed Description	N-Channel 30 V 5.3A (Ta) 1W (Ta) Surface Mount PO WERDI3333-8

https://www.DiGi-Electronics.com



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## Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
DMN3029LFG-13	Diodes Incorporated
Series:	Product Status:
	Active
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
30 V	5.3A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ ld, Vgs:
4.5V, 10V	18.6mOhm @ 10A, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
1.8V @ 250µA	11.3 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±25V	580 pF @ 15 V
FET Feature:	Power Dissipation (Max):
	1W (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
PowerDI3333-8	8-PowerVDFN
Base Product Number:	
DMN3029	

## **Environmental & Export classification**

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0095	





### DMN3029LFG

#### **Product Summary**

V <sub>(BR)DSS</sub>	R <sub>DS(ON)</sub>	I <sub>D</sub> T <sub>A</sub> = 25°C
30V	18.6mΩ @ V <sub>GS</sub> = 10V	8.0A
	$26.5m\Omega @ V_{GS} = 4.5V$	6.5A

### Description

This new generation MOSFET has been designed to minimize the onstate resistance ( $R_{DS(on)}$ ) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

### Applications

- Backlighting
- DC-DC Converters
- Power management functions

#### Features

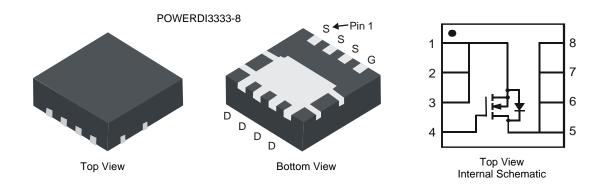
- Low R<sub>DS(ON)</sub> ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product

N-CHANNEL ENHANCEMENT MODE MOSFET

- 100% UIS (Avalanche) rated
- 100% Rg tested
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

### **Mechanical Data**

- Case: POWERDI3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 🔞
- Weight: 0.072 grams (approximate)



### Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3029LFG-7	POWERDI3333-8	2000 / Tape & Reel
DMN3029LFG-13	POWERDI3333-8	3000 / Tape & Reel

Notes:

1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.

See http://www.diodes.com for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.</li>

4. For packaging details, go to our website at http://www.diodes.com.

### **Marking Information**



N39 = Product marking code YYWW = Date code marking YY = Last digit of year (ex: 10 for 2010) WW = Week code (01 - 53)



### Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V <sub>DSS</sub> V <sub>GSS</sub>	30 ±25	V
Gate-Source Voltage					V
Continuous Drain Current (Note 5) $V_{GS} = 10V$	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	Ι <sub>D</sub>	5.3 4.2	A
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	Ι <sub>D</sub>	8.0 6.3	A
Continuous Drain Current (Note 6) V <sub>GS</sub> = 10V	$t \le 10s$	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	9.5 7.7	A
Continuous Drain Current (Note 6) V <sub>GS</sub> = 4.5V	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	ID	6.5 4.9	A
Continuous Drain Current (Note 6) $V_{GS} = 4.5V$	t≤10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	Ι <sub>D</sub>	7.8 6.2	A
Pulsed Drain Current (Note 7)			I <sub>DM</sub>	70	А
Avalanche Current (Notes 7 & 8)		I <sub>AR</sub>	18	А	
Repetitive Avalanche Energy (Notes 7 & 8) L = 0.1mH		E <sub>AR</sub>	16	mJ	

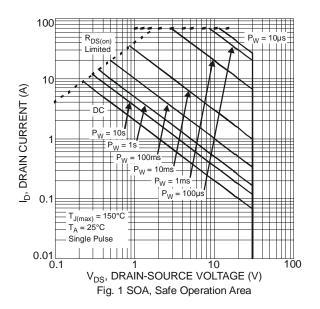
### **Thermal Characteristics**

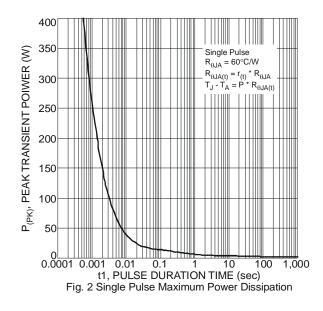
Characteristic	Symbol	Max	Unit
Power Dissipation (Note 5)	PD	1.0	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 5)	R <sub>0JA</sub>	130.6	°C/W
Power Dissipation (Note 6)	PD	2.07	W
Thermal Resistance, Junction to Ambient $@T_A = +25^{\circ}C$ (Note 6)	R <sub>0JA</sub>	62.5	°C/W
Power Dissipation (Note 6) t $\leq$ 10s	PD	3.0	W
Thermal Resistance, Junction to Ambient @T <sub>A</sub> = +25°C (Note 6) t $\leq$ 10s	R <sub>0JA</sub>	43.8	°C/W
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150	°C

Notes: 5. Device mounted on FR-4 PCB with minimum recommended pad layout, single sided.

6. Device mounted on 2" x 2" FR-4 PCB with high coverage 2 oz. Copper, single sided.

7. Repetitive rating, pulse width limited by junction temperature. 8.  $I_{AR}$  and  $E_{AR}$  rating are based on low frequency and duty cycles to keep  $T_J = +25^{\circ}C$ .

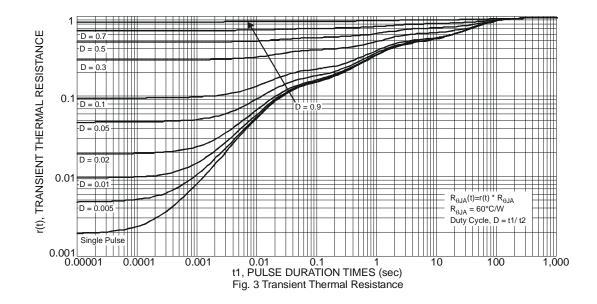




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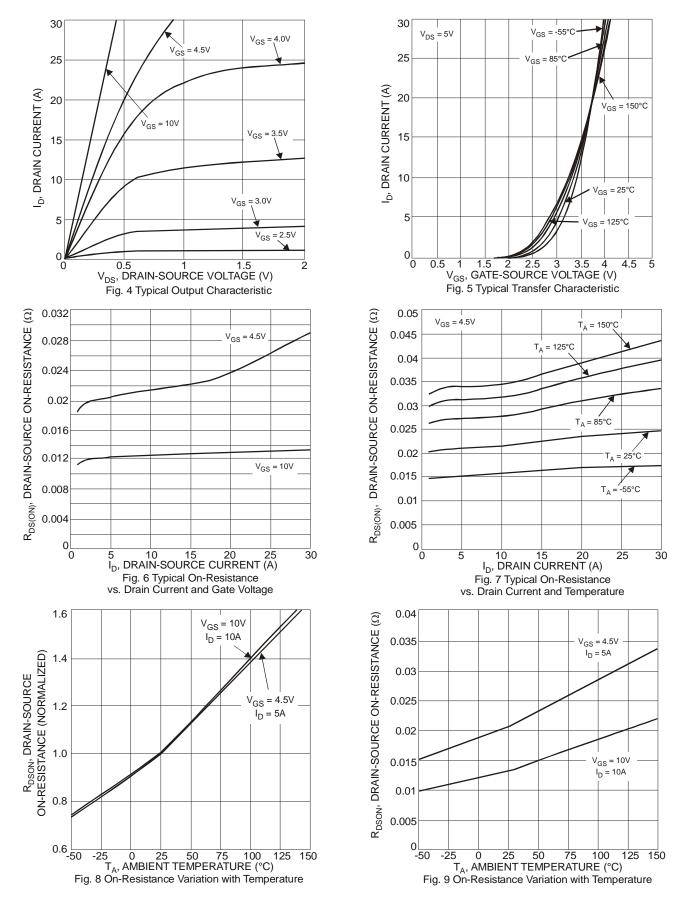


#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.) Characteristic **Test Condition** Symbol Min Тур Max Unit OFF CHARACTERISTICS (Note 9) Drain-Source Breakdown Voltage 30 V $V_{GS} = 0V, I_D = 250 \mu A$ **BV**<sub>DSS</sub> --Zero Gate Voltage Drain Current T<sub>J</sub> = +25°C $V_{DS} = 30V, V_{GS} = 0V$ 0.1 μΑ IDSS --±100 $V_{GS} = \pm 25V, V_{DS} = 0V$ Gate-Source Leakage -nA IGSS ON CHARACTERISTICS (Note 9) Gate Threshold Voltage V<sub>GS(th)</sub> 0.9 1.2 1.8 V $V_{DS} = V_{GS}, I_D = 250 \mu A$ -13,5 18.6 $V_{GS} = 10V, I_D = 10A$ mΩ Static Drain-Source On-Resistance R<sub>DS</sub> (ON) 22 26.5 $V_{GS} = 4.5V, I_D = 7.5A$ -Forward Transfer Admittance S -13.0 $V_{DS} = 5V, I_D = 10A$ |Y<sub>fs</sub>| \_ Diode Forward Voltage \_ 0.7 1.0 V $V_{GS} = 0V, I_S = 1A$ V<sub>SD</sub> **DYNAMIC CHARACTERISTICS (Note 10)** 580 Input Capacitance Ciss \_ - $V_{DS} = 15V, V_{GS} = 0V,$ Output Capacitance 110 pF Coss -f = 1.0MHzReverse Transfer Capacitance 70 -Crss \_ Gate Resistance Rg 2.0 3.0 Ω $V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$ Total Gate Charge V<sub>GS</sub> = 4.5V 5.3 $V_{GS} = 4.5V, V_{DS} = 15V, I_{D} = 10A$ Qg --Total Gate Charge V<sub>GS</sub> = 10V 11.3 Qg -nC $V_{GS} = 10V, V_{DS} = 15V,$ Gate-Source Charge 1.9 Q<sub>qs</sub> -- $I_{D} = 10A$ Gate-Drain Charge 1.9 Q<sub>gd</sub> --Turn-On Delay Time 4.4 ns \_ \_ t<sub>D(on)</sub> Turn-On Rise Time -4.6 ns $V_{GS} = 10V, V_{DS} = 15V,$ tr Turn-Off Delay Time \_ 19.5 \_ $R_L = 15\Omega, R_G = 6\Omega$ ns tD(off) Turn-Off Fall Time 5.8 ns tf -

Notes: 9. Short duration pulse test used to minimize self-heating effect. 10. Guaranteed by design. Not subject to production testing.



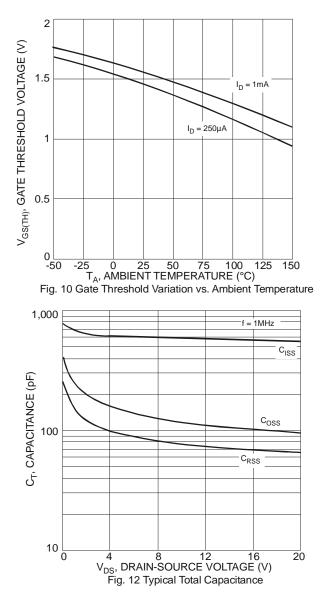
### DMN3029LFG

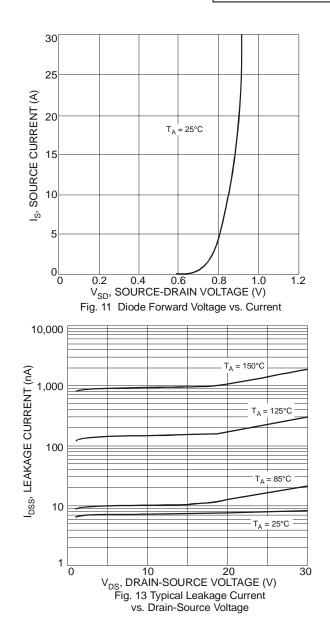


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### DMN3029LFG

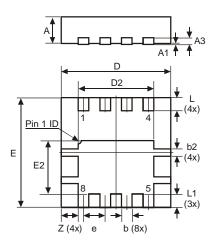






### **Package Outline Dimensions**

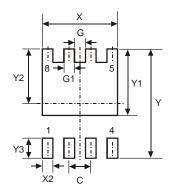
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



POWERDI3333-8					
Dim	Min	Max	Тур		
D	3.25	3.35	3.30		
ш	3.25	3.35	3.30		
D2	2.22	2.32	2.27		
E2	1.56	1.66	1.61		
Α	0.75	0.85	0.80		
A1	0	0.05	0.02		
A3	-	-	0.203		
b	0.27	0.37	0.32		
b2	-	-	0.20		
L	0.35	0.45	0.40		
L1	_	_	0.39		
е	_	_	0.65		
Ζ	-	-	0.515		
All I	All Dimensions in mm				

### Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)	
С	0.650	
G	0.230	
G1	0.420	
Y	3.700	
Y1	2.250	
Y2	1.850	
Y3	0.700	
Х	2.370	
X2	0.420	



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