

# DMN6017SFV-7 Datasheet



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DiGi Electronics Part Number DMN6017SFV-7-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number DMN6017SFV-7

**Description** MOSFET N-CH 60V 35A POWERDI3333

Detailed Description N-Channel 60 V 35A (Tc) 1W (Ta) Surface Mount Po

werDI3333-8 (Type UX)



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### **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
DMN6017SFV-7	Diodes Incorporated
Series:	Product Status:
	Active
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
60 V	35A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ ld, Vgs:
4.5V, 10V	18mOhm @ 6A, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
3V @ 250μA	55 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	2711 pF @ 15 V
FET Feature:	Power Dissipation (Max):
	1W (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
PowerDI3333-8 (Type UX)	8-PowerVDFN
Base Product Number:	
DMN6017	

### **Environmental & Export classification**

8541.29.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	





### 60V N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI3333-8 (Type UX)

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(ON)</sub> max	I <sub>D</sub> max T <sub>C</sub> = +25°C
	18mΩ @ V <sub>GS</sub> = 10V	35A
60V	20mΩ @ V <sub>GS</sub> = 4.5V	34A

## Description and Applications

This MOSFET is designed to minimize the on-state resistance  $(R_{DS(ON)})$  and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

- Backlighting
- Power Management Functions
- DC-DC Converters

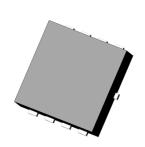
#### **Features and Benefits**

- Low R<sub>DS(ON)</sub> Ensures on state losses are minimized
- Small form factor thermally efficient package enables higher density end products
- Occupies just 33% of the board area occupied by SO-8 enabling smaller end product
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)

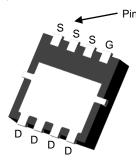
#### **Mechanical Data**

- Case: PowerDI<sup>®</sup>3333-8 (Type UX)
- Case Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Leadframe.
   Solderable per MIL-STD-202, Method 208 63
- Weight: 0.030 grams (Approximate)

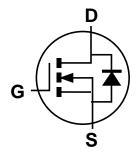
#### PowerDI3333-8 (Type UX)







**Bottom View** 



**Equivalent Circuit** 

#### **Ordering Information** (Note 4)

Part Number	Case	Packaging
DMN6017SFV-7	PowerDI3333-8 (Type UX)	2,000/Tape & Reel
DMN6017SFV-13	PowerDI3333-8 (Type UX)	3,000/Tape & Reel

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



 $\underline{\text{N67}}$  = Product Type Marking Code  $\underline{\text{YY}}$ WW = Date Code Marking  $\underline{\text{YY}}$  = Last Two Digits of Year (ex: 18 = 2018) WW = Week Code (01 to 53)



#### **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage		$V_{DSS}$	60	V
Gate-Source Voltage		$V_{GSS}$	±20	V
Continuous Drain Current (Note 7) V <sub>GS</sub> = 10V	$T_C = +25$ °C $T_C = +70$ °C	I <sub>D</sub>	35 28	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I <sub>DM</sub>	140	Α
Maximum Continuous Body Diode Forward Current (Note 7)		Is	20	Α
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I <sub>SM</sub>	140	Α
Avalanche Current, L = 0.1mH (Note 8)		I <sub>AS</sub>	25	Α
Avalanche Energy, L = 0.1mH (Note 8)		E <sub>AS</sub>	32	mJ

#### Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		$P_D$	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{ hetaJA}$	126	°C/W
Total Power Dissipation (Note 6)		$P_{D}$	2.0	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{ heta JA}$	62	°C/W
Thermal Resistance, Junction to Case (Note 7)		$R_{ heta JC}$	3.7	C/VV
Operating and Storage Temperature Range		T <sub>J,</sub> T <sub>STG</sub>	-55 to +150	°C

#### Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 9)	Oymboi		ΙJP	WIGA	Oilit	rest condition	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	60	_	_	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	_	1	μΑ	V <sub>DS</sub> = 48V, V <sub>GS</sub> = 0V	
Gate-Source Leakage	I <sub>GSS</sub>	_	_	±100	nA	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 9)					I.	, 20	
Gate Threshold Voltage	V <sub>GS(TH)</sub>	1	_	3	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		_	12	18	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 6A	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	_	13	20	11177	$V_{GS} = 4.5V, I_D = 4A$	
Diode Forward Voltage	V <sub>SD</sub>	_	0.7	1	V	V <sub>G</sub> S = 0V, I <sub>S</sub> = 1A	
DYNAMIC CHARACTERISTICS (Note 10)				•	•	•	
Input Capacitance	C <sub>iss</sub>	-	2711	_	pF	\\ A5\\\\\ 0\\	
Output Capacitance	Coss	_	152	_	pF	$V_{DS} = 15V, V_{GS} = 0V,$ f = 1MHz	
Reverse Transfer Capacitance	C <sub>rss</sub>	_	126	_	pF	1 = 11/11/12	
Gate Resistance	Rg	_	1.4	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge (V <sub>GS</sub> = 4.5V)	Qg	_	26	_	nC		
Total Gate Charge (V <sub>GS</sub> = 10V)	Qq	_	55	_	nC	101/ 1 00	
Gate-Source Charge	Q <sub>gs</sub>	_	6.2	_	nC	$V_{DS} = 48V, I_{D} = 6A$	
Gate-Drain Charge	$Q_gd$	_	8.5	_	nC	1	
Turn-On Delay Time	t <sub>D(ON)</sub>	_	4.9	_	ns		
Turn-On Rise Time	t <sub>R</sub>	_	5.4	_	ns	$V_{DD} = 30V, V_{GS} = 10V,$	
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	38.2	_	ns	$R_G = 3.3\Omega, I_D = 6A$	
Turn-Off Fall Time	t <sub>F</sub>	_	11	_	ns	7	
Reverse Recovery Time	t <sub>RR</sub>	_	16.6	_	ns	L CA 41:/44 4000/	
Reverse Recovery Charge	$Q_{RR}$	_	10.3	_	nC	$I_F = 6A$ , di/dt = 100A/ $\mu$ s	

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
  6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
- 7. Thermal resistance from junction to soldering point (on the exposed drain pad).
- 8.  $I_{AS}$  and  $E_{AS}$  ratings are based on low frequency and duty cycles to keep  $T_J$  = +25°C.
- Short duration pulse test used to minimize self-heating effect.
   Guaranteed by design. Not subject to product testing.

10

8

6

2

0

0.1

0.08

0.06

0.04

0.02

0

2.4

0

-50

-25

0

 $R_{DS(ON)}$ , DRAIN-SOURCE ON-RESISTANCE ( $\Omega$ )

ID, DRAIN CURRENT (A)

 $V_{DS} = 5V$ 

T<sub>J</sub> = 150℃

 $I_{D} = 6.0A$ 

 $T_{J} = 125^{\circ}$ 



#### DMN6017SFV

T<sub>J</sub> = 85℃

3.5

T<sub>J</sub> = 25 <sup>'</sup>℃

T<sub>J</sub> = -55℃

 $\begin{array}{ccc} \text{1.5} & \text{2} & \text{2.5} & \text{3} \\ \text{V}_{\text{GS}}, \text{GATE-SOURCE VOLTAGE (V)} \end{array}$ 

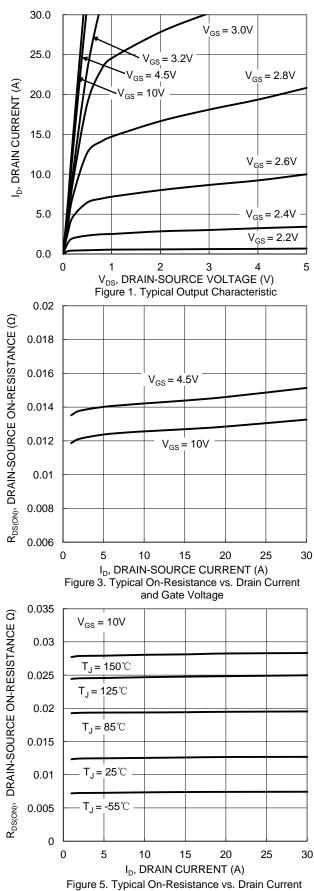
Figure 2. Typical Transfer Characteristic

12

V<sub>GS</sub>, GATE-SOURCE VOLTAGE (V) Figure 4. Typical Transfer Characteristic

16

20



25

 $\rm T_{J},$  JUNCTION TEMPERATURE ( ${\rm ^{\circ}\!C})$  Figure 6. On-Resistance Variation with Junction Temperature

50

75

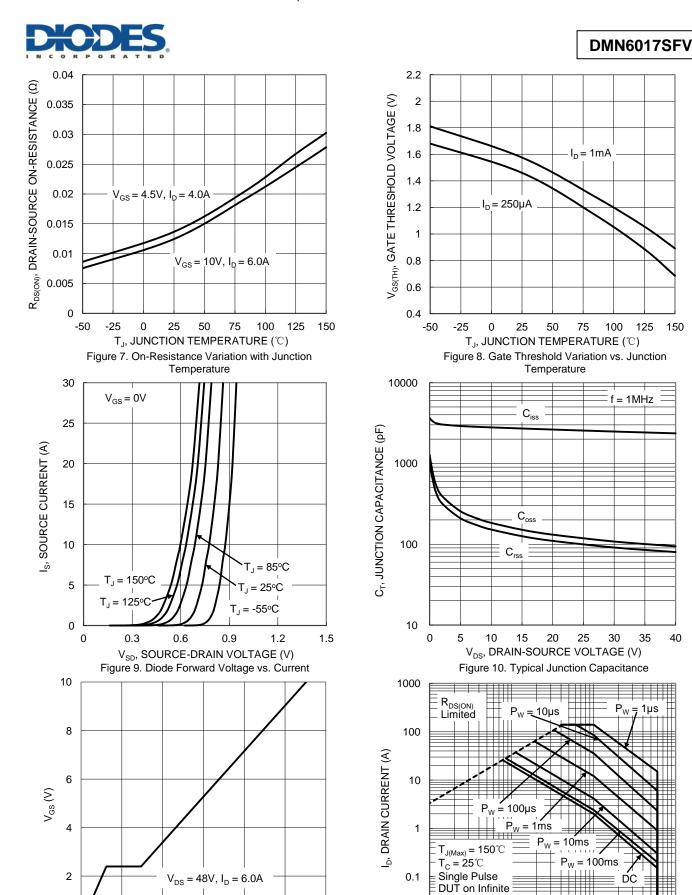
100

125

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and Junction Temperature



10

20

30

 $Q_g$  (nC)

Figure 11. Gate Charge

40

50

60

0

0

Heatsink V<sub>GS</sub> = 10V

 $\begin{array}{cc} & & 1 & 10 \\ V_{DS}, \, DRAIN\text{-SOURCE VOLTAGE (V)} \end{array}$ 

Figure 12. SOA, Safe Operation Area

0.01

0.1

100



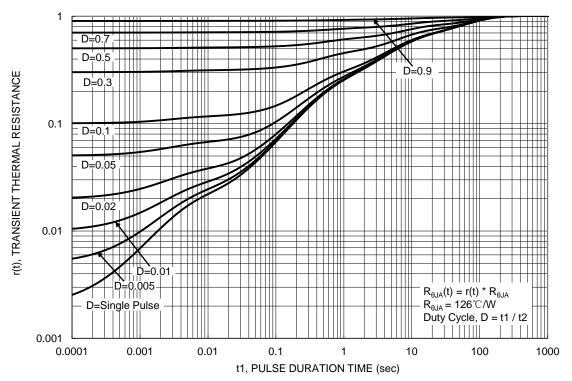


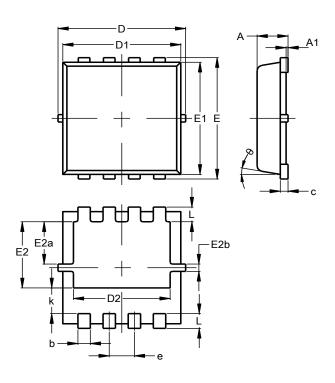
Figure 13. Transient Thermal Resistance



#### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8 (Type UX)

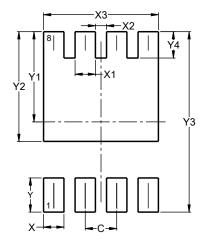


PowerDI3333-8					
(Type UX)					
Dim	Min	Max	Тур		
Α	0.75	0.85	0.80		
A1	0.00	0.05			
b	0.25	0.40	0.32		
С	0.10	0.25	0.15		
D	3.20	3.40	3.30		
D1	2.95	3.15	3.05		
D2	2.30	2.70	2.50		
Е	3.20	3.40	3.30		
E1	2.95	3.15	3.05		
E2	1.60	2.00	1.80		
E2a	0.95	1.35	1.15		
E2b	0.10 0.30 0.20				
е	0.65 BSC				
k	0.50	0.90	0.70		
L	0.30	0.50	0.40		
θ	0°	12°	10°		
All Dimensions in mm					

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### PowerDI3333-8 (Type UX)



Dimensions	Value (in mm)
С	0.650
Х	0.420
X1	0.420
X2	0.230
Х3	2.370
Υ	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



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