

DMP2240UW-7 Datasheet



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DiGi Electronics Part Number DMP2240UW-7-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number DMP2240UW-7

Description MOSFET P-CH 20V 1.5A SOT323

Detailed Description P-Channel 20 V 1.5A (Ta) 250mW (Ta) Surface Mou

nt SOT-323



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
DMP2240UW-7	Diodes Incorporated
Series:	Product Status:
	Active
FET Type:	Technology:
P-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
20 V	1.5A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
1.8V, 4.5V	150mOhm @ 2A, 4.5V
Vgs(th) (Max) @ ld:	Vgs (Max):
1V @ 250μA	±12V
Input Capacitance (Ciss) (Max) @ Vds:	FET Feature:
320 pF @ 16 V	
Power Dissipation (Max):	Operating Temperature:
250mW (Ta)	-55°C ~ 150°C (TJ)
Mounting Type:	Supplier Device Package:
Surface Mount	SOT-323
Package / Case:	Base Product Number:
SC-70, SOT-323	DMP2240

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS.	

8541.21.0095





DMP2240UW

P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

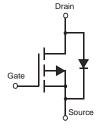
Features

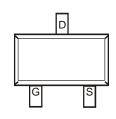
- P-Channel MOSFET
- Low On-Resistance
 - 150 mΩ @ V_{GS} = -4.5V
 - 200 m Ω @ V_{GS} = -2.5V
 - 240 mΩ @ V_{GS} = -1.8V
- Very Low Gate Threshold Voltage V_{GS(th)} ≤ 1V
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- Lead Free By Design/RoHS Compliant (Note 2)
- "Green" Device (Note 3)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals Connections: See Diagram Below
- Terminals: Finish Matte Tin annealed over Alloy 42 leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 4
- Ordering Information: See Page 4
- Weight: 0.006 grams (approximate)







Top View

Internal Schematic

Top View

Maximum Ratings @TA = 25°C unless otherwise specified

Characteristic		Symbol	Value	Units
Drain-Source Voltage		V_{DSS}	-20	V
Gate-Source Voltage		V _{GSS}	±12	V
Drain Current (Note 1)	$T_A = 25$ °C $T_A = 70$ °C	I _D	-1.5 -1.0	Α
Pulsed Drain Current		I _{DM}	-5	Α

Thermal Characteristics

Characteristic	Symbol	Value	Units
Total Power Dissipation (Note 1)	P_{D}	250	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	500	°C/W
Operating and Storage Temperature Range	T _{J,} T _{STG}	-55 to +150	°C

Notes: 1. Device mounted on FR-4 substrate PC board, 2oz. Copper, with minimum recommended pad layout.

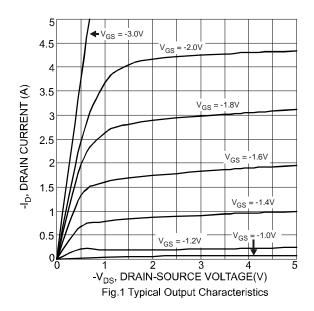
- 2. No purposefully added lead.
- 3. Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

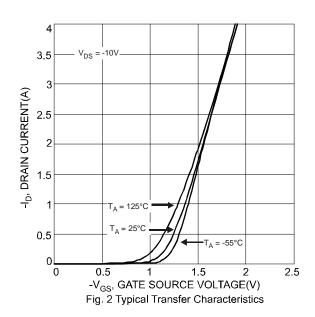


Electrical Characteristics @TA = 25°C unless otherwise specified

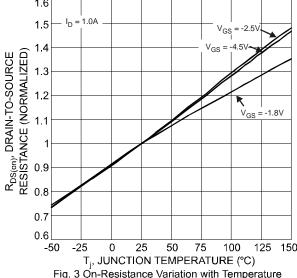
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 4)							
Drain-Source Breakdown Voltage		BV _{DSS}	-20		_	V	$V_{GS} = 0V, I_D = -250\mu A$
Zero Gate Voltage Drain Current	T _J = 25°C T _J = 125°C	I _{DSS}	_	_	-1.0 -5.0	μΑ	V _{DS} = -20V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 12V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 4)							
Gate Threshold Voltage		V _{GS(th)}	-0.45		-1.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$
Static Drain-Source On-Resistance	R _{DS (ON)}	_	92 134 180	150 200 240	mΩ	$V_{GS} = -4.5V$, $I_D = -2.0A$ $V_{GS} = -2.5V$, $I_D = -1.5A$ $V_{GS} = -1.8V$, $I_D = -0.5A$	
Forward Transconductance		g _{FS}	_	3.1	_	S	$V_{DS} = -10V, I_{D} = -810mA$
Diode Forward Voltage (Note 4)		V _{SD}	_	_	-0.9	V	$V_{GS} = 0V, I_{S} = -0.5A$
DYNAMIC CHARACTERISTICS							
Input Capacitance		C _{iss}	_	320	_	pF	101/11/
Output Capacitance	Coss	_	80	_	pF	$V_{DS} = -16V, V_{GS} = 0V$ -f = 1.0MHz	
Reverse Transfer Capacitance	C _{rss}	_	60	_	pF	1 = 1.000112	
Turn-On Delay Time	t _{D(on)}	_	12.5	_	ns		
Turn-On Rise Time	t _r	_	10.3		ns	$V_{DS} = -10V, V_{GS} = -4.5V,$	
Turn-Off Delay Time	t _{D(off)}	_	46.5		ns	$R_L = 10\Omega$, $R_G = 1.0\Omega$	
Turn-Off Fall Time	t _f	_	22.2	_	ns		

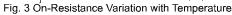
Notes: 4. Short duration pulse test used to minimize self-heating effect.

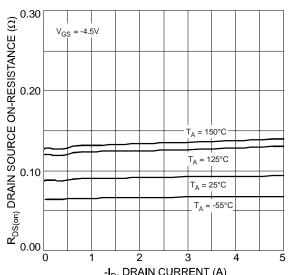












-I_D, DRAIN CURRENT (A) Fig. 5 Drain-Source On-Resistance Vs. Drain Current and Temperature

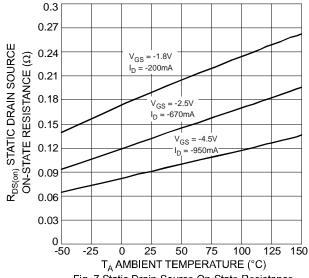


Fig. 7 Static Drain-Source On-State Resistance vs Ambient Temperature

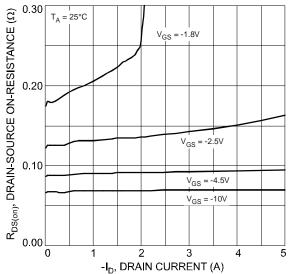
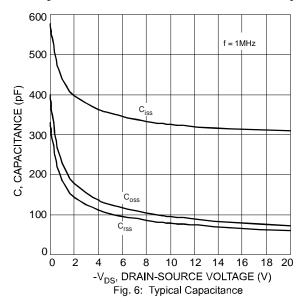
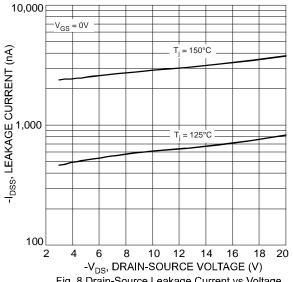
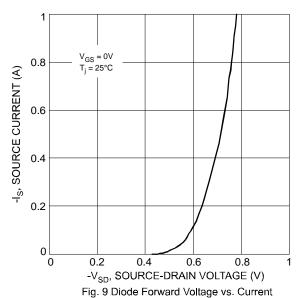


Fig. 4 On-Resistance vs Drain Current and Gate Voltage







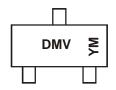


Ordering Information (Note 5)

Part Number	Case	Packaging
DMP2240UW-7	SOT-323	3000/Tape & Reel

5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf. Notes:

Marking Information



DMV = Product Type Marking Code YM = Date Code Marking

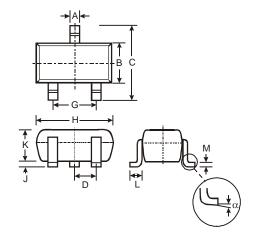
Y = Year (ex: V = 2008)

M = Month (ex: 9 = September)

Date Code Key

Year	2008		2009	2010		2011	2012		2013	2014		2015
Code	V		W	Х		Υ	Z		Α	В		С
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	g Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

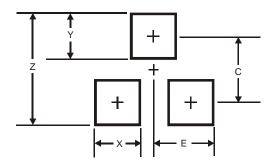
Package Outline Dimensions



Dim Mir A 0.29 B 1.19 C 2.00 D - G 1.20 H 1.80	5 0.4	0 0.30					
B 1.15 C 2.00 D - G 1.20	5 1.3						
C 2.00 D - G 1.20		- 400					
D - G 1.20	-	5 1.30					
G 1.20	2.2	0 2.10					
	-	0.65					
H 1.80	1.4	0 1.30					
	2.2	0 2.15					
J 0.0	0.1	0.05					
K 0.90	1.0	0 1.00					
L 0.25	L 0.25 0.40						
M 0.10	0.1	8 0.11					
α 0°	8°	-					
All Dime	All Dimensions in mm						



Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.8
Х	0.7
Υ	0.9
С	1.9
E	1.0

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