DMP3165L-7 Datasheet

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DiGi Electronics Part Number	DMP3165L-7-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMP3165L-7
Description	MOSFET P-CH 30V 3.3A SOT23 T&R
Detailed Description	P-Channel 30 V 3.3A (Ta) 800mW (Ta) Surface Mount SOT-23-3

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
DMP3165L-7	Diodes Incorporated
Series:	Product Status:
-	Active
FET Type:	Technology:
P-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
30 V	3.3A (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
4.5V, 10V	90mOhm @ 2.7A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
2.1V @ 250µA	2 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	300 pF @ 10 V
FET Feature:	Power Dissipation (Max):
-	800mW (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
SOT-23-3	TO-236-3, SC-59, SOT-23-3
Base Product Number:	
DMP3165	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.21.0095	

Lead-free Green

DMP3165L

P-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D T_A = +25°C
-30V	90mΩ @ V_{GS} = -10V	-3.3A
	134mΩ @ V_{GS} = -4.5V	-2.5A

Description

This new generation MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high efficiency power management applications.

Applications

- DC-DC Converters
- Power Management Functions

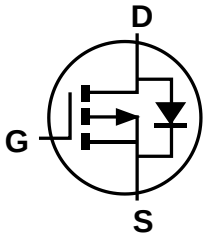
Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

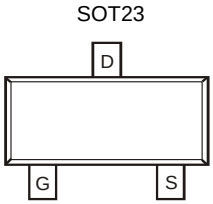
Mechanical Data

- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 e3
- Terminal Connections: See Diagram
- Weight: 0.008 grams (Approximate)

Top View



Internal Schematic



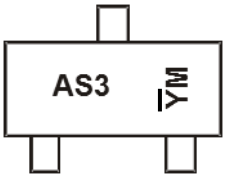
Top View

Ordering Information (Note 4)

Part Number	Case	Packaging
DMP3165L-7	SOT23	3000/Tape & Reel
DMP3165L-13	SOT23	10000/Tape & Reel

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



AS3 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: F = 2018)
M = Month (ex: 9 = September)

Date Code Key

Year	2017	2018	2019	2020	2021	2022	2023	2024	2025
Code	E	F	G	H	I	J	K	L	M

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

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DMP3165L

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	-30	V
Gate-Source Voltage			V_{GSS}	±20	V
Drain Current (Note 6) V_{GS} = -10V	Steady State	T_A = +25°C T_A = +70°C	I_D	-3.3 -2.7	A
Pulsed Drain Current (380µs Pulse, Duty Cycle = 1%)			I_{DM}	-13	A

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		P_D	0.8	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	$R_{\theta JA}$	159	°C/W
Total Power Dissipation (Note 6)		P_D	1.3	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	$R_{\theta JA}$	98	°C/W
Operating and Storage Temperature Range		T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	-30	—	—	V	V_{GS} = 0V, I_D = -250µA
Zero Gate Voltage Drain Current	I_{DSS}	—	—	-800	nA	V_{DS} = -30V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	—	—	±80 ±800	nA	V_{GS} = ±12V, V_{DS} = 0V V_{GS} = ±15V, V_{DS} = 0V
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	-1.3	—	-2.1	V	V_{DS} = V_{GS}, I_D = -250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	59 100	90 134	mΩ	V_{GS} = -10V, I_D = -2.7A V_{GS} = -4.5V, I_D = -2.0A
Diode Forward Voltage	V_{SD}	—	-0.83	-1.26	V	V_{GS} = 0V, I_S = -2.7A
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	300	—	pF	V_{DS} = -10V, V_{GS} = 0V f = 1.0MHz
Output Capacitance	C_{oss}	—	52	—	pF	
Reverse Transfer Capacitance	C_{rss}	—	35	—	pF	
Gate Resistance	R_G	—	12.5	—	Ω	V_{GS} = 0V, V_{DS} = 0V, f = 1.0MHz
Total Gate Charge (V_{GS} = -4.5V)	Q_g	—	1.0	—	nC	V_{GS} = -10V/-4.5V, V_{DS} = -15V, I_D = -3A
Total Gate Charge (V_{GS} = -10V)	Q_g	—	2.0	—	nC	
Gate-Source Charge	Q_{gs}	—	0.2	—	nC	
Gate-Drain Charge	Q_{gd}	—	0.5	—	nC	
Turn-On Delay Time	$t_{D(ON)}$	—	3.7	—	ns	V_{DS} = -15V, V_{GS} = -10V, R_G = 6Ω, I_D = -1A
Turn-On Rise Time	t_R	—	5.5	—	ns	
Turn-Off Delay Time	$t_{D(OFF)}$	—	13.6	—	ns	
Turn-Off Fall Time	t_F	—	8.4	—	ns	
Reverse Recovery Time	t_{RR}	—	6.5	—	ns	I_F = -1.0A, di/dt = 100A/µs
Reverse Recovery Charge	Q_{RR}	—	1.2	—	nC	I_F = -1.0A, di/dt = 100A/µs

Notes:
5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Short duration pulse test used to minimize self-heating effect.
8. Guaranteed by design. Not subject to product testing.

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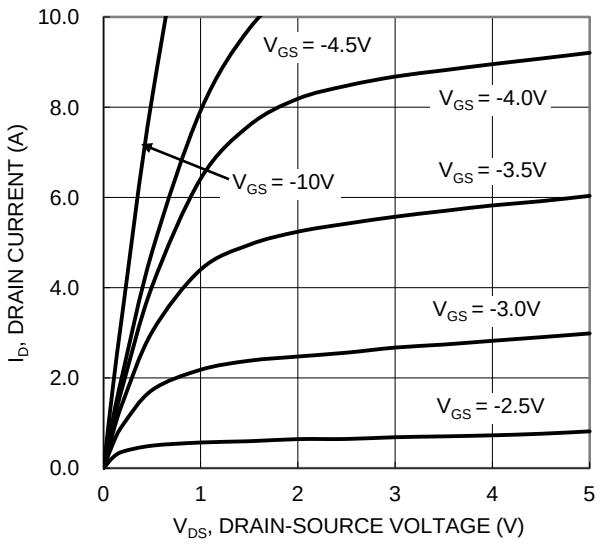

Figure 1.Typical Output Characteristic

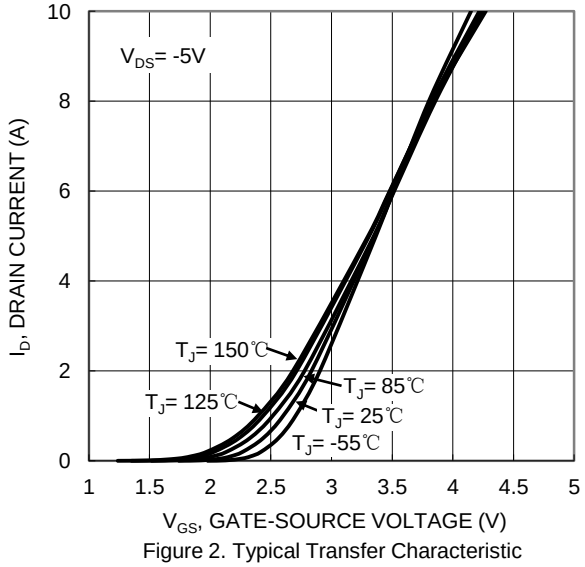

Figure 2. Typical Transfer Characteristic

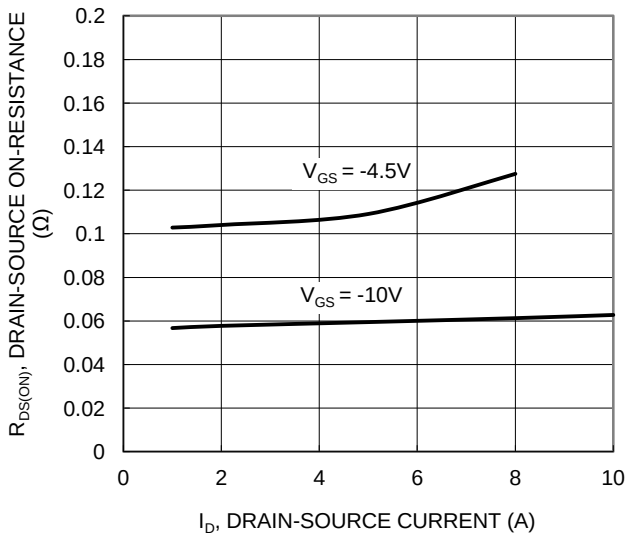

Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

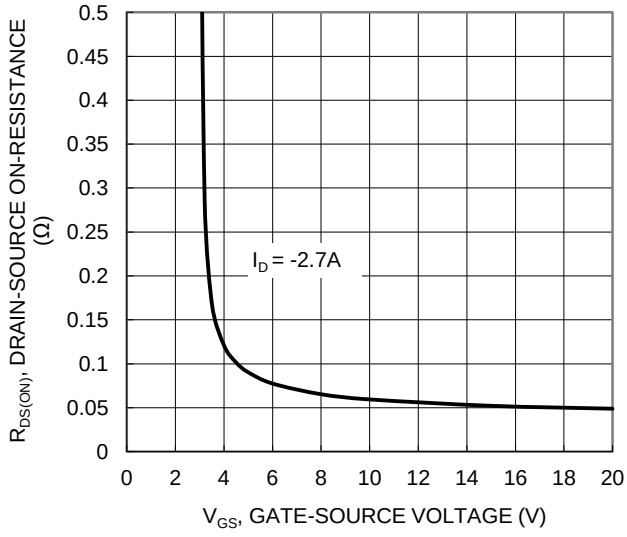

Figure 4. Typical Transfer Characteristic

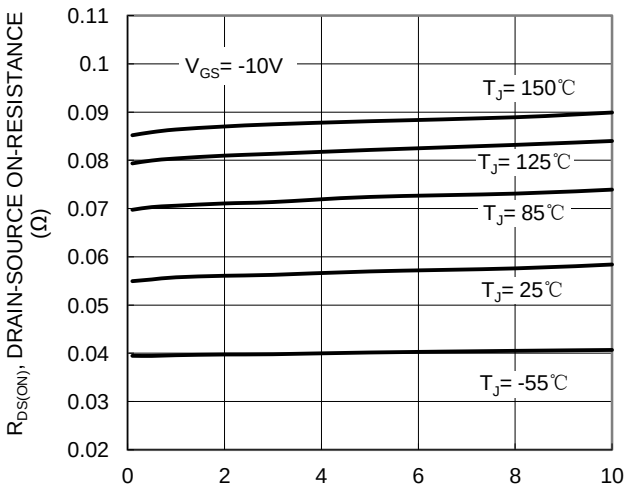

Figure 5. Typical On-Resistance vs. Drain Current and Temperature

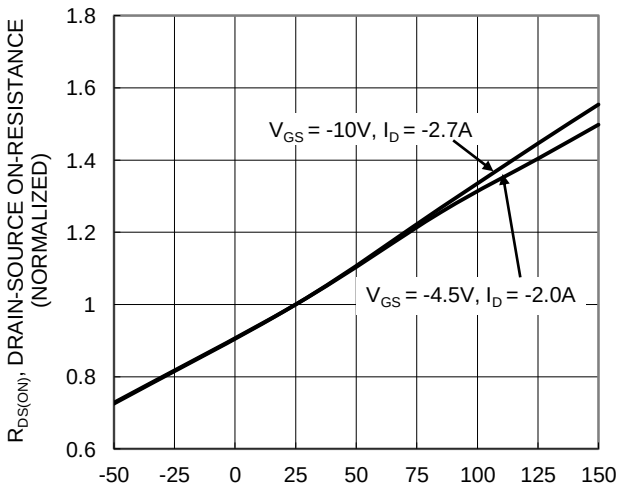

Figure 6. On-Resistance Variation with Temperature

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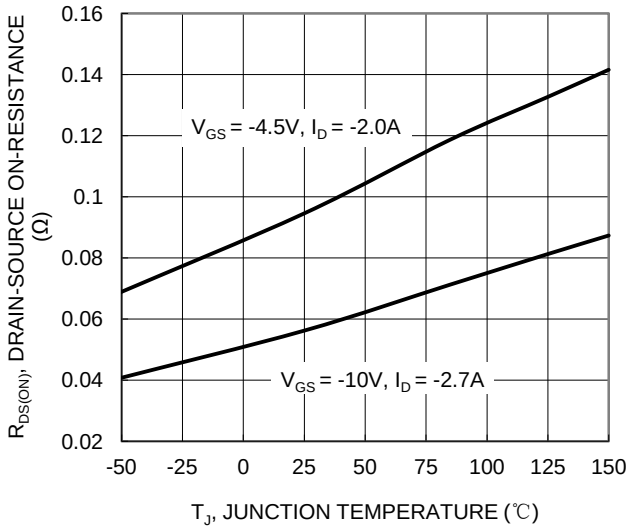

Figure 7. On-Resistance Variation with Temperature

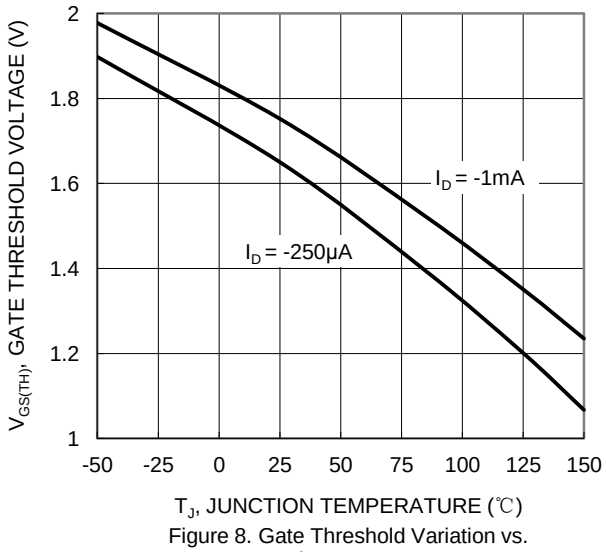

Figure 8. Gate Threshold Variation vs. JunctionTemperature

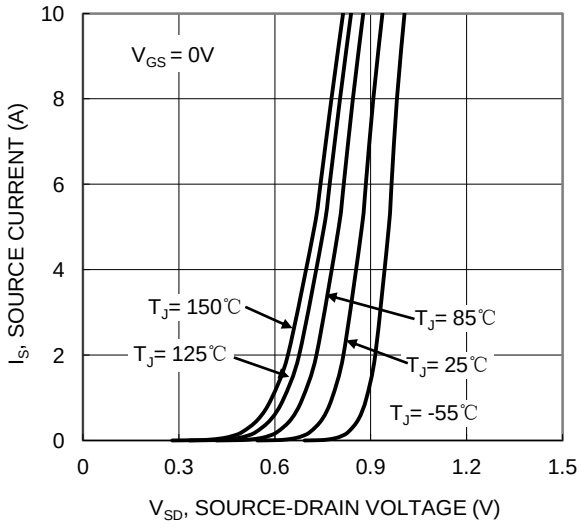

Figure 9. Diode Forward Voltage vs. Current

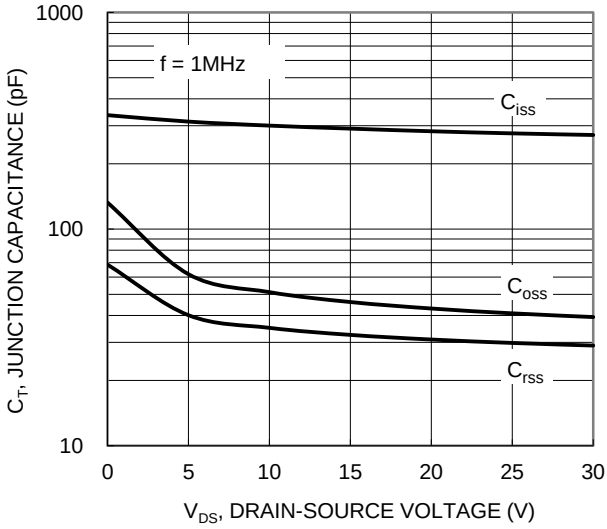

Figure 10. Typical Junction Capacitance

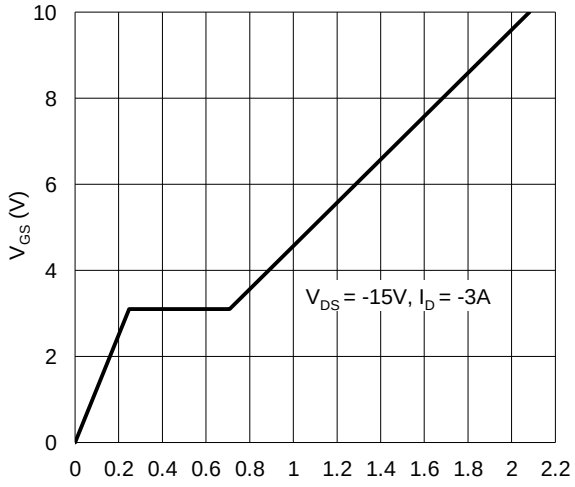

Figure 11. Gate Charge

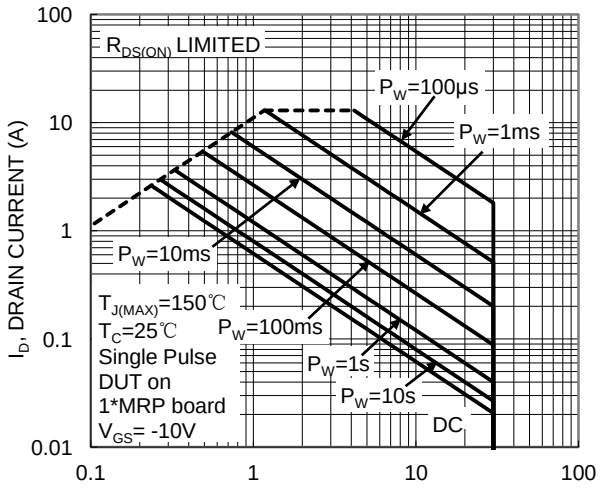

Figure 12. SOA, Safe Operation Area

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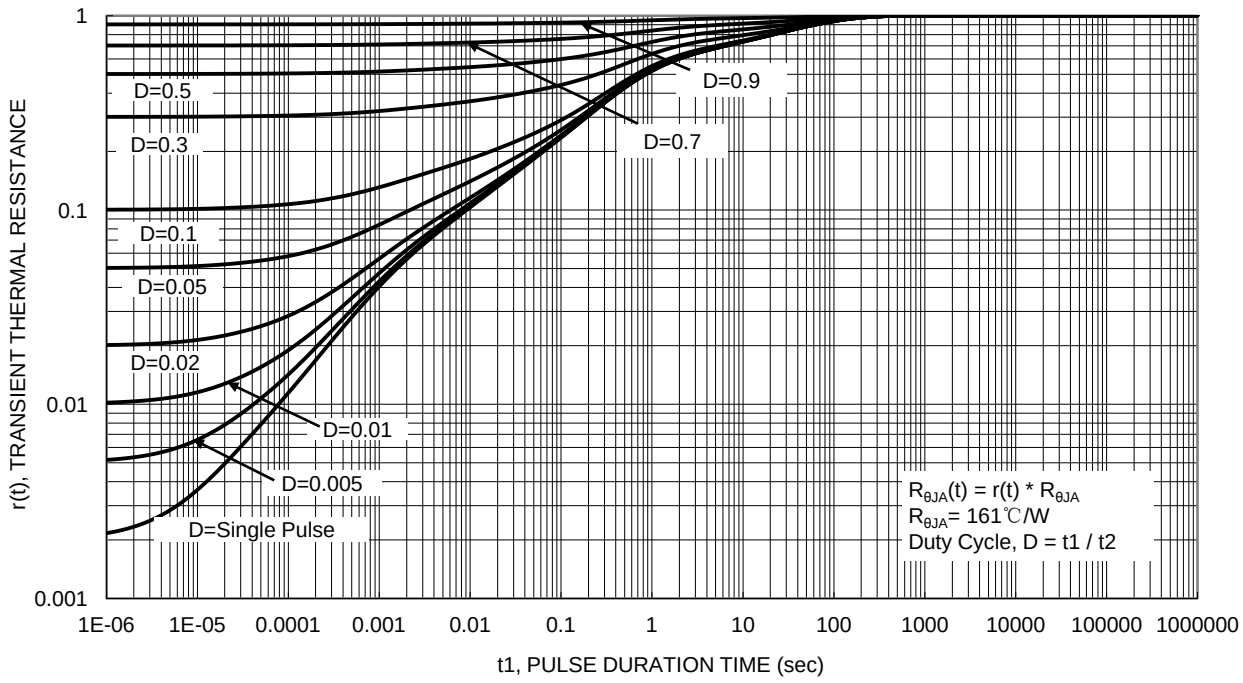

Figure 13. Transient Thermal Resistance

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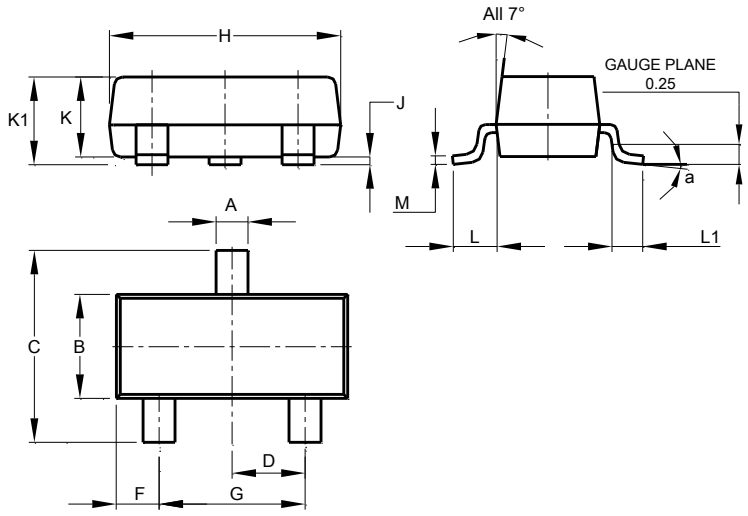
Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23

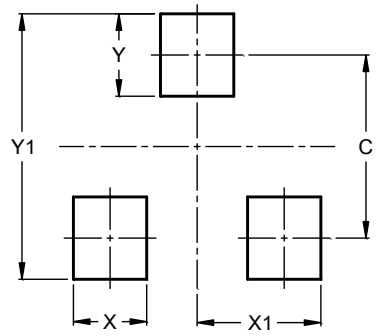


SOT23			
Dim	**Min**	**Max**	**Typ**
A	0.37	0.51	0.40
B	1.20	1.40	1.30
C	2.30	2.50	2.40
D	0.89	1.03	0.915
F	0.45	0.60	0.535
G	1.78	2.05	1.83
H	2.80	3.00	2.90
J	0.013	0.10	0.05
K	0.890	1.00	0.975
K1	0.903	1.10	1.025
L	0.45	0.61	0.55
L1	0.25	0.55	0.40
M	0.085	0.150	0.110
a	0°	8°	--
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT23



Dimensions	Value (in mm)
C	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9

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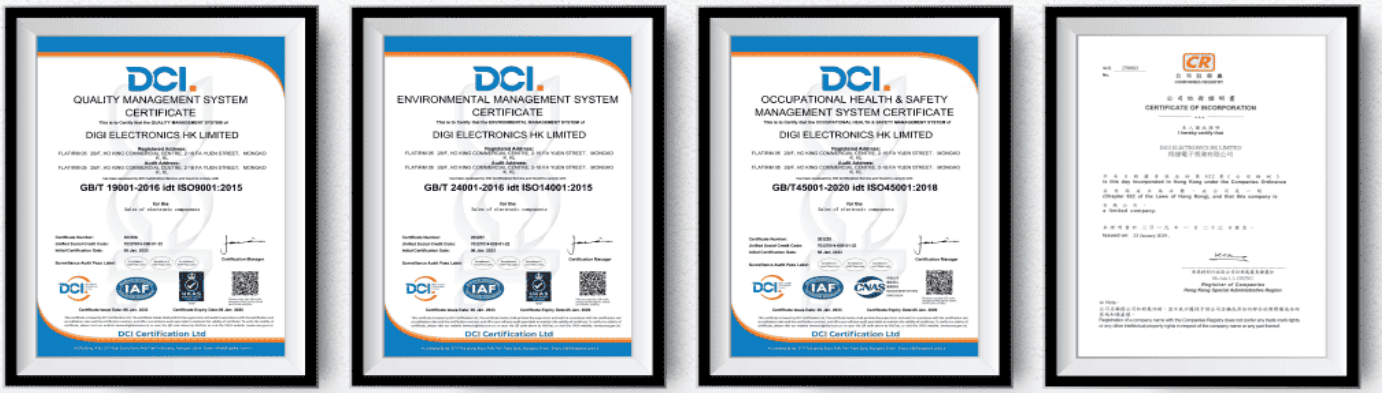

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