

# DMP32D4S-7 Datasheet



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DiGi Electronics Part Number DMP32D4S-7-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number DMP32D4S-7

Description MOSFET P-CH 30V 300MA SOT23

Detailed Description P-Channel 30 V 300mA (Ta) 370mW (Ta) Surface Mo

unt SOT-23-3



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# **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
DMP32D4S-7	Diodes Incorporated
Series:	Product Status:
	Active
FET Type:	Technology:
P-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
30 V	300mA (Ta)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ ld, Vgs:
4.5V, 10V	2.40hm @ 300mA, 10V
Vgs(th) (Max) @ ld:	Gate Charge (Qg) (Max) @ Vgs:
2.4V @ 250μA	1.2 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	51.16 pF @ 15 V
FET Feature:	Power Dissipation (Max):
	370mW (Ta)
Operating Temperature:	Mounting Type:
-55°C ~ 150°C (TJ)	Surface Mount
Supplier Device Package:	Package / Case:
SOT-23-3	TO-236-3, SC-59, SOT-23-3
Base Product Number:	
DMP32	

# **Environmental & Export classification**

8541.21.0095

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	





#### 30V P-CHANNEL ENHANCEMENT MODE MOSFET

#### **Product Summary**

BV <sub>DSS</sub>	R <sub>DS(on)</sub> Max	I <sub>D</sub> Max @T <sub>A</sub> = +25°C
-30V	$2.4\Omega$ @ V <sub>GS</sub> = -10V	-300mA
-307	$4\Omega @ V_{GS} = -4.5V$	-250mA

#### **Description**

This MOSFET has been designed to minimize the on-state resistance (RDS(ON)) yet maintain superior switching performance, making it ideal for high-efficiency power-management applications.

#### **Applications**

- Load switches
- Portable applications
- Power-management functions

#### **Features**

- Low On-Resistance
- ESD Protected Gate
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part.
   A listing can be found at

https://www.diodes.com/products/automotive/automotive-products/.

 This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.

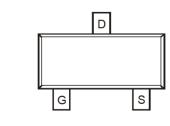
https://www.diodes.com/quality/product-definitions/

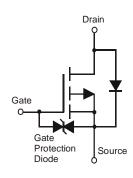
#### **Mechanical Data**

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound.
   UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Lead Free Plating (Matte Tin Finish Annealed over Alloy 42 Leadframe). (§3)
- Weight: 0.006 grams (Approximate)









Top View

Top View Pinout

**Equivalent Circuit** 

#### **Ordering Information** (Note 4)

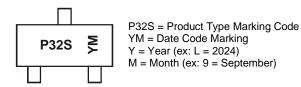
Orderable Part Number	Paakaga	Packing		
Orderable Part Number	Package	Qty.	Carrier	
DMP32D4S-7	SOT23	3,000	Tape & Reel	
DMP32D4S-13	SOT23	10,000	Tape & Reel	

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.



## **Marking Information**



#### Date Code Key

Year	2012		2024	2025	2026	2027	2028	2029	2030	2031	2032	2033
Code	Z		L	М	N	Р	R	S	T	U	V	W
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Character	istic		Symbol	Value	Unit
Drain-Source Voltage			VDSS	-30	V
Gate-Source Voltage			$V_{GSS}$	±20	V
Continuous Drain Current (Note 6)	Vgs = -10V	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	lo	-300 -250	mA
Pulsed Drain Current (Note 6)			I <sub>DM</sub>	-1	А

## Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Total Dawer Dissination	(Note 5)	D-	370	mW	
Total Power Dissipation	(Note 6)	PD	540		
Thermal Decistores, Junction to Ambient	(Note 5)	D	348		
Thermal Resistance, Junction to Ambient	(Note 6)	Reja	241	°C/W	
Thermal Resistance, Junction to Case	(Note 6)	Rejc	91		
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	

Notes:

- 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.
- 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper pad layout.

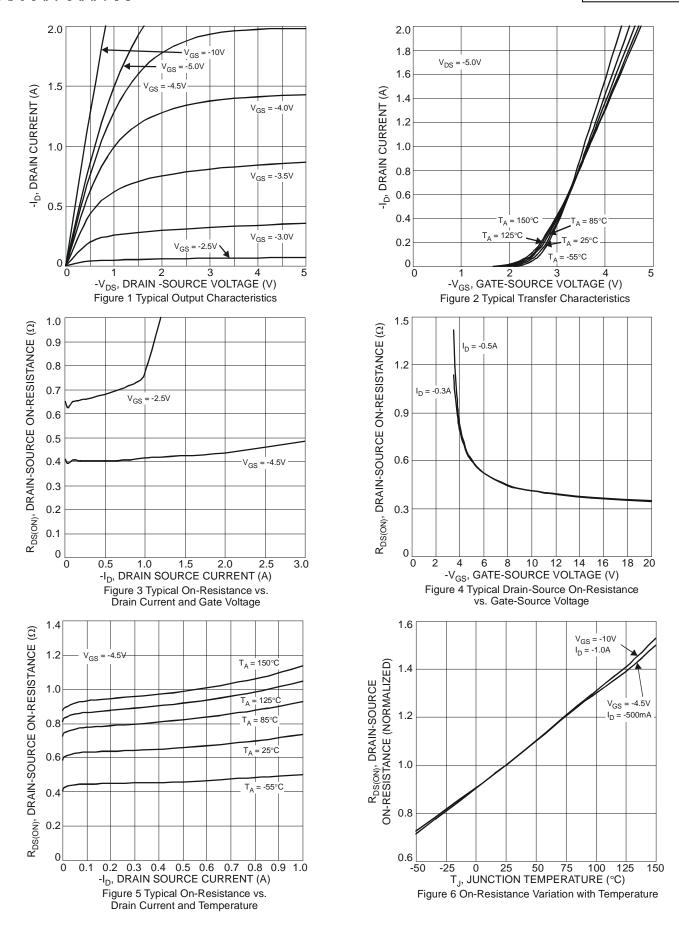


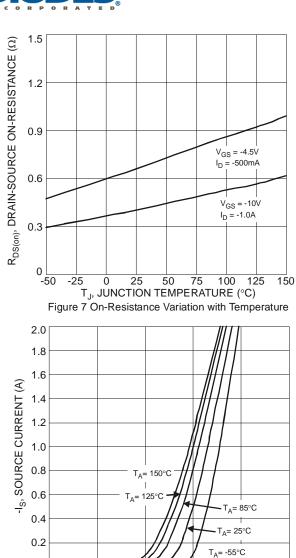
## **Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

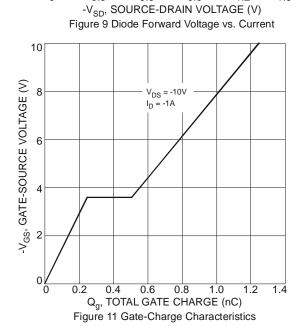
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30	_		V	$V_{GS} = 0V$ , $I_D = -1mA$	
Zero Gate Voltage Drain Current, TJ = +25°C	IDSS	_	_	-1	μΑ	$V_{DS} = -30V$ , $V_{GS} = 0V$	
Gate-Source Leakage	Igss	_	_	±10	μΑ	$V_{GS} = \pm 16V$ , $V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V <sub>GS(TH)</sub>	-1.4 -1.2	_	-2.4 -2.0	V	$V_{DS} = V_{GS}$ , $I_{D} = -250\mu A$ $V_{DS} = -5V$ , $I_{D} = -1\mu A$	
Static Drain-Source On-Resistance	6			2.4	Ω	$V_{GS} = -10V, I_{D} = -0.3A$	
Static Drain-Source On-Resistance	RDS(ON)	_	_	4	12	$V_{GS} = -4.5V, I_{D} = -0.25A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	6	_	S	V <sub>DS</sub> = -10V, I <sub>D</sub> = -400mA	
Diode Forward Voltage	$V_{SD}$	_	-0.8	-1.2	V	$V_{GS} = 0V, I_{S} = -300mA$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	51.16	_	pF	\/ 45\/ \/ 0\/	
Output Capacitance	Coss	_	10.85	_	pF	V <sub>DS</sub> = -15V, V <sub>GS</sub> = 0V, -f = 1.0MHz	
Reverse Transfer Capacitance	Crss	_	8.88	_	pF	I = 1.0WII IZ	
Gate Resistance	Rg	_	275	_	Ω	$V_{DS} = 0V$ , $V_{GS} = 0V$ , $f = 1MHz$	
Total Gate Charge	Qg	_	0.6	_	nC	V <sub>GS</sub> = -4.5V	
Total Gate Charge	Qg	_	1.2	_	nC	$V_{DS} = -10V$ ,	
Gate-Source Charge	Qgs	_	0.2	_	nC	V <sub>GS</sub> = -10V I <sub>D</sub> = -1A	
Gate-Drain Charge	Qgd	_	0.3	_	nC		
Turn-On Delay Time	t <sub>D(on)</sub>	_	9.86	_	ns		
Turn-On Rise Time	tr	_	11.5	_	ns	V <sub>DS</sub> = -15V, I <sub>D</sub> = -1A	
Turn-Off Delay Time	t <sub>D(off)</sub>	_	31.8	_	ns	$V_{GS} = -10V$ , $R_{G} = 6\Omega$	
Turn-Off Fall Time	t <sub>f</sub>	_	21.9	_	ns		

Notes:

<sup>7.</sup> Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to production testing.







0.6

0.9

1.2

1.5

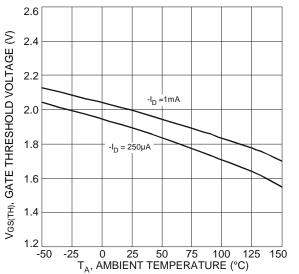
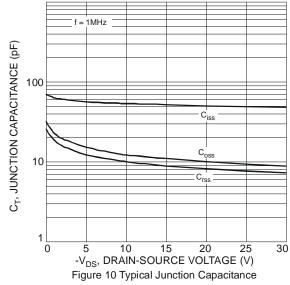
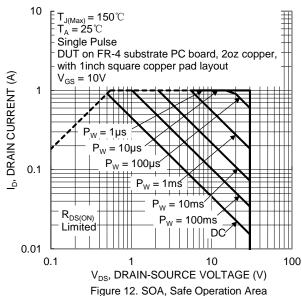


Figure 8 Gate Threshold Variation vs. Ambient Temperature





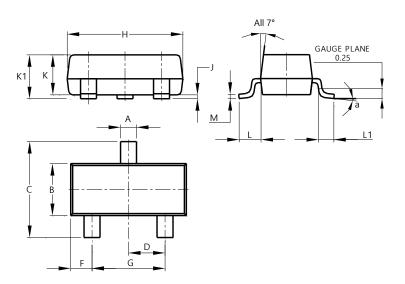
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## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23

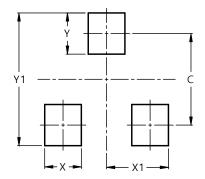


	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
М	0.085	0.150	0.110				
а	0°	8°					
All	Dimens	ions in	mm				

## **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)
С	2.0
X	0.8
X1	1.35
Y	0.9
Y1	2.9



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