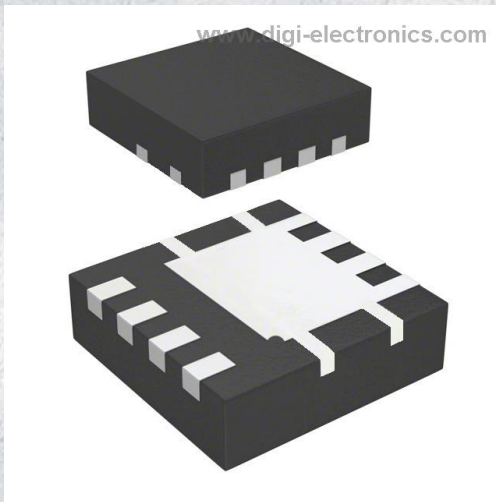


DMTH43M8LFG-7 Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	DMTH43M8LFG-7-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMTH43M8LFG-7
Description	MOSFET N-CH 40V PWRDI3333
Detailed Description	N-Channel 40 V 24A (Ta), 100A (Tc) 2.62W (Ta), 65.2W (Tc) Surface Mount POWERDI3333-8



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

DMTH43M8LFG-7

Series:

-

FET Type:

N-Channel

Drain to Source Voltage (Vdss):

40 V

Drive Voltage (Max Rds On, Min Rds On):

5V, 10V

Vgs(th) (Max) @ Id:

2.5V @ 250 μ A

Vgs (Max):

\pm 20V

FET Feature:

-

Operating Temperature:

-55°C ~ 175°C (Tj)

Supplier Device Package:

PowerDI3333-8

Base Product Number:

DMTH43

Manufacturer:

Diodes Incorporated

Product Status:

Active

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

24A (Ta), 100A (Tc)

Rds On (Max) @ Id, Vgs:

3mOhm @ 20A, 10V

Gate Charge (Qg) (Max) @ Vgs:

40.1 nC @ 10 V

Input Capacitance (Ciss) (Max) @ Vds:

2798 pF @ 20 V

Power Dissipation (Max):

2.62W (Ta), 65.2W (Tc)

Mounting Type:

Surface Mount

Package / Case:

8-PowerVDFN

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.29.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



DMTH43M8LFG

40V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET
PowerDI3333-8

Product Summary

BV_{DSS}	$R_{DS(ON)}$ Max	I_D Max (Note 9) $T_C = +25^\circ C$
40V	3.0m Ω @ $V_{GS} = 10V$	100A
	5.0m Ω @ $V_{GS} = 5V$	93A

Features and Benefits

- Rated to +175°C — Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching, Test in Production — Ensures More Reliable and Robust End Application
- Low $R_{DS(ON)}$ — Ensures On-State Losses are Minimized
- Excellent $Q_{GD} \times R_{DS(ON)}$ Product (FOM)
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- Halogen and Antimony Free. "Green" Device (Note 3)**
- Qualified to AEC-Q101 Standards for High Reliability**
- An Automotive-Compliant Part is Available Under Separate Datasheet ([DMTH43M8LFGQ](#))**

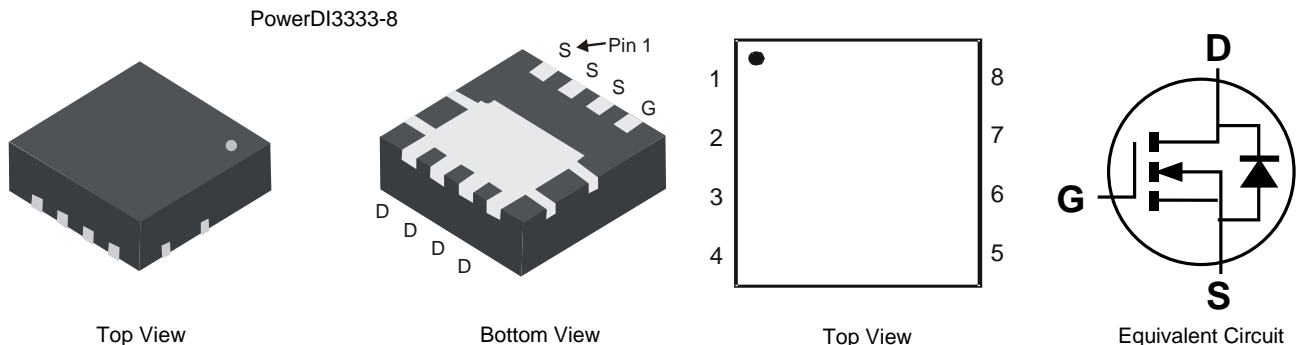
Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

- DC-DC Converters
- Power Management

Mechanical Data

- Case: PowerDI[®]3333-8
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208@3
- Weight: 0.008 grams (Approximate)

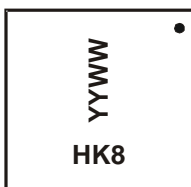


Ordering Information (Note 4)

Part Number	Case	Packaging
DMTH43M8LFG-7	PowerDI3333-8	2000/Tape & Reel
DMTH43M8LFG-13	PowerDI3333-8	3000/Tape & Reel

- Notes:
- EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 - See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 - Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 - For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



HK8 = Product Type Marking Code
 YYWW = Date Code Marking
 YY = Last Two Digits of Year (ex: 19 = 2019)
 WW = Week Code (01 to 53)



DMTH43M8LFG

Maximum Ratings (@ $T_C = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	40	V
Gate-Source Voltage	V_{GSS}	± 20	V
Continuous Drain Current (Notes 6 & 9) $V_{GS} = 10\text{V}$	I_D	$T_C = +25^\circ\text{C}$	100
		$T_C = +100^\circ\text{C}$	85
Continuous Drain Current (Note 5) $V_{GS} = 10\text{V}$	I_D	$T_A = +25^\circ\text{C}$	24.0
		$T_A = +100^\circ\text{C}$	16.9
Pulsed Drain Current (10 μs Pulse, Duty Cycle = 1%)	I_{DM}	400	A
Maximum Continuous Body Diode Forward Current (Note 6)	I_S	3.05	A
Pulsed Body Diode Forward Current (10 μs Pulse, Duty Cycle = 1%)	I_{SM}	400	A
Avalanche Current, L = 1mH	I_{AS}	18.2	A
Avalanche Energy, L = 1mH	E_{AS}	165	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P_D	2.62	W
Thermal Resistance, Junction to Ambient (Note 5)	$R_{\theta JA}$	57.8	$^\circ\text{C/W}$
Total Power Dissipation (Note 6)	P_D	65.2	W
Thermal Resistance, Junction to Case (Note 6)	$R_{\theta JC}$	2.3	$^\circ\text{C/W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +175	$^\circ\text{C}$

Electrical Characteristics (@ $T_J = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 7)						
Drain-Source Breakdown Voltage	BV_{DSS}	40	—	—	V	$V_{GS} = 0\text{V}, I_D = 1\text{mA}$
Zero Gate Voltage Drain Current	I_{DSS}	—	—	1	μA	$V_{DS} = 32\text{V}, V_{GS} = 0\text{V}$
Gate-Source Leakage	I_{GSS}	—	—	± 100	nA	$V_{GS} = \pm 20\text{V}, V_{DS} = 0\text{V}$
ON CHARACTERISTICS (Note 7)						
Gate Threshold Voltage	$V_{GS(TH)}$	1	1.5	2.5	V	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$
Static Drain-Source On-Resistance	$R_{DS(ON)}$	—	2.3	3.0	m Ω	$V_{GS} = 10\text{V}, I_D = 20\text{A}$
		—	3.4	5.0		$V_{GS} = 5\text{V}, I_D = 15\text{A}$
Static Drain-Source On-Resistance ($T_J = +175^\circ\text{C}$) (Note 8)	$R_{DS(ON)}$	—	—	6.0	m Ω	$V_{GS} = 10\text{V}, I_D = 20\text{A}$
		—	—	9.0		$V_{GS} = 5\text{V}, I_D = 15\text{A}$
Diode Forward Voltage	V_{SD}	—	0.8	1.0	V	$V_{GS} = 0\text{V}, I_S = 20\text{A}$
DYNAMIC CHARACTERISTICS (Note 8)						
Input Capacitance	C_{iss}	—	2798	—	pF	$V_{DS} = 20\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Output Capacitance	C_{oss}	—	904	—		
Reverse Transfer Capacitance	C_{rss}	—	88	—		
Gate Resistance	R_G	—	2.44	—	Ω	$V_{DS} = 0\text{V}, V_{GS} = 0\text{V}, f = 1\text{MHz}$
Total Gate Charge ($V_{GS} = 10\text{V}$)	Q_G	—	40.1	—	nC	$V_{DS} = 20\text{V}, I_D = 20\text{A}, V_{GS} = 10\text{V}$
Gate-Source Charge	Q_{GS}	—	5.2	—		
Gate-Drain Charge	Q_{GD}	—	8.8	—		
Turn-On Delay Time	$t_{D(ON)}$	—	5.16	—	ns	$V_{DD} = 20\text{V}, V_{GS} = 10\text{V}, R_G = 1.6\Omega, I_D = 20\text{A}$
Turn-On Rise Time	t_R	—	10.7	—		
Turn-Off Delay Time	$t_{D(OFF)}$	—	24.6	—		
Turn-Off Fall Time	t_F	—	12.4	—		
Body Diode Reverse Recovery Time	t_{RR}	—	32.6	—	ns	$I_F = 15\text{A}, di/dt = 100\text{A}/\mu\text{s}$
Body Diode Reverse Recovery Charge	Q_{RR}	—	26.6	—	nC	

- Notes:
- Device mounted on FR-4 substrate PCB, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.
 - Thermal resistance from junction to soldering point (on the exposed drain pad).
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.
 - Package limit.



DMTH43M8LFG

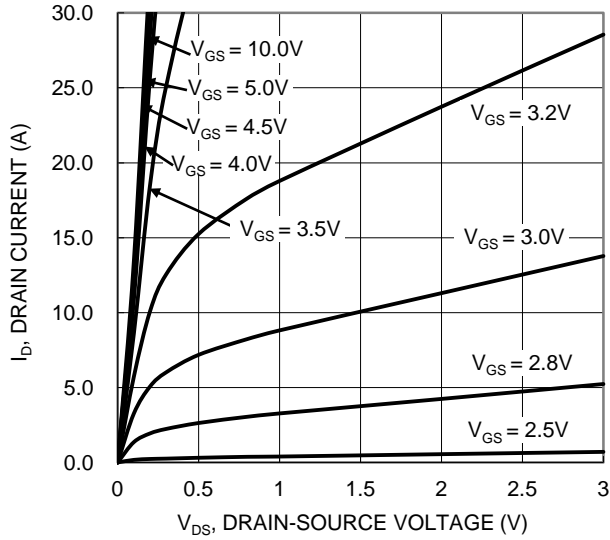


Figure 1. Typical Output Characteristic

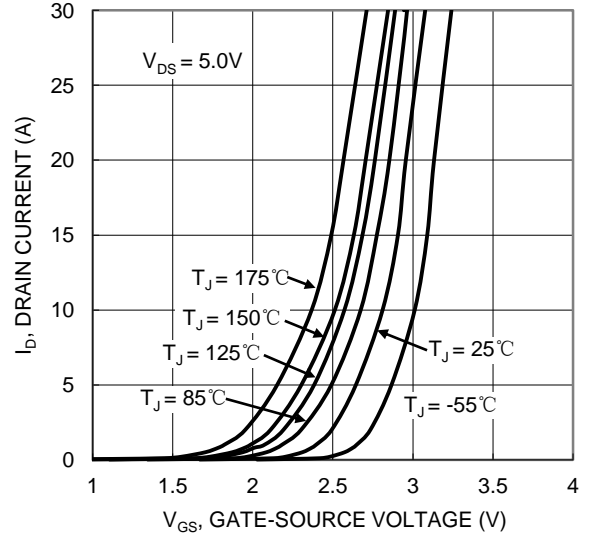


Figure 2. Typical Transfer Characteristic

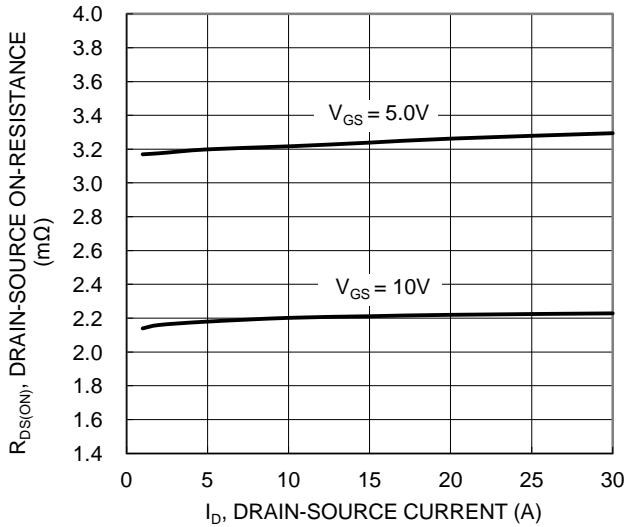


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

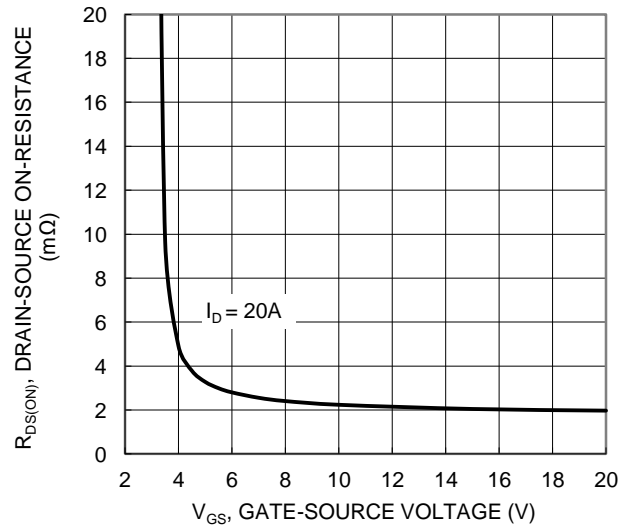


Figure 4. Typical Transfer Characteristic

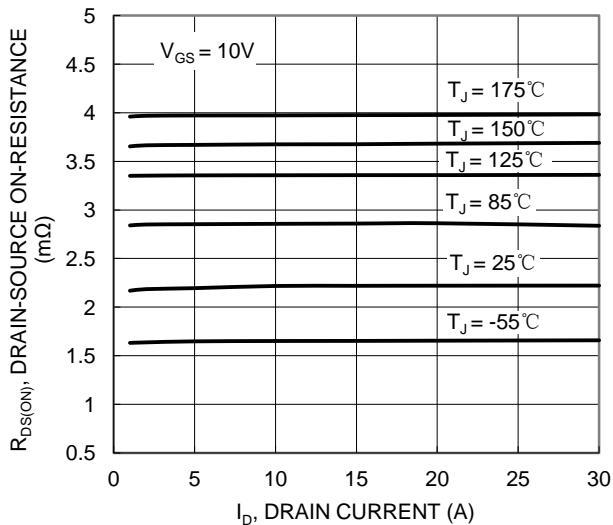


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

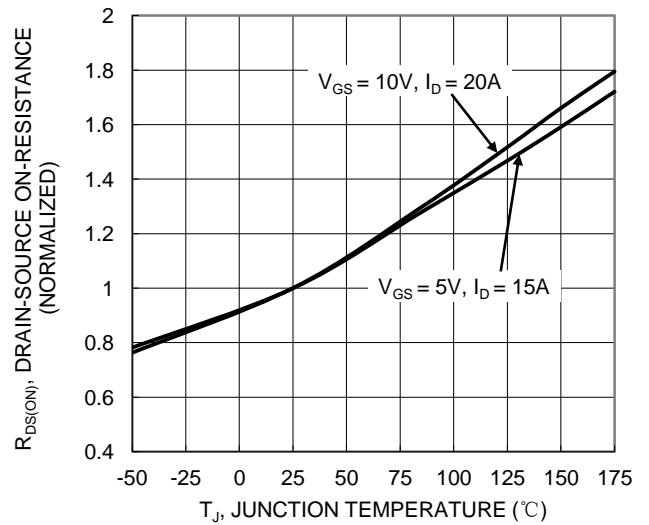


Figure 6. On-Resistance Variation with Temperature



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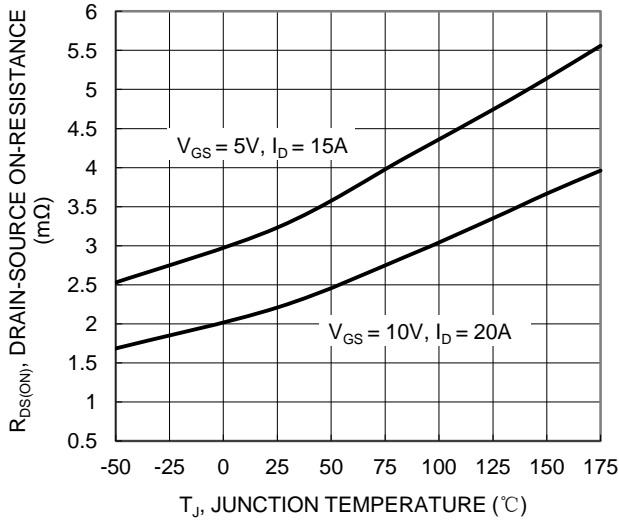


Figure 7. On-Resistance Variation with Temperature

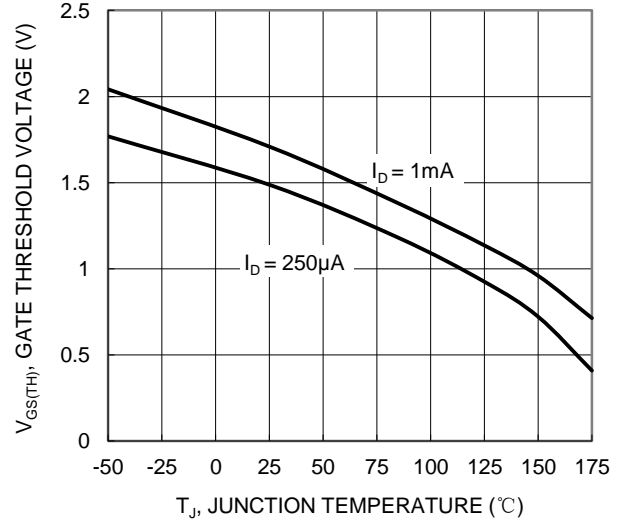


Figure 8. Gate Threshold Variation vs. Junction Temperature

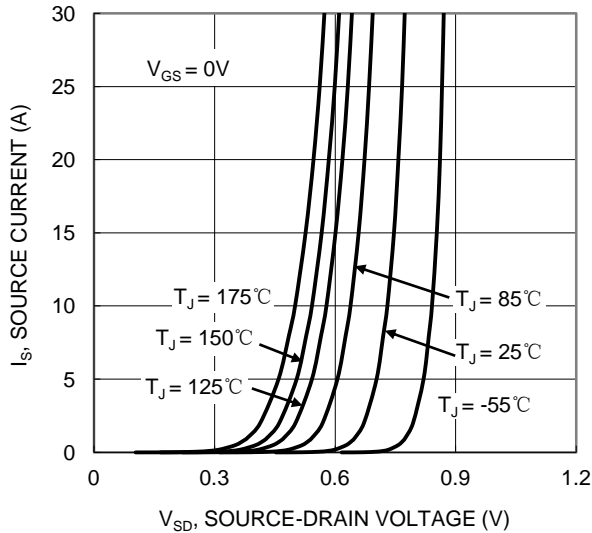


Figure 9. Diode Forward Voltage vs. Current

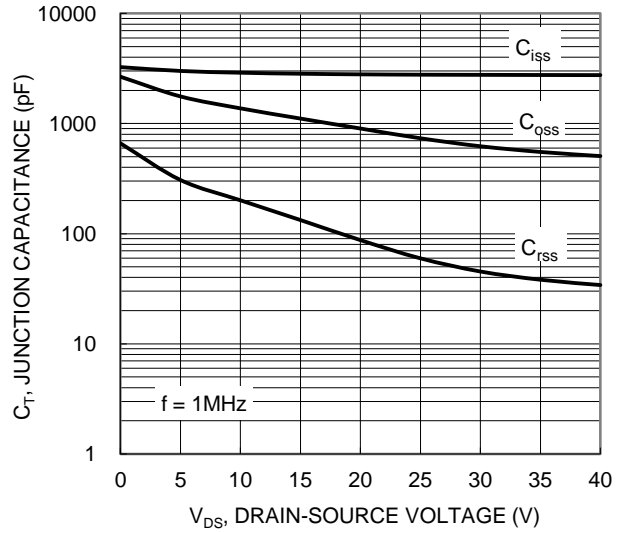


Figure 10. Typical Junction Capacitance

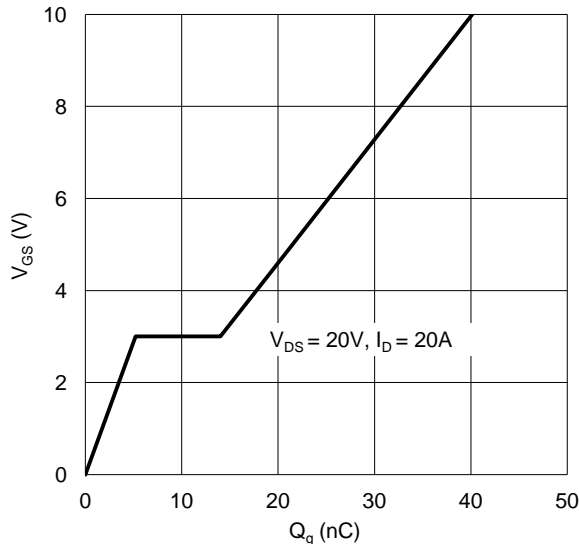


Figure 11. Gate Charge

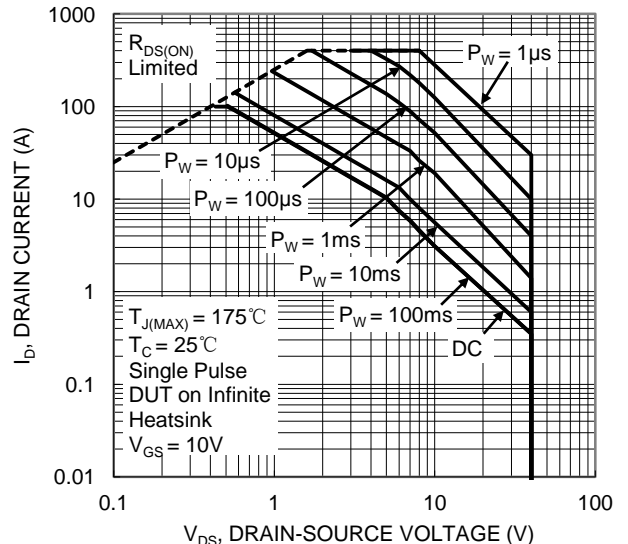


Figure 12. SOA, Safe Operation Area



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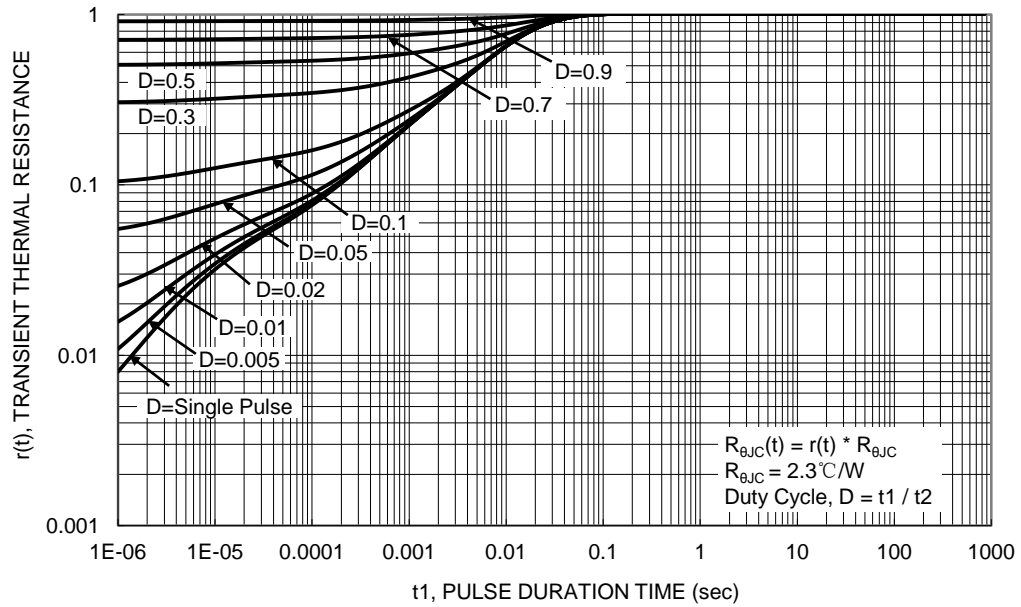
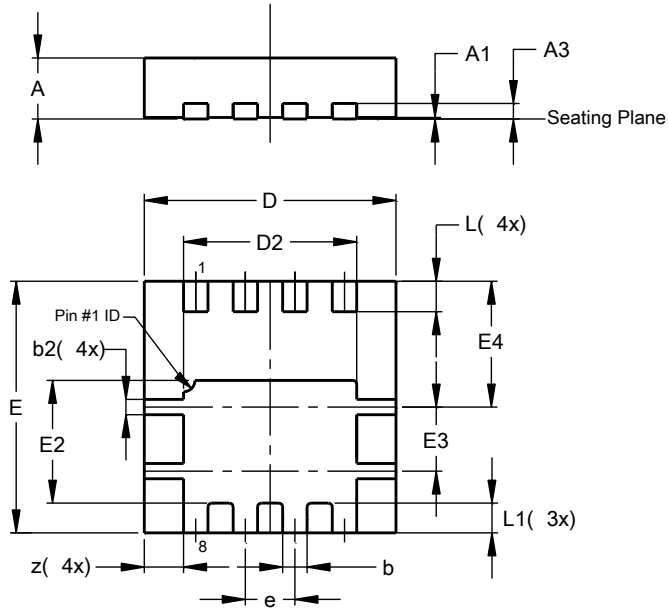


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8

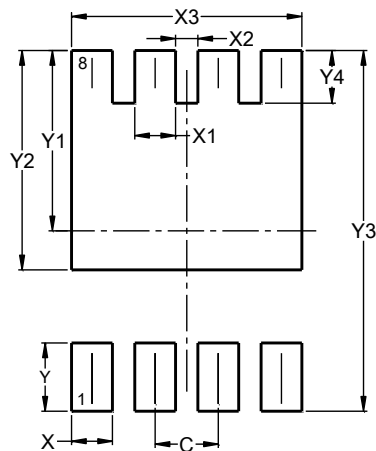


PowerDI3333-8			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	0.02
A3	-	-	0.203
b	0.27	0.37	0.32
b2	0.15	0.25	0.20
D	3.25	3.35	3.30
D2	2.22	2.32	2.27
E	3.25	3.35	3.30
E2	1.56	1.66	1.61
E3	0.79	0.89	0.84
E4	1.60	1.70	1.65
e	-	-	0.65
L	0.35	0.45	0.40
L1	-	-	0.39
z	-	-	0.515
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

PowerDI3333-8



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700
Y4	0.540



DMTH43M8LFG

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