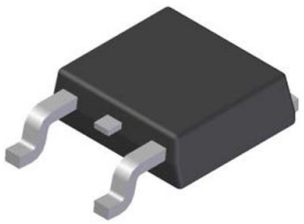
DMTH6010SK3Q-13 Datasheet



DiGi Electronics Part Number	DMTH6010SK3Q-13-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMTH6010SK3Q-13
Description	MOSFET N-CH 60V 16.3A/70A TO252
Detailed Description	N-Channel 60 V 16.3A (Ta), 70A (Tc) 3.1W (Ta) Surface Mount TO-252-3

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
DMTH6010SK3Q-13	Diodes Incorporated
Series:	Product Status:
-	Active
FET Type:	Technology:
N-Channel	MOSFET (Metal Oxide)
Drain to Source Voltage (Vdss):	Current - Continuous Drain (Id) @ 25°C:
60 V	16.3A (Ta), 70A (Tc)
Drive Voltage (Max Rds On, Min Rds On):	Rds On (Max) @ Id, Vgs:
10V	8mOhm @ 20A, 10V
Vgs(th) (Max) @ Id:	Gate Charge (Qg) (Max) @ Vgs:
4V @ 250µA	38.1 nC @ 10 V
Vgs (Max):	Input Capacitance (Ciss) (Max) @ Vds:
±20V	2841 pF @ 30 V
FET Feature:	Power Dissipation (Max):
-	3.1W (Ta)
Operating Temperature:	Grade:
-55°C ~ 175°C (TJ)	Automotive
Qualification:	Mounting Type:
AEC-Q101	Surface Mount
Supplier Device Package:	Package / Case:
TO-252-3	TO-252-3, DPAK (2 Leads + Tab), SC-63
Base Product Number:	
DMTH6010	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8541.29.0095	

DMTH6010SK3Q

60V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET

Product Summary

BV_{DSS}	$R_{DS(ON)}$ max	I_D max T_C = +25°C
60V	8mΩ @ V_{GS} = 10V	70A

Features

- Rated to +175°C – Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production – Ensures More Reliable and Robust End Application
- Low $R_{DS(ON)}$ – Ensures On State Losses Are Minimized
- Excellent Q_{gd} x $R_{DS\ (ON)}$ Product (FOM)
- Advanced Technology for DC/DC Converters
- Small Form Factor Thermally Efficient Package Enables Higher Density End Products
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**
- **PPAP Capable (Note 4)**

Description and Applications

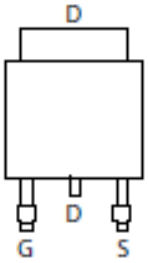
This MOSFET is designed to meet the stringent requirements of automotive applications. It is qualified to AEC-Q101, supported by a PPAP and is ideal for use in:

- Power Management Functions
- DC-DC Converters
- Backlighting

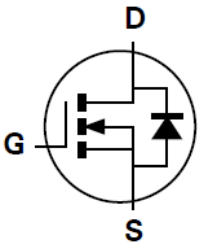
Mechanical Data

- Case: TO252
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Finish - Matte Tin Annealed over Copper Leadframe; Solderable per MIL-STD-202, Method 208 e3
- Weight: 0.33 grams (Approximate)

Top View



Pin Out Top View



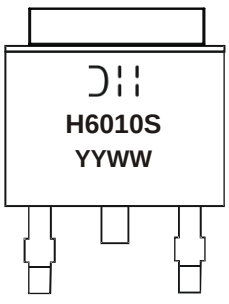
Equivalent Circuit

Ordering Information (Note 5)

Part Number	Case	Packaging
DMTH6010SK3Q-13	TO252 (DPAK)	2,500/Tape & Reel

Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. Automotive products are AEC-Q101 qualified and are PPAP capable. Refer to https://www.diodes.com/quality/.
5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information



=Manufacturer's Marking
H6010S = Product Type Marking Code
YYWW = Date Code Marking
YY = Last Two Digits of Year (ex: 18 = 2018)
WW = Week Code (01 to 53)

DIODES INCORPORATED

DMTH6010SK3Q

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Drain-Source Voltage		V_{DSS}	60	V
Gate-Source Voltage		V_{GSS}	±20	V
Continuous Drain Current (Note 6)	T_A = +25°C T_A = +70°C	I_D	16.3 13.6	A
Continuous Drain Current (Note 7)	T_C = +25°C T_C = +100°C	I_D	70 49	A
Maximum Continuous Body Diode Forward Current (Note 6)		I_S	3	A
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)		I_{DM}	280	A
Pulsed Body Diode Forward Current (10µs Pulse, Duty Cycle = 1%)		I_{SM}	280	A
Avalanche Current, L=0.1mH		I_{AS}	20	A
Avalanche Energy, L=0.1mH		E_{AS}	27.7	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 6)	P_D	3.1	W
Thermal Resistance, Junction to Ambient (Note 6)	$R_{\theta JA}$	47	°C/W
Total Power Dissipation (Note 7)	P_D	59	W
Thermal Resistance, Junction to Case (Note 7)	$R_{\theta JC}$	2.5	°C/W
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +175	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV_{DSS}	60	-	-	V	V_{GS} = 0V, I_D = 1mA
Zero Gate Voltage Drain Current	I_{DSS}	-	-	1	µA	V_{DS} = 48V, V_{GS} = 0V
Gate-Source Leakage	I_{GSS}	-	-	±100	nA	V_{GS} = ±20V, V_{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	$V_{GS(TH)}$	2	-	4	V	V_{DS} = V_{GS}, I_D = 250µA
Static Drain-Source On-Resistance	$R_{DS(ON)}$	-	5.4	8	mΩ	V_{GS} = 10V, I_D = 20A
Diode Forward Voltage	V_{SD}	-	0.84	1.2	V	V_{GS} = 0V, I_S = 20A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C_{iss}	-	2841	-	pF	V_{DS} = 30V, V_{GS} = 0V, f = 1MHz
Output Capacitance	C_{oss}	-	690	-		
Reverse Transfer Capacitance	C_{rss}	-	46	-		
Gate Resistance	R_g	-	0.55	-	Ω	V_{DS} = 0V, V_{GS} = 0V, f = 1MHz
Total Gate Charge	Q_g	-	38.1	-	nC	V_{DS} = 30V, I_D = 20A, V_{GS} = 10V
Gate-Source Charge	Q_{gs}	-	8.3	-		
Gate-Drain Charge	Q_{gd}	-	9.3	-		
Turn-On Delay Time	$t_{D(ON)}$	-	8.6	-	ns	V_{DD} = 30V, V_{GS} = 10V, I_D = 20A, R_G = 3Ω
Turn-On Rise Time	t_R	-	8.2	-		
Turn-Off Delay Time	$t_{D(OFF)}$	-	17.4	-		
Turn-Off Fall Time	t_F	-	5.7	-		
Body Diode Reverse Recovery Time	t_{RR}	-	33.8	-	ns	I_F = 20A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q_{RR}	-	35.6	-	nC	

Notes: 6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
7. Device mounted on infinite heat sink and measured by thermal couple attached on bottom heat sink of package.
8. Short duration pulse test used to minimize self-heating effect.
9. Guaranteed by design. Not subject to product testing.

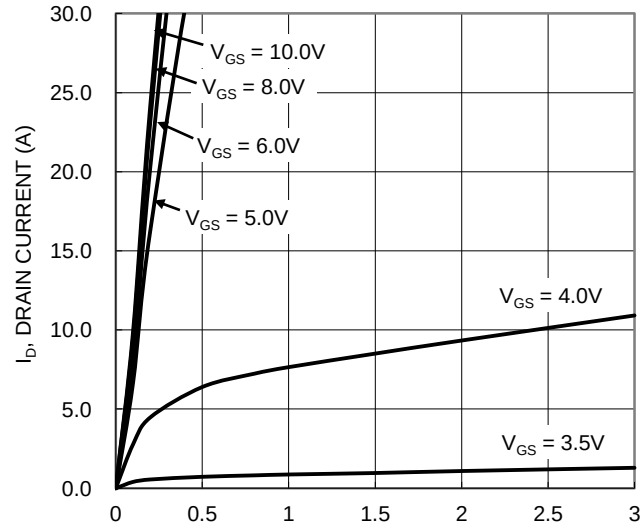


Figure 1. Typical Output Characteristic

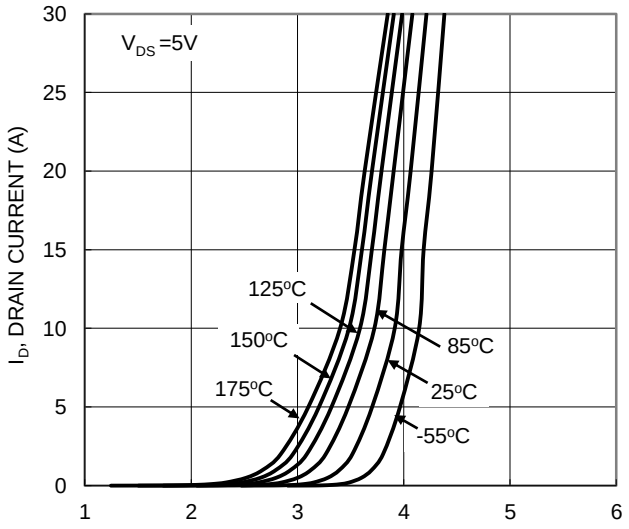


Figure 2. Typical Transfer Characteristic

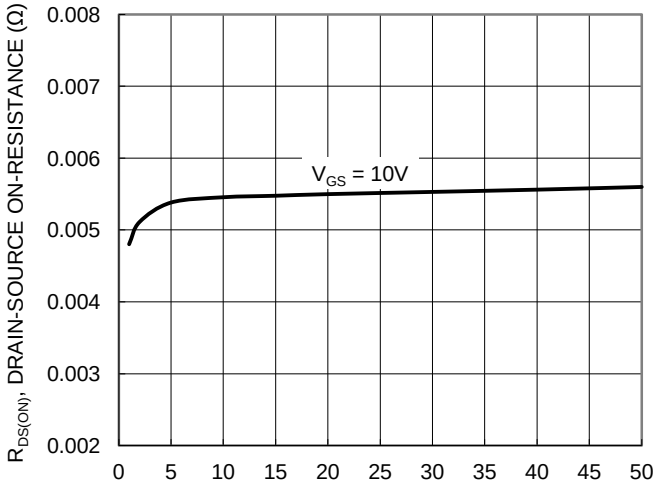


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

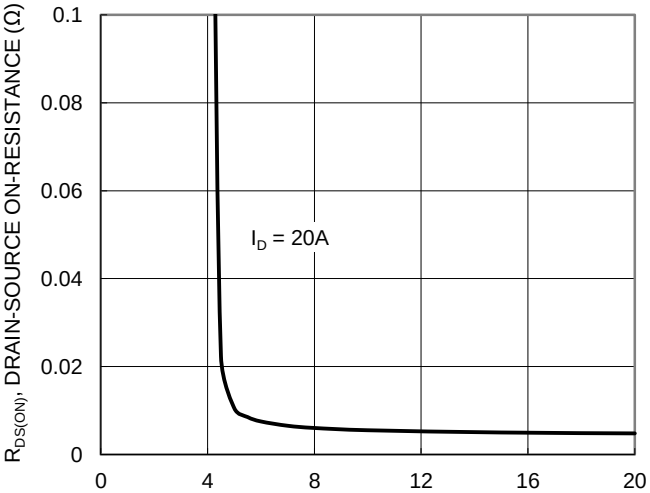


Figure 4. Typical Transfer Characteristic

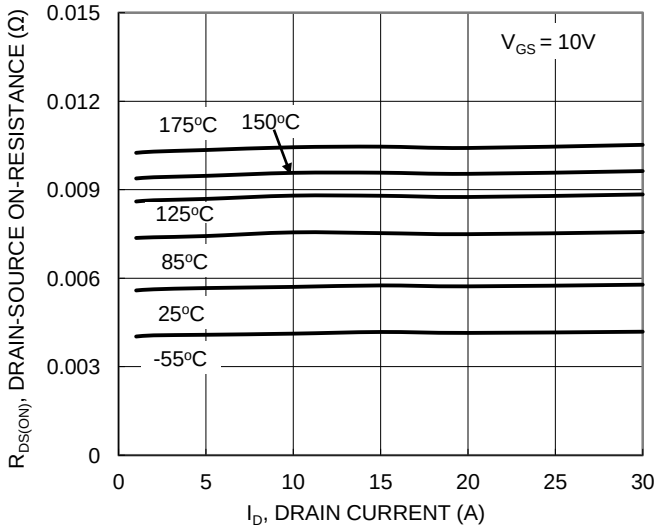


Figure 5. Typical On-Resistance vs. Drain Current and Junction Temperature

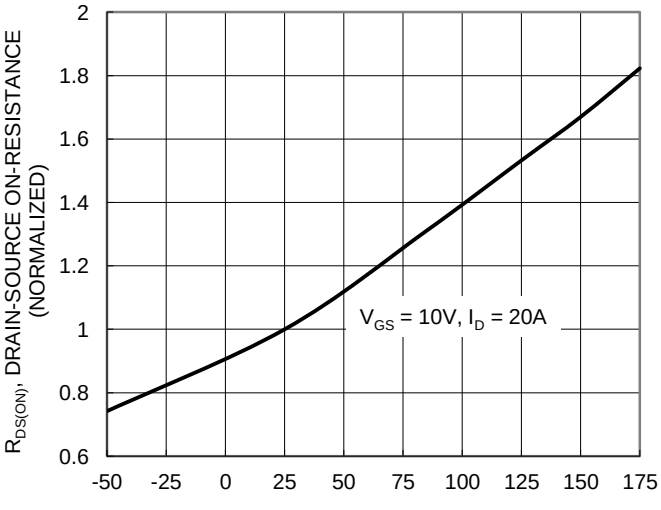


Figure 6. On-Resistance Variation with Junction Temperature

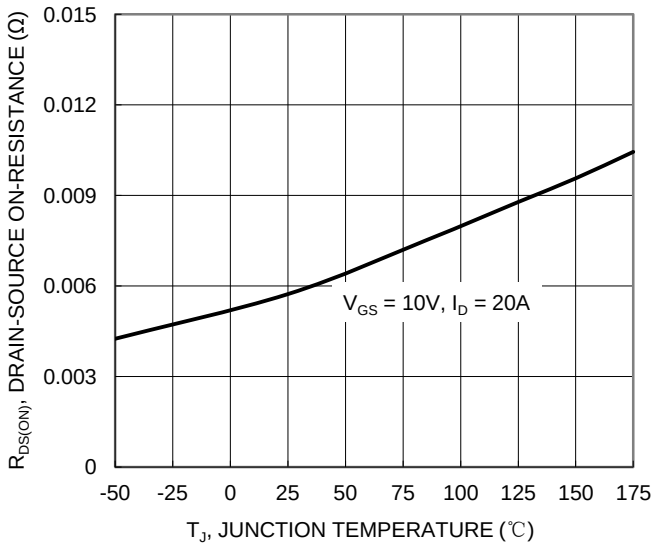


Figure 7. On-Resistance Variation vs. Junction Temperature

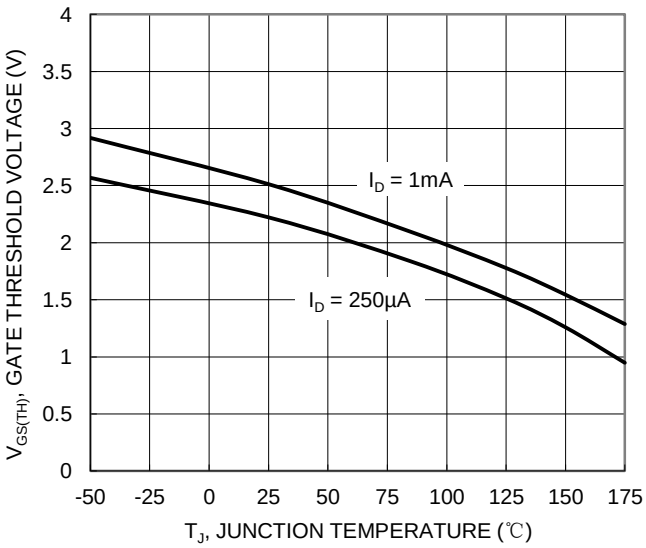


Figure 8. Gate Threshold Variation vs. Junction Temperature

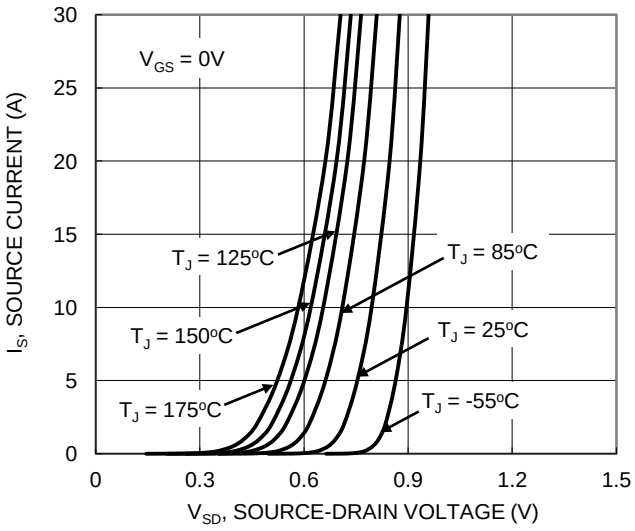


Figure 9. Diode Forward Voltage vs. Current

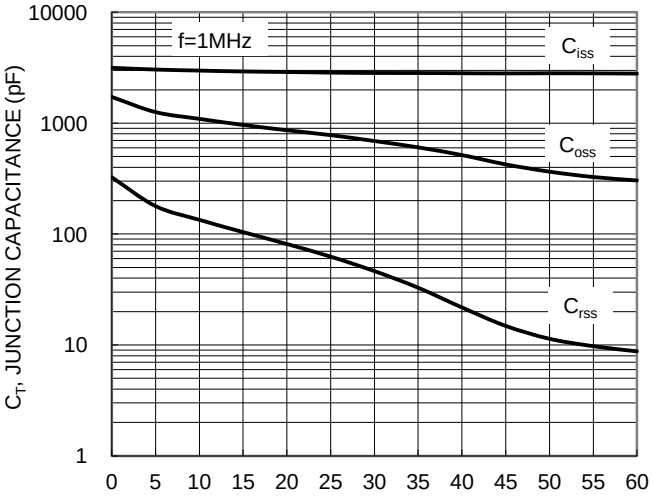


Figure 10. Typical Junction Capacitance

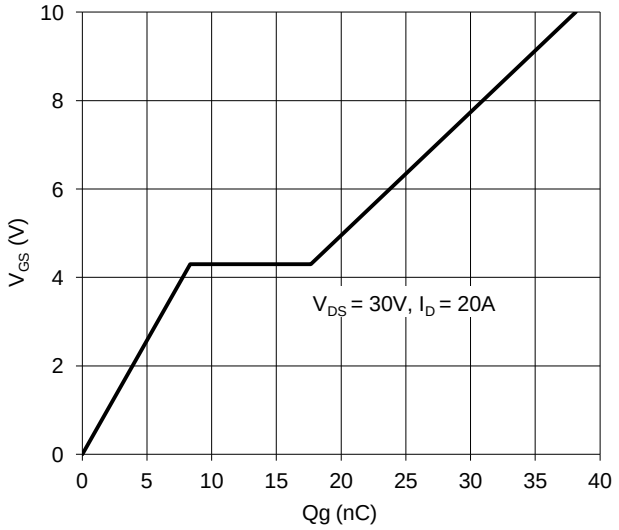


Figure 11. Gate Charge

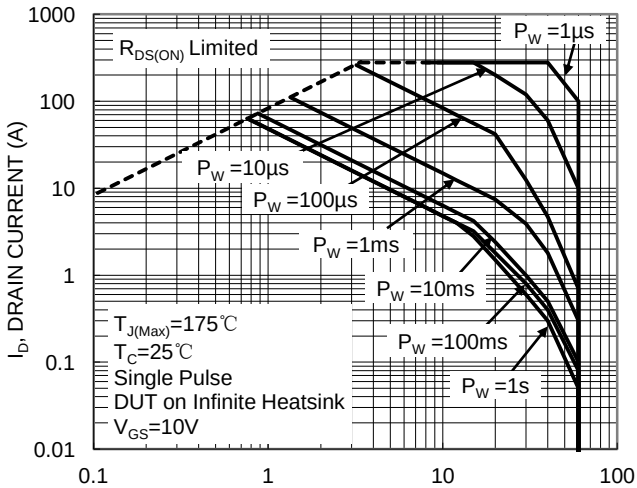


Figure 12. SOA, Safe Operation Area

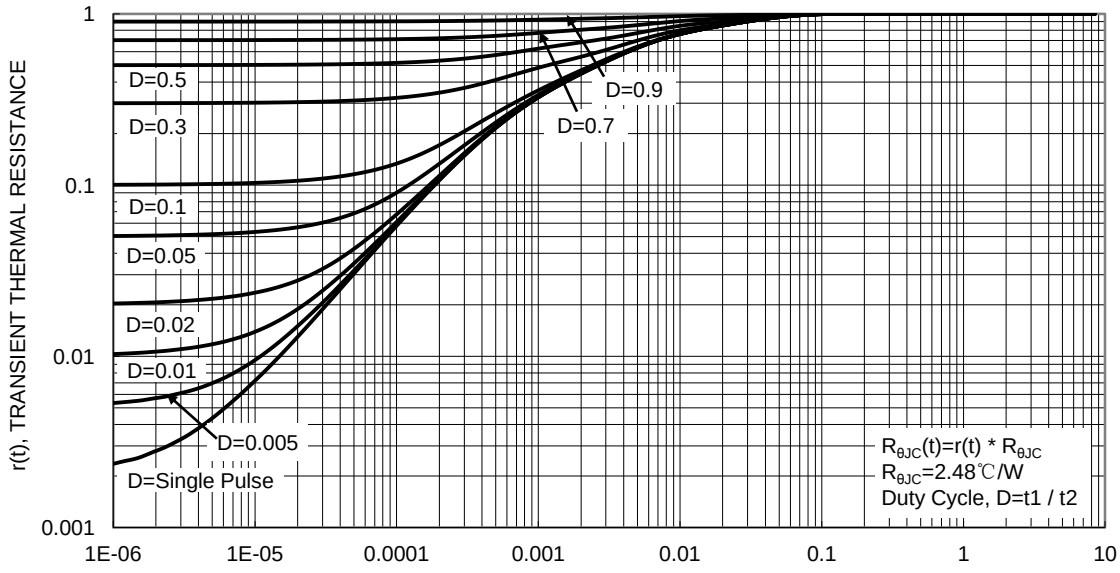
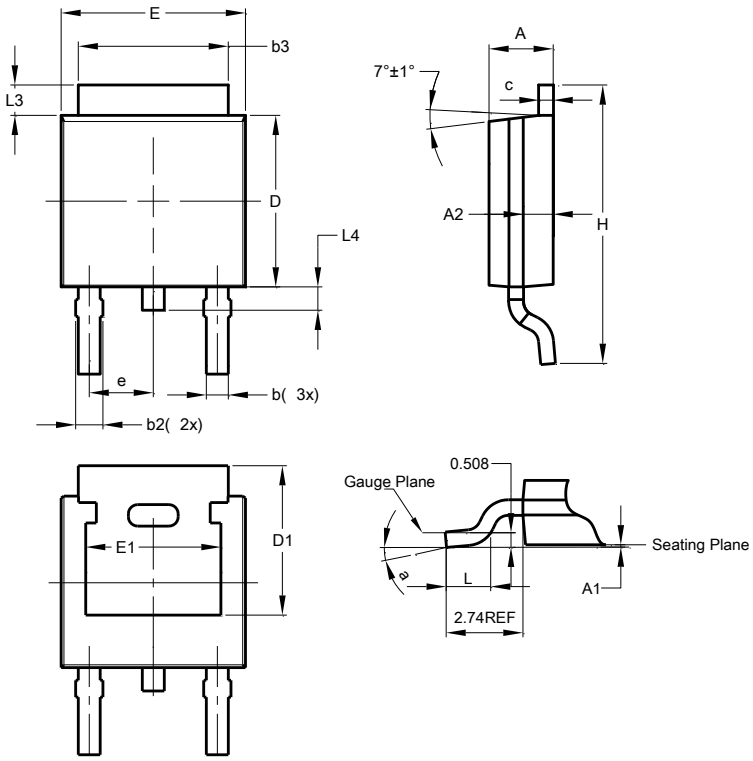


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

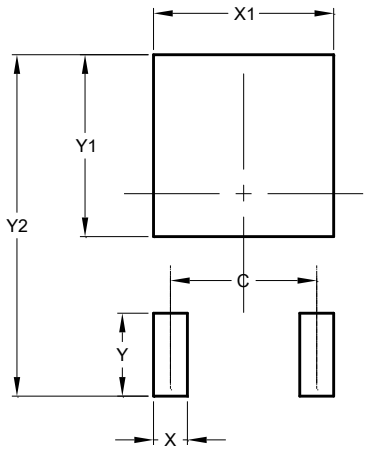
Please see http://www.diodes.com/package-outlines.html for the latest version.



TO252 (DPAK)			
Dim	**Min**	**Max**	**Typ**
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.

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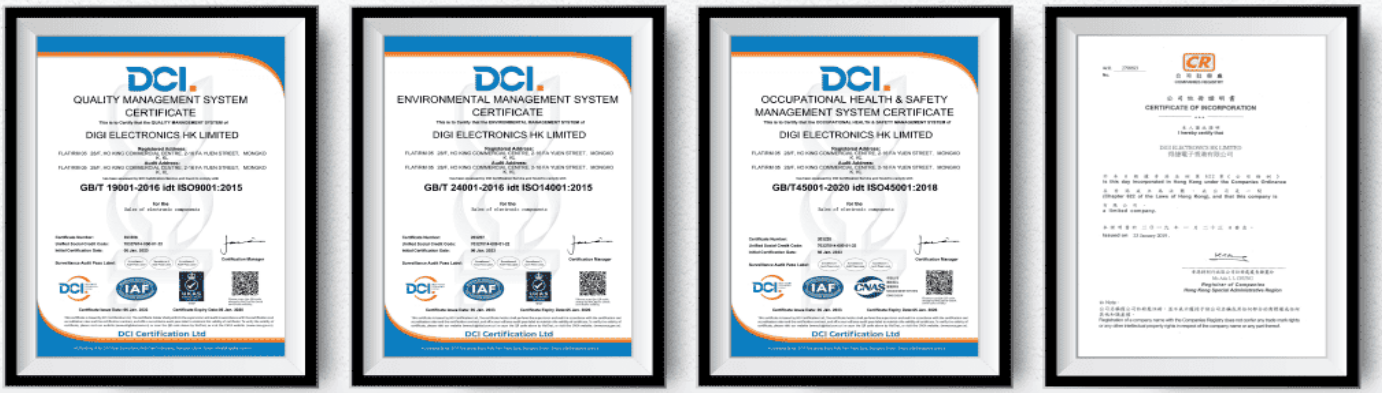

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