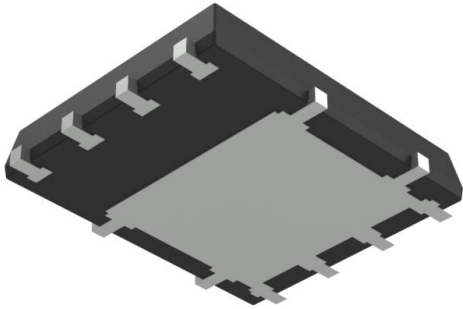


DMTH8008SPS-13 Datasheet

www.digi-electronics.com



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	DMTH8008SPS-13-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DMTH8008SPS-13
Description	MOSFET N-CH 80V 92A PWRDI5060-8
Detailed Description	N-Channel 80 V 92A (Tc) 1.6W (Ta), 100W (Tc) Surface Mount PowerDI5060-8



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

DMTH8008SPS-13

Series:

-

FET Type:

N-Channel

Drain to Source Voltage (Vdss):

80 V

Drive Voltage (Max Rds On, Min Rds On):

6V, 10V

Vgs(th) (Max) @ Id:

4V @ 1mA

Vgs (Max):

±20V

FET Feature:

-

Operating Temperature:

-55°C ~ 175°C (Tj)

Supplier Device Package:

PowerDI5060-8

Base Product Number:

DMTH8008

Manufacturer:

Diodes Incorporated

Product Status:

Active

Technology:

MOSFET (Metal Oxide)

Current - Continuous Drain (Id) @ 25°C:

92A (Tc)

Rds On (Max) @ Id, Vgs:

7.8mOhm @ 14A, 10V

Gate Charge (Qg) (Max) @ Vgs:

34 nC @ 10 V

Input Capacitance (Ciss) (Max) @ Vds:

1950 pF @ 40 V

Power Dissipation (Max):

1.6W (Ta), 100W (Tc)

Mounting Type:

Surface Mount

Package / Case:

8-PowerTDFN

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.29.0095

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



DMTH8008SPS

80V +175°C N-CHANNEL ENHANCEMENT MODE MOSFET
PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)}	I _D T _C = +25°C
80V	7.8mΩ @ V _{GS} = 10V	92A

Description and Applications

This new generation MOSFET is designed to minimize R_{DS(ON)} yet maintain superior switching performance. This device is ideal for use in power management and load switches.

- DC-DC converters
- Load switches

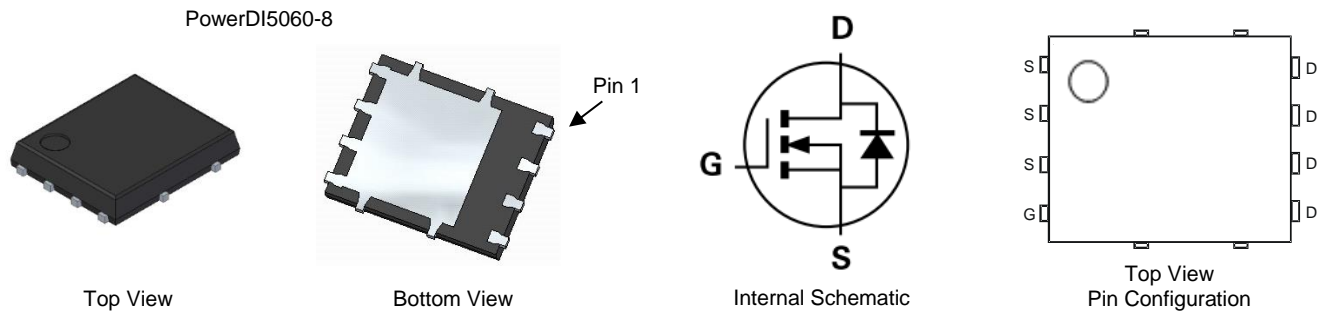
Features

- Rated to +175°C — Ideal for High Ambient Temperature Environments
- 100% Unclamped Inductive Switching (UIS) Test in Production — Ensures More Reliable and Robust End Application
- High-Conversion Efficiency
- Low R_{DS(ON)} — Minimizes On-State Losses
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **An automotive-compliant part is available under separate datasheet ([DMTH8008SPSQ](#))**

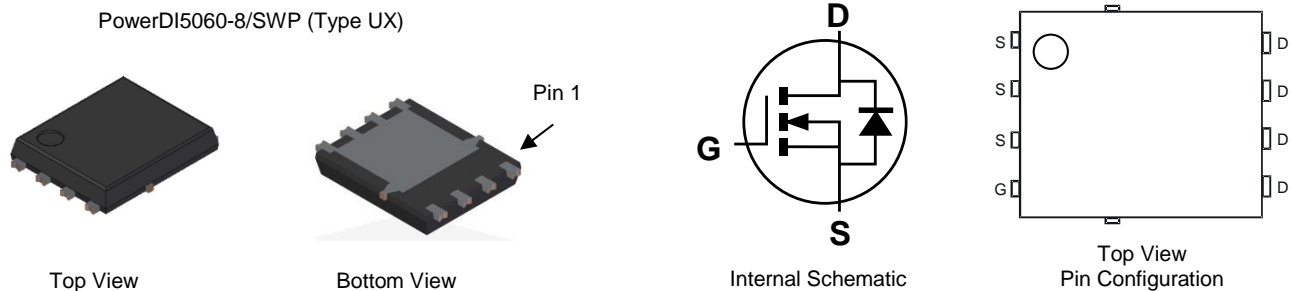
Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections: See Diagram Below
- Terminals: Finish — Matte Tin Annealed over Copper Leadframe. Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.097 grams (Approximate)

Site 1:



Site 2:



Notes:

1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



DMTH8008SPS

Electrical Characteristics (@T_C = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 9)						
Drain-Source Breakdown Voltage	BV _{DSS}	80	—	—	V	V _{GS} = 0V, I _D = 1mA
Zero Gate Voltage Drain Current	I _{DSS}	—	—	1	μA	V _{DS} = 64V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	—	—	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 9)						
Gate Threshold Voltage	V _{GS(TH)}	2	—	4	V	V _{DS} = V _{GS} , I _D = 1mA
Static Drain-Source On-Resistance	R _{DS(ON)}	—	6.5	7.8	mΩ	V _{GS} = 10V, I _D = 14A
		—	7.8	11		V _{GS} = 6V, I _D = 12A
Diode Forward Voltage	V _{SD}	—	0.8	1.2	V	V _{GS} = 0V, I _S = 14A
DYNAMIC CHARACTERISTICS (Note 10)						
Input Capacitance	C _{iss}	—	1950	—	pF	V _{DS} = 40V, V _{GS} = 0V f = 1MHz
Output Capacitance	C _{oss}	—	826	—		
Reverse Transfer Capacitance	C _{rss}	—	56	—	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Gate Resistance	R _g	—	1.7	—		
Total Gate Charge (V _{GS} = 6V)	Q _g	—	23	—	nC	V _{DS} = 40V, I _D = 14A
Total Gate Charge (V _{GS} = 10V)	Q _g	—	34	—		
Gate-Source Charge	Q _{gs}	—	6	—		
Gate-Drain Charge	Q _{gd}	—	12	—		
Turn-On Delay Time	t _{D(ON)}	—	8	—	ns	V _{DD} = 40V, V _{GS} = 10V I _D = 14A, R _g = 6Ω
Turn-On Rise Time	t _R	—	15	—		
Turn-Off Delay Time	t _{D(OFF)}	—	29	—		
Turn-Off Fall Time	t _F	—	21	—		
Body Diode Reverse Recovery Time	t _{RR}	—	43	—	ns	I _S = 14A, dI/dt = 100A/μs
Body Diode Reverse Recovery Charge	Q _{RR}	—	49	—	nC	

Notes: 9. Short duration pulse test used to minimize self-heating effect.
10. Guaranteed by design. Not subject to product testing.



DMTH8008SPS

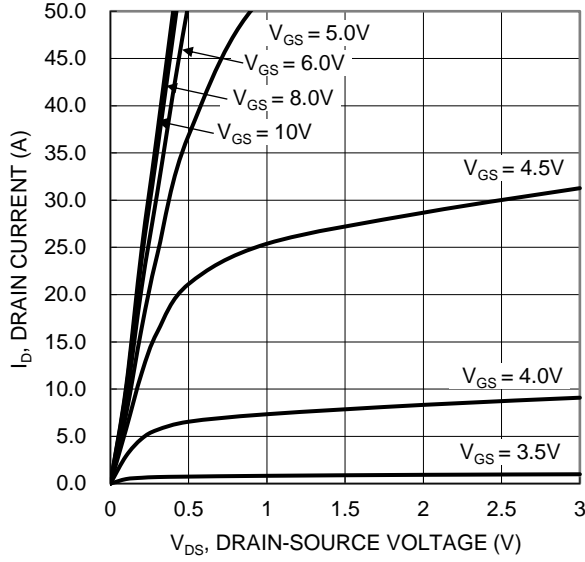


Figure 1. Typical Output Characteristic

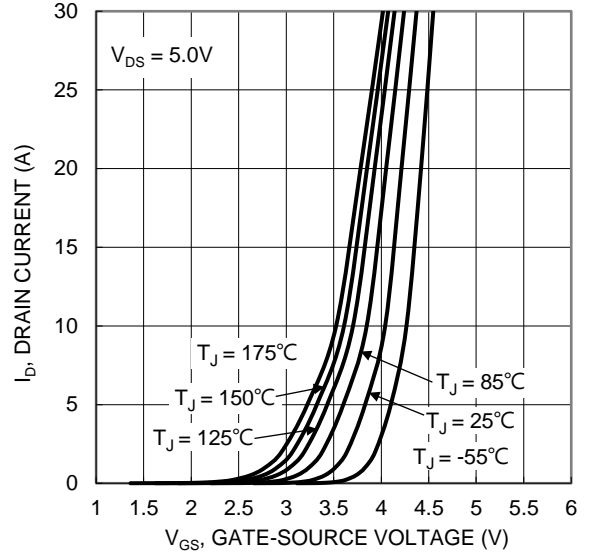


Figure 2. Typical Transfer Characteristic

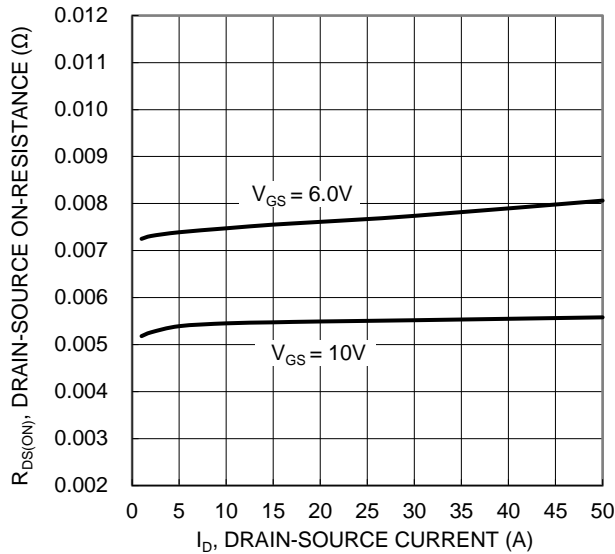


Figure 3. Typical On-Resistance vs. Drain Current and Gate Voltage

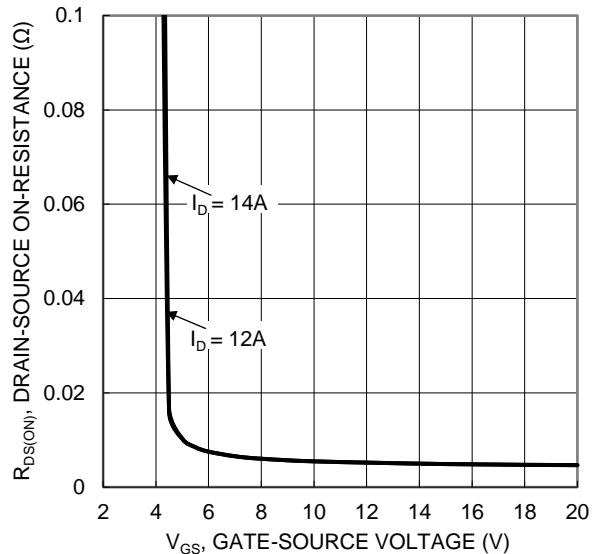


Figure 4. Typical Transfer Characteristic

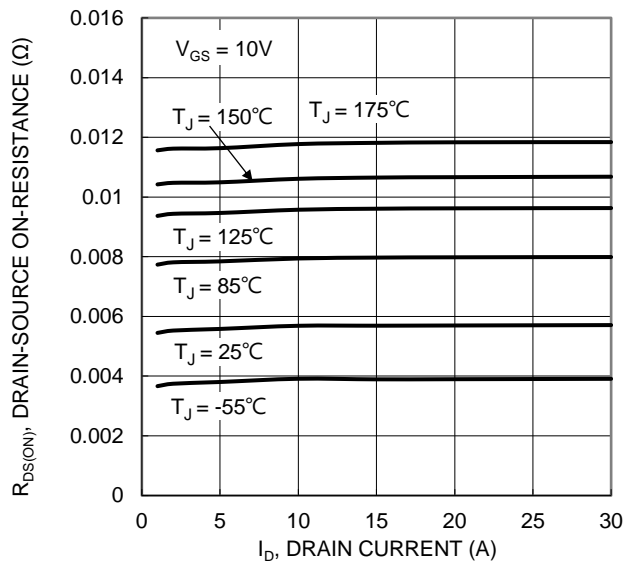


Figure 5. Typical On-Resistance vs. Drain Current and Temperature

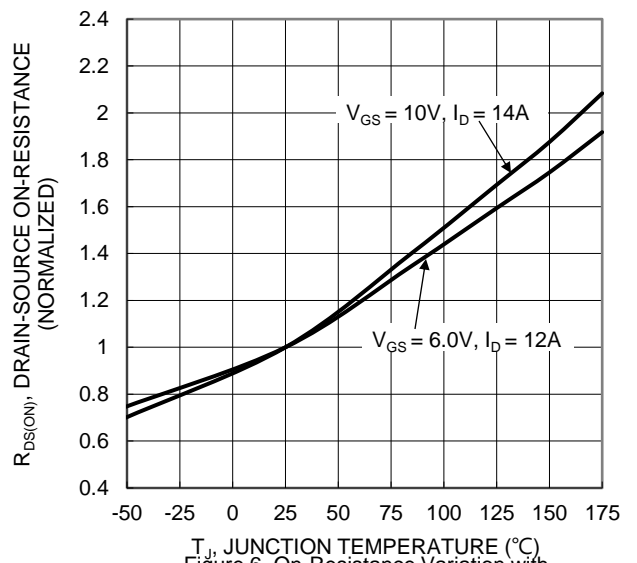


Figure 6. On-Resistance Variation with Temperature



DMTH8008SPS

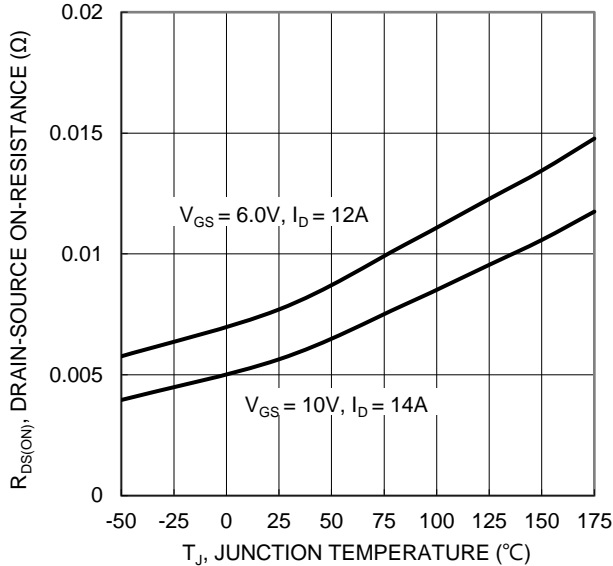


Figure 7. On-Resistance Variation with Temperature

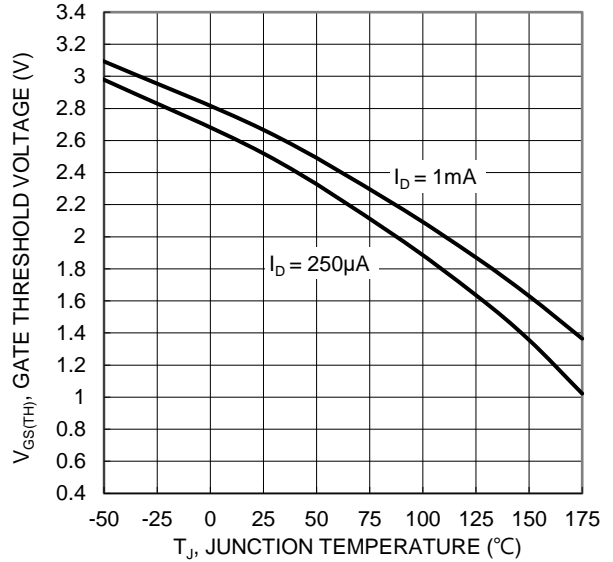


Figure 8. Gate Threshold Variation vs. Junction Temperature

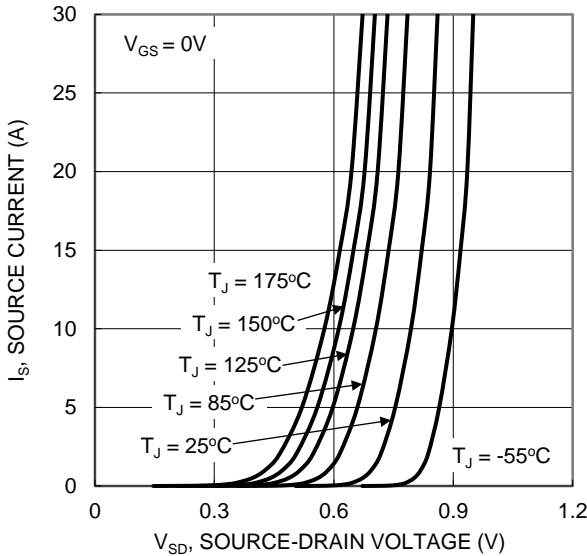


Figure 9. Diode Forward Voltage vs. Current

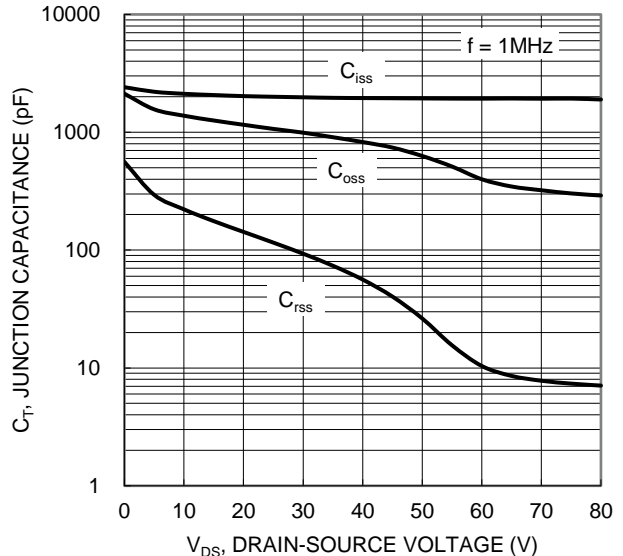


Figure 10. Typical Junction Capacitance

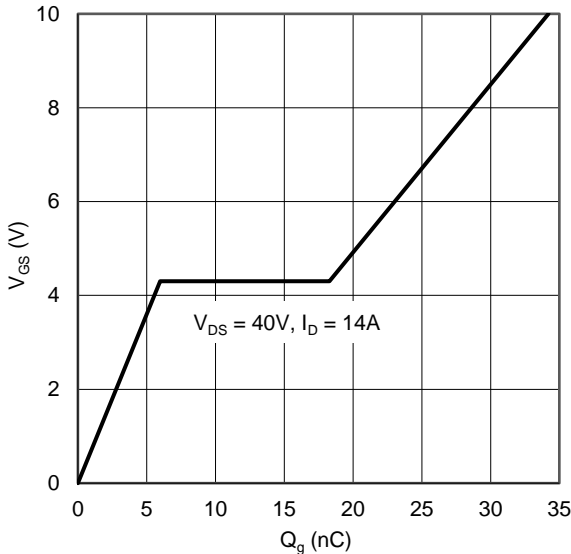


Figure 11. Gate Charge

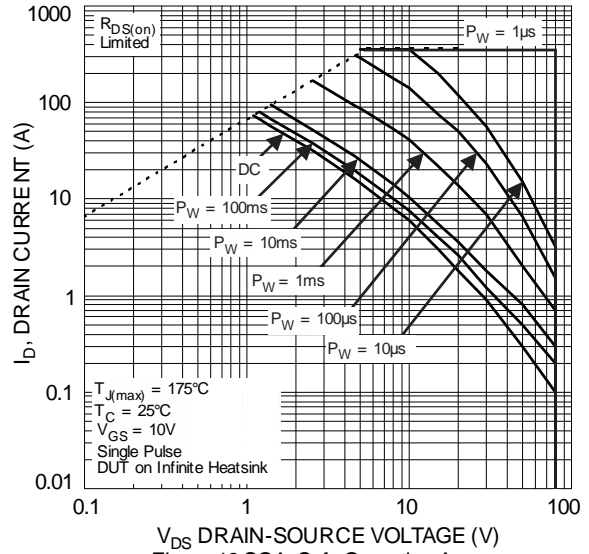


Figure 12 SOA, Safe Operation Area



DMTH8008SPS

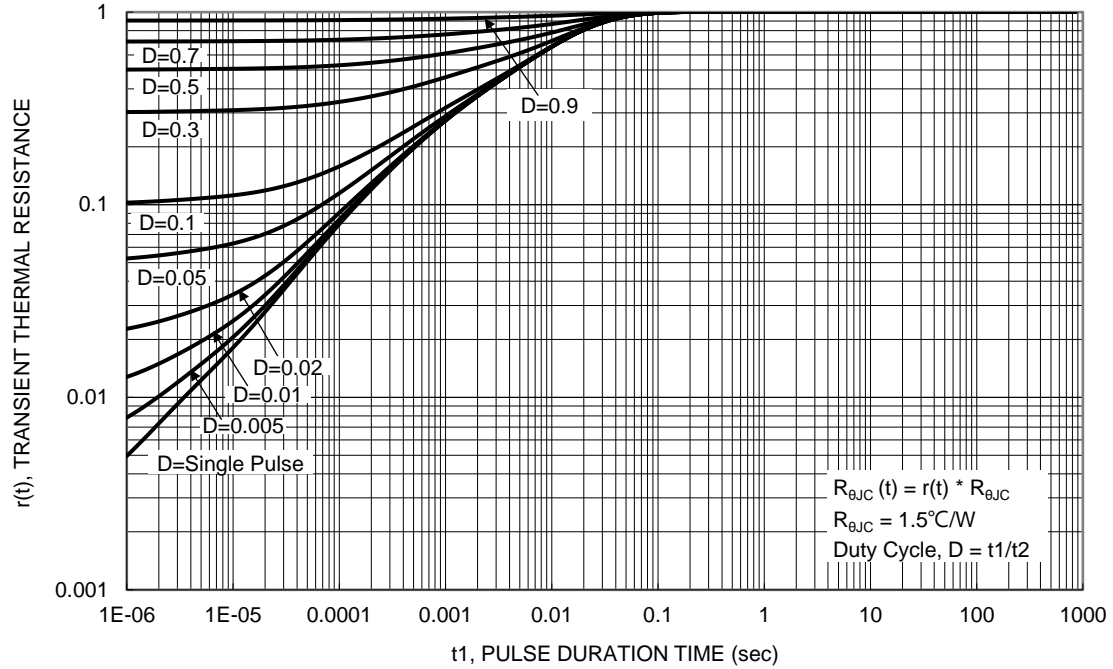
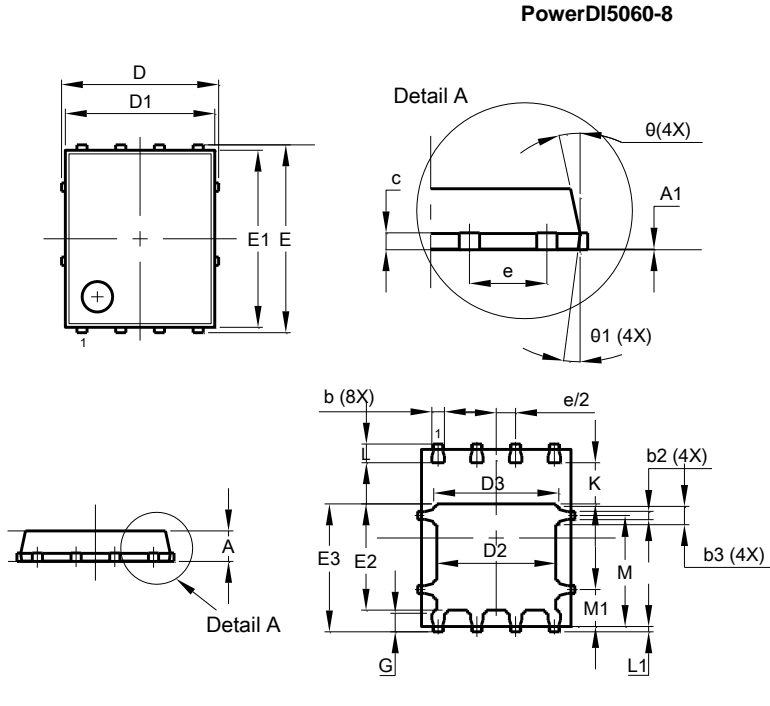


Figure 13. Transient Thermal Resistance

Package Outline Dimensions

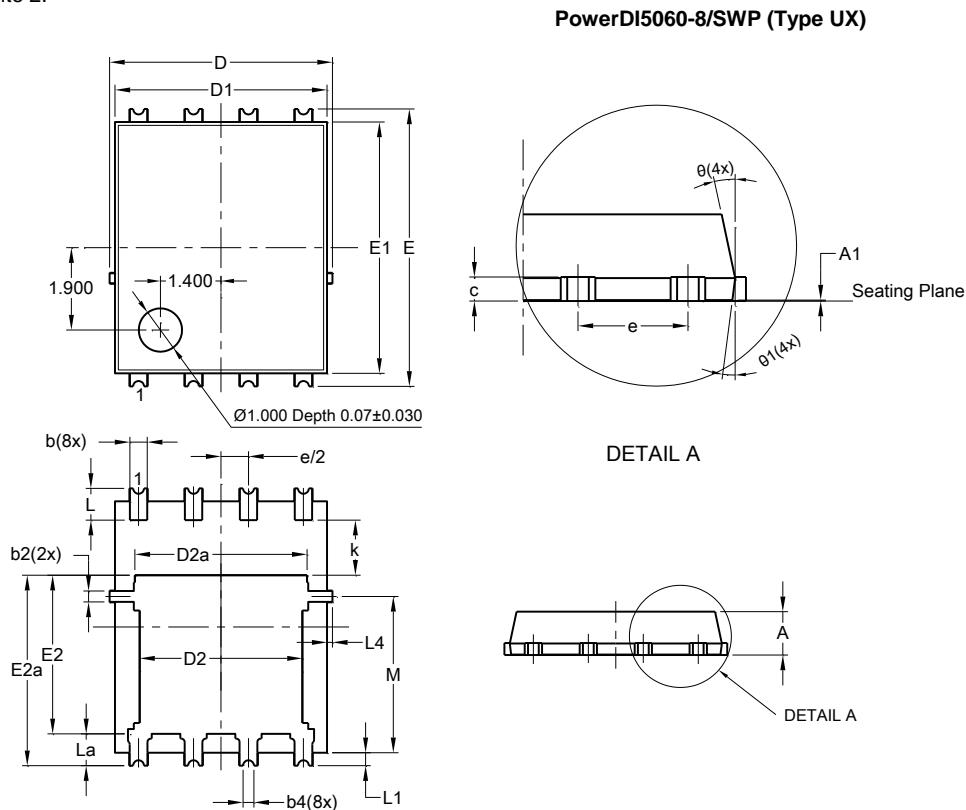
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

Site 1:



PowerDI5060-8			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0.00	0.05	—
b	0.33	0.51	0.41
b2	0.200	0.350	0.273
b3	0.40	0.80	0.60
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.70	4.10	3.90
D3	3.90	4.30	4.10
E	6.15 BSC		
E1	5.60	6.00	5.80
E2	3.28	3.68	3.48
E3	3.99	4.39	4.19
e	1.27 BSC		
G	0.51	0.71	0.61
K	0.51	—	—
L	0.51	0.71	0.61
L1	0.100	0.200	0.175
M	3.235	4.035	3.635
M1	1.00	1.40	1.21
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Site 2:



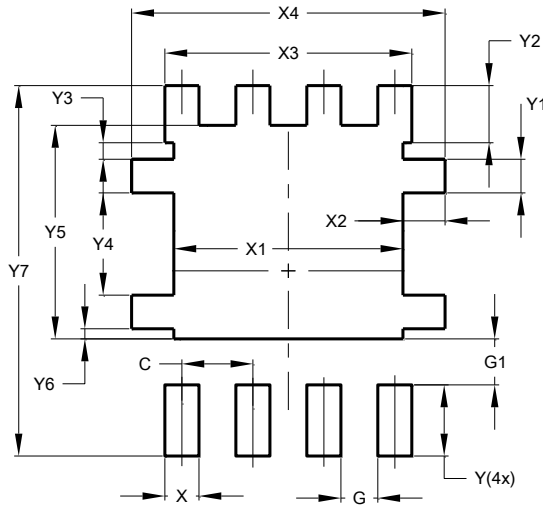
PowerDI5060-8/SWP (Type UX)			
Dim	Min	Max	Typ
A	0.90	1.10	1.00
A1	0	0.05	--
b	0.30	0.50	0.41
b2	0.20	0.35	0.25
b4	0.25REF		
c	0.230	0.330	0.277
D	5.15 BSC		
D1	4.70	5.10	4.90
D2	3.56	3.96	3.76
D2a	3.78	4.18	3.98
E	6.40 BSC		
E1	5.60	6.00	5.80
E2	3.46	3.86	3.66
E2a	4.195	4.595	4.395
e	1.27BSC		
k	1.05	--	--
L	0.635	0.835	0.735
La	0.635	0.835	0.735
L1	0.200	0.400	0.300
L1a	0.050REF		
L4	0.025	0.225	0.125
M	3.205	4.005	3.605
θ	10°	12°	11°
θ1	6°	8°	7°
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

Site 1:

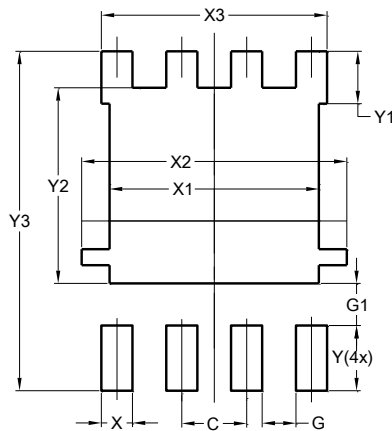
PowerDI5060-8



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	0.755
X3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

Site 2:

PowerDI5060-8/SWP (Type UX)



Dimensions	Value (in mm)
C	1.270
G	0.660
G1	0.820
X	0.610
X1	4.100
X2	5.190
X3	4.420
Y	1.270
Y1	1.020
Y2	3.810
Y3	6.610

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