

DSS4240T-7 Datasheet



<https://www.DiGi-Electronics.com>

DiGi Electronics Part Number	DSS4240T-7-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	DSS4240T-7
Description	TRANS NPN 40V 2A SOT23-3
Detailed Description	Bipolar (BJT) Transistor NPN 40 V 2 A 100MHz 600 mW Surface Mount SOT-23-3



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.

Purchase and inquiry

Manufacturer Product Number:

DSS4240T-7

Series:

-

Transistor Type:

NPN

Voltage - Collector Emitter Breakdown (Max):

40 V

Current - Collector Cutoff (Max):

100nA (ICBO)

Power - Max:

600 mW

Operating Temperature:

-55°C ~ 150°C (TJ)

Package / Case:

TO-236-3, SC-59, SOT-23-3

Base Product Number:

DSS4240

Manufacturer:

Diodes Incorporated

Product Status:

Not For New Designs

Current - Collector (Ic) (Max):

2 A

Vce Saturation (Max) @ Ib, Ic:

320mV @ 200mA, 2A

DC Current Gain (hFE) (Min) @ Ic, Vce:

300 @ 1A, 2V

Frequency - Transition:

100MHz

Mounting Type:

Surface Mount

Supplier Device Package:

SOT-23-3

Environmental & Export classification

RoHS Status:

ROHS3 Compliant

REACH Status:

REACH Unaffected

HTSUS:

8541.21.0075

Moisture Sensitivity Level (MSL):

1 (Unlimited)

ECCN:

EAR99



**THE DSS4240T IS NOT RECOMMENDED FOR NEW DESIGNS.
PLEASE USE THE [ZXTN4240F](#).**

DSS4240T



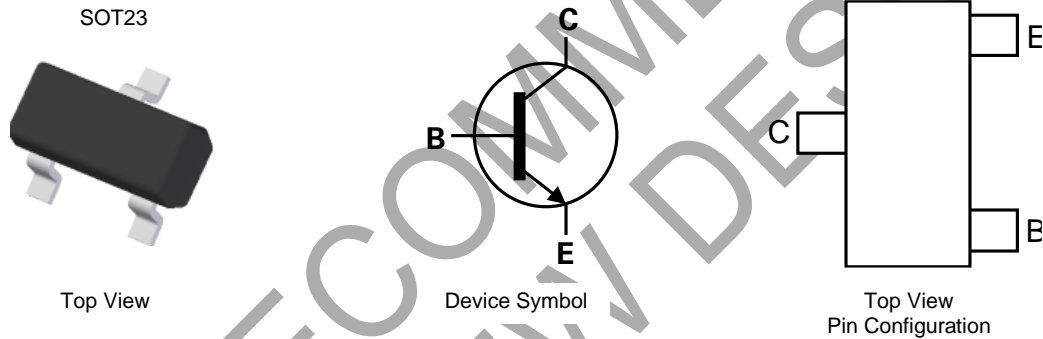
40V NPN LOW SATURATION TRANSISTOR IN SOT23

Features

- $BV_{CEO} > 40V$
- $I_C = 2A$ High Continuous Collector Current
- $I_{CM} = 3A$ Peak Pulse Current
- Low Saturation Voltage 180mV Max @ $I_C = 1A$
- $R_{CE(SAT)} = 60m\Omega$ at 0.5A for a Low Equivalent On-Resistance
- 730mW Power Dissipation
- Complimentary PNP Type: DSS5240T
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **For automotive applications requiring specific change control (i.e.: parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please refer to the related automotive grade (Q-suffix) part. A listing can be found at <https://www.diodes.com/products/automotive/>**
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability. <https://www.diodes.com/quality/product-definitions/>

Mechanical Data

- Package: SOT23
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish — Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208 (E3)
- Weight: 0.008 grams (Approximate)

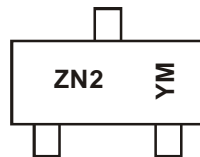


Ordering Information (Note 4)

Orderable Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
DSS4240T-7	SOT23	ZN2	7	8	3000	Reel
DSS4240T-13	SOT23	ZN2	13	8	10,000	Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information



ZN2 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: M = 2025)
M = Month (ex: 9 = September)

Date Code Key

Year	2008	-	2025	2026	2027	2028	2029	2030	2031	2032	2033	2034
Code	V	-	M	N	P	R	S	T	U	V	W	X

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D



DSS4240T

Absolute Maximum Ratings (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V_{CBO}	40	V
Collector-Emitter Voltage	V_{CEO}	40	V
Emitter-Base Voltage	V_{EBO}	5	V
Peak Pulse Collector Current	I_{CM}	3	A
Continuous Collector Current	I_C	2	A
Peak Base Current	I_{BM}	0.3	A

Thermal Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5)	P_D	730	mW
Power Dissipation (Note 6)	P_D	600	mW
Thermal Resistance, Junction to Ambient Air (Note 5)	$R_{\theta JA}$	171	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{\theta JA}$	209	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Lead (Note 7)	$R_{\theta JL}$	75	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction to Case (Note 8)	$R_{\theta JC}$	51	$^\circ\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^\circ\text{C}$

ESD Ratings (Note 9)

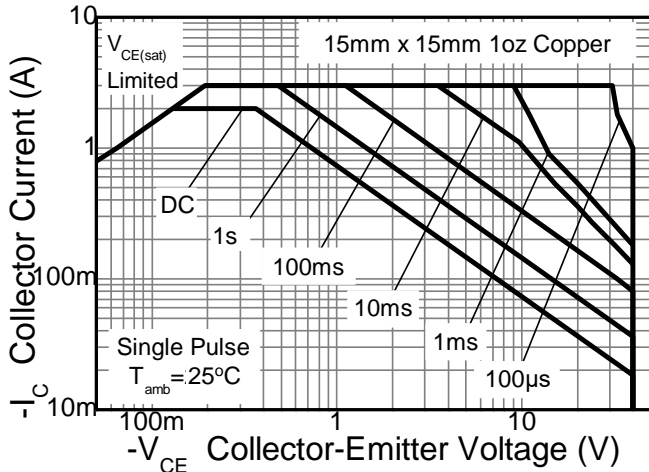
Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge — Human Body Model	ESD HBM	4000	V	3A
Electrostatic Discharge — Machine Model	ESD MM	400	V	C
Electrostatic Discharge — Charged Device Model	ESD CDM	1,000	V	IV

- Notes:
- For a device mounted with the collector lead on 15mm x 15mm 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in a steady-state.
 - Same as Note 5, except the device is mounted on minimum recommended pad layout.
 - Thermal resistance from junction to solder-point (at the end of the collector lead).
 - Thermal resistance from junction to the top of the case.
 - Refer to JEDEC specification JESD22-A114, JESD22-A115 and JESD22-C101.

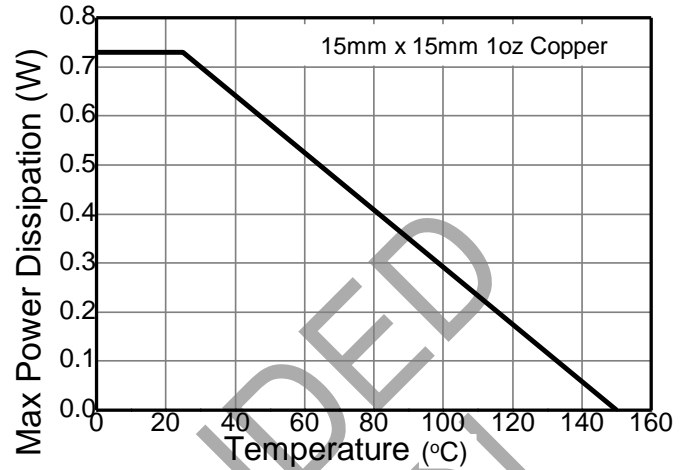


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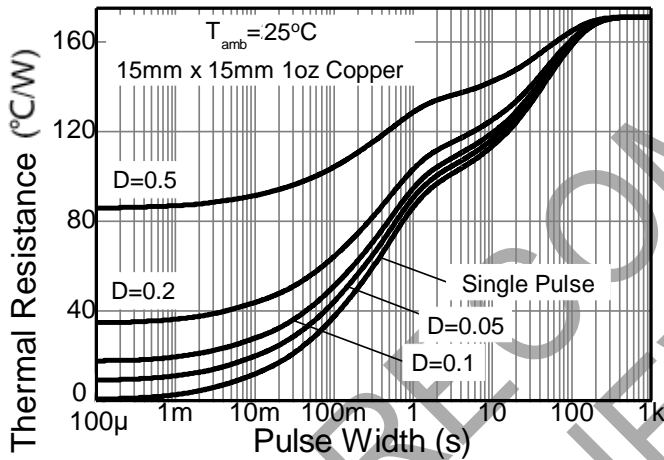
Thermal Characteristics and Derating Information



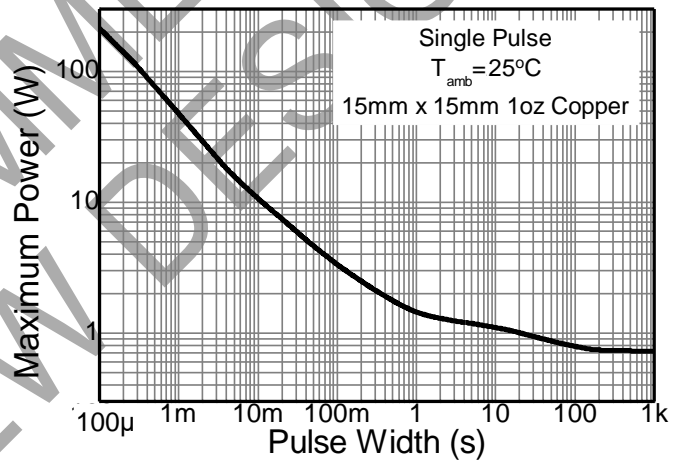
Safe Operating Area



Derating Curve



Transient Thermal Impedance



Pulse Power Dissipation



DSS4240T

Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV_{CBO}	40	—	—	V	$I_C = 100\mu\text{A}$
Collector-Emitter Breakdown Voltage (Note 10)	BV_{CEO}	40	—	—	V	$I_C = 10\text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	5	—	—	V	$I_E = 100\mu\text{A}$
Collector-Base Cutoff Current	I_{CBO}	—	—	100	nA	$V_{CB} = 30\text{V}, I_E = 0$
		—	—	50	μA	$V_{CB} = 30\text{V}, I_E = 0, T_A = +150^\circ\text{C}$
Emitter-Base Cutoff Current	I_{EBO}	—	—	100	nA	$V_{EB} = 4\text{V}, I_C = 0$
ON CHARACTERISTICS (Note 7)						
DC Current Gain	h_{FE}	350	—	—	—	$V_{CE} = 2\text{V}, I_C = 0.1\text{A}$
		300	—	—		$V_{CE} = 2\text{V}, I_C = 0.5\text{A}$
		300	—	—		$V_{CE} = 2\text{V}, I_C = 1\text{A}$
		150	—	—		$V_{CE} = 2\text{V}, I_C = 2\text{A}$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	—	—	70	mV	$I_C = 100\text{mA}, I_B = 1\text{mA}$
		—	30	100		$I_C = 500\text{mA}, I_B = 50\text{mA}$
		—	—	180		$I_C = 750\text{mA}, I_B = 15\text{mA}$
		—	—	180		$I_C = 1\text{A}, I_B = 50\text{mA}$
		—	—	320		$I_C = 2\text{A}, I_B = 200\text{mA}$
Equivalent On-Resistance	$R_{CE(sat)}$	—	60	200	m Ω	$I_C = 500\text{mA}, I_B = 50\text{mA}$
Base-Emitter Saturation Voltage	$V_{BE(sat)}$	—	—	1.1	V	$I_C = 2\text{A}, I_B = 200\text{mA}$
Base-Emitter Turn-on Voltage	$V_{BE(on)}$	—	—	0.75	V	$V_{CE} = 2\text{V}, I_C = 100\text{mA}$
SMALL SIGNAL CHARACTERISTICS						
Transition Frequency	f_T	100	—	—	MHz	$V_{CE} = 10\text{V}, I_C = 100\text{mA}, f = 100\text{MHz}$
Output Capacitance	C_{ob}	—	—	20	pF	$V_{CB} = 10\text{V}, f = 1\text{MHz}$

- Note:
7. Thermal resistance from junction to solder-point (at the end of the collector lead).
 8. Thermal resistance from junction to the top of the case.
 9. Refer to JEDEC specification JESD22-A114, JESD22-A115 and JESD22-C101.
 10. Measured under pulsed conditions. Pulse width $\leq 300\mu\text{s}$. Duty cycle $\leq 2\%$.

NOT RECOMMENDED FOR NEW DESIGN



DSS4240T

Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)

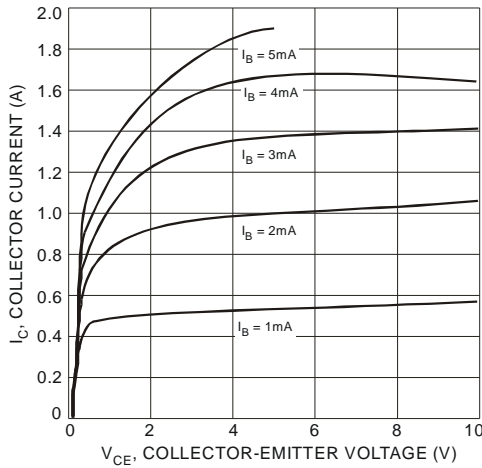


Fig. 2 Typical Collector Current vs. Collector-Emitter Voltage

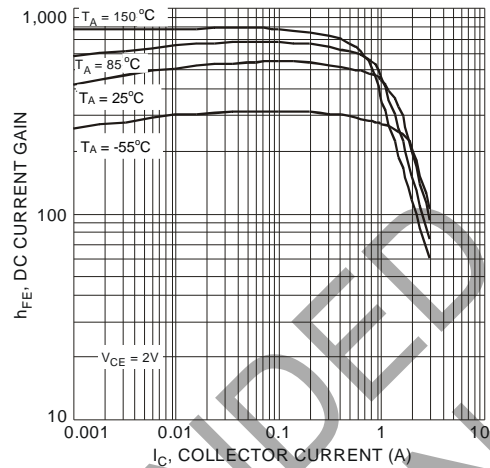


Fig. 3 Typical DC Current Gain vs. Collector Current

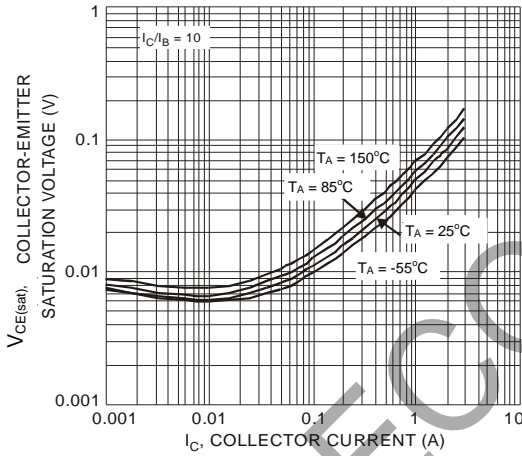


Fig. 4 Typical Collector-Emitter Saturation Voltage vs. Collector Current

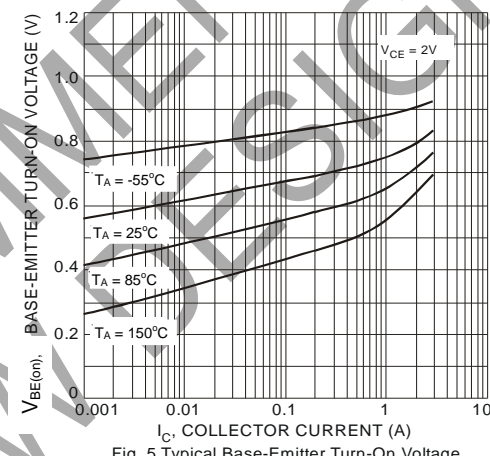


Fig. 5 Typical Base-Emitter Turn-On Voltage vs. Collector Current

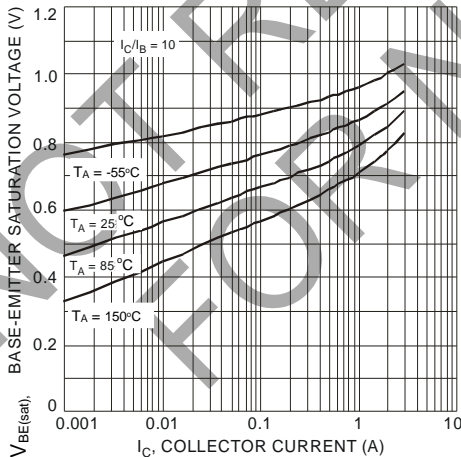


Fig. 6 Typical Base-Emitter Saturation Voltage vs. Collector Current

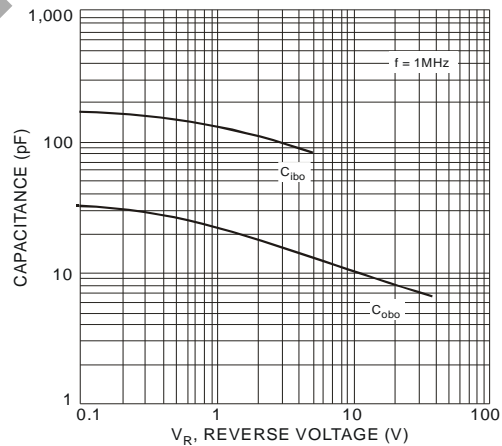


Fig. 7 Typical Capacitance Characteristics

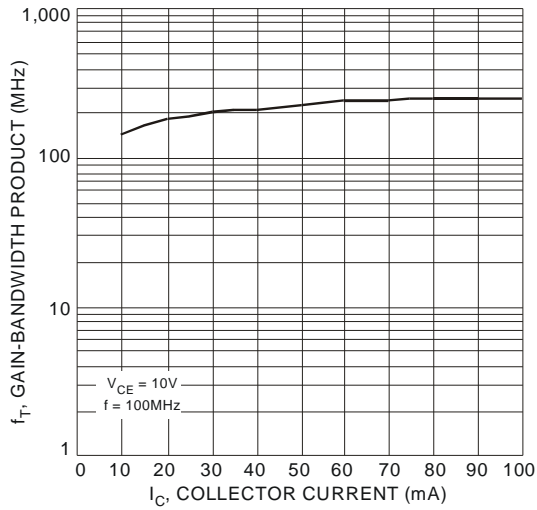
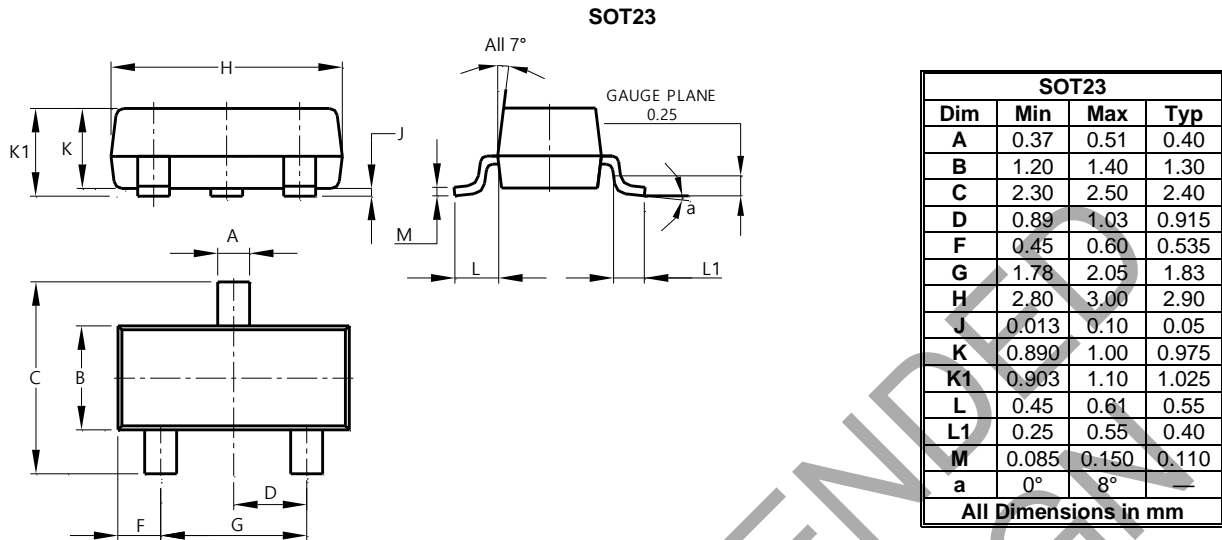
Typical Electrical Characteristics (@ $T_A = +25^\circ\text{C}$, unless otherwise specified.)


Fig. 8 Typical Gain-Bandwidth Product
vs. Collector Current

NOT RECOMMENDED
FOR NEW DESIGN

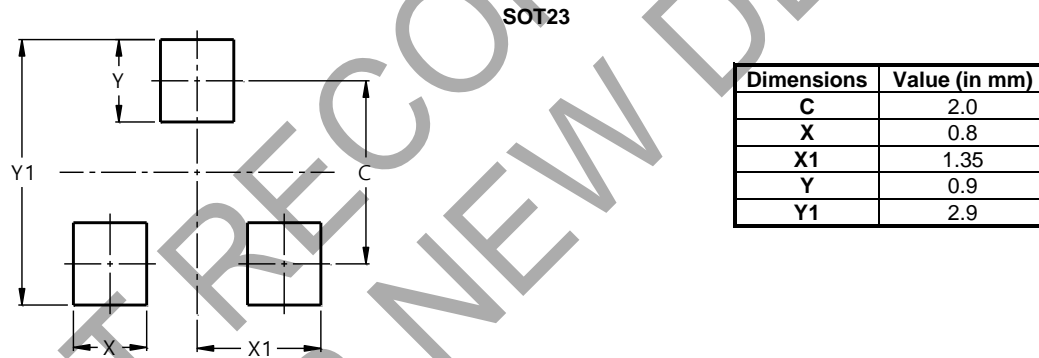
Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



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