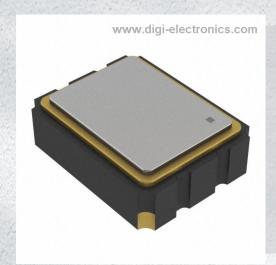


FK3270001 Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number FK3270001-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number FK3270001

Description XTAL OSC XO 32.7680MHZ CMOS SMD

Detailed Description 32.768 MHz XO (Standard) CMOS Oscillator 3.3V En

able/Disable 4-SMD, No Lead



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



Purchase and inquiry

Manufacturer Product Number:	Manufacturer:			
FK3270001	Diodes Incorporated			
Series:	Product Status:			
SaRonix-eCera™ FK	Active			
Base Resonator:	Type:			
Crystal	XO (Standard)			
Frequency:	Function:			
32.768 MHz	Enable/Disable			
Output:	Voltage - Supply:			
CMOS	3.3V			
Frequency Stability:	Absolute Pull Range (APR):			
±25ppm				
Operating Temperature:	Current - Supply (Max):			
-20°C ~ 70°C	10mA			
Ratings:	Mounting Type:			
	Surface Mount			
Package / Case:	Size / Dimension:			
4-SMD, No Lead	0.126" L x 0.098" W (3.20mm x 2.50mm)			
Height - Seated (Max):	Current - Supply (Disable) (Max):			
0.045" (1.15mm)	10μΑ			

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

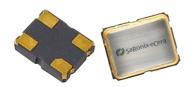
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FK Series Crystal Clock Oscillator (XO) 3.2 x 2.5mm

3.3V CMOS Low Jitter XO





3.2 x 2.5mm Ceramic SMD

Product Features

- •1 to 156.25 MHz Frequency Range
- <1 ps RMS jitter</pre>
- •3.3V CMOS compatible logic levels
- Designed for standard reflow and washing techniques
- Low power standby mode
- Pb-free and RoHS/Green compliant

Product Description

The FK Series 3.3V crystal clock oscillator achieves superb stability and low power consumption over a broad range of operating conditions and frequencies. The low jitter output clock signal, generated internally with a non-PLL oscillator design, is compatible with LVCMOS logic levels. The device, available on tape and reel, is contained in a 3.2 x 2.5mm surface-mount ceramic package.

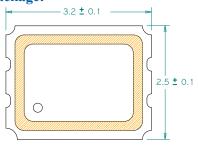
Applications

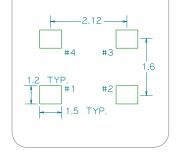
Ideal for compact, high-density applications requiring low power or tight stability, including:

15-0088

- Network adapter cards
- Portable Multimedia Devices
- Hard Disk Drives
- GPS/Navigation
- Bluetooth
- •802.11a/b/g WiFi

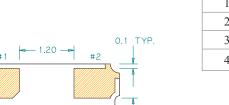
Package:





Recommended Land Pattern:

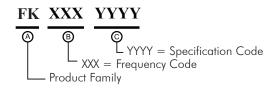




Pin Functions:

Pin	Function					
1	OE					
2	Ground					
3	Clock Output					
4	V_{DD}					

Part Ordering Information:



Following the above format, Saronix-eCera part numbers will be assigned upon confirmation of exact customer requirements.

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3.3V CMOS Low Jitter XO FK



FK Series Crystal Clock Oscillator (XO) 3.2 x 2.5mm

Electrical Performance

	Parameter	Min.	Тур.	Max.	Units	Notes
Output Frequence	су	1		156.25	MHz	As specified
Supply Voltage		2.97	3.3	3.63	V	
Supply Current, Output Enabled				10	mA	1 to 50 MHz
				18	IIIA	50 to 156.25 MHz
Supply Current,	Standby Mode			10	μΑ	Output Hi-Z
Frequency Stabi	lity			±20 to ±50	ppm	See Note 1 below
Operating Temperature Range		-20		+70	°C	Commercial (standard)
		-40		+85	C	Industrial (standard)
Output Logic 0,	V_{OL}			10% V _{DD}	V	
Output Logic 1,	V_{OH}	90% V _{DD}			V	
Output Load				15	pF	
Duty Cycle		45		55	%	Measured 50% V _{DD}
Rise and Fall Ti	me			5	ns	Measured 10/90% of waveform
Jitter, Phase	1 to 156.25 MHz			1	ps RMS (1-σ)	12kHz to 20 MHz frequency band
Jitter, Accumulated	up to 75 MHz			5	ng PMS (1 g)	20.000 adjacent periods
	75 to 156.25 MHz			3	ps RMS (1-σ)	
Jitter,	up to 75 MHz			50	na ple ple	100 000 randam nariada
Total	75 to 156.25 MHz			30	ps pk-pk	100.000 random periods

Notes:

- Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), aging (1 year at 25°C average effective ambient temperature), shock and vibration.
- 2. For specifications othere than those listed, please contact sales.

Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (pin 1), Output Enable	0.7 V _{DD}			V	or open
Input Voltage (pin 1), Output Disable (low power standby)			0.3 V _{DD}	V	Output is Hi-Z
Internal Pullup Resistance	50			kΩ	
Output Disable Delay			100	ns	
Output Enable Delay			10	ms	

Absolute Maximum Ratings

Parameter	Min.	Тур.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: http://www.pericom.com/products/timing/oscillators/FK3.3/

For test circuit go to: http://www.pericom.com/pdf/sre/tc_hcmos2.pdf

For soldering reflow profile and reliability test ratings go to: http://www.pericom.com/pdf/sre/reflow.pdf

For tape and reel information go to: http://www.pericom.com/pdf/sre/tr_3225_xo.pdf





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