

LDE830001 Datasheet

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DiGi Electronics Part Number	LDE830001-DG
Manufacturer	Diodes Incorporated
lanufacturer Product Number	LDE830001
Description	XTAL OSC XO 148.351648MHZ LVDS
Detailed Description	148.351648 MHz XO (Standard) LVDS Oscillator 2.5 V

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
LDE830001	Diodes Incorporated
Series:	Product Status:
SaRonix-eCera™ LD	Active
Base Resonator:	Туре:
Crystal	XO (Standard)
Frequency:	Output:
148.351648 MHz	LVDS
Voltage - Supply:	Absolute Pull Range (APR):
2.5V	- A Contract of the second
Operating Temperature:	Size / Dimension:
-40°C ~ 85°C	0.197" L x 0.126" W (5.00mm x 3.20mm)

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	



LD Series Crystal Clock Oscillator (XO)

5.0 x 3.2mm

2.5V LVDS Low Jitter XO





Product Features

- 38.88 to 162 MHz Frequency Range
- <1 ps RMS jitter with non-PLL design
- Designed for standard reflow & washing techniques
- IBIS models available
- Pb-free & RoHS/Green compliant

Product Description

The LD Series 2.5V crystal clock oscillator achieves superb jitter and stability over a broad range of operating conditions and frequencies. The output clock signal, generated internally with a non-PLL oscillator design, is compatible with LVDS logic levels. The device, available on tape and reel, is contained in a 5.0 x 3.2mm surface-mount ceramic package.

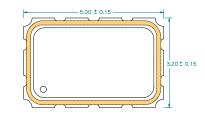
Applications

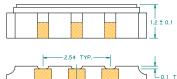
The LD Series is ideal for high-speed applications requiring low jitter, including:

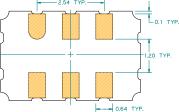
- 1/10 Gigabit Ethernet
- 2/4/10G FibreChannel
- Serial Attached SCSI (SAS)
- Server & Storage platforms
- SONET/SDH linecards
- Passive Optical Network (PON) devices
- HD Video Systems

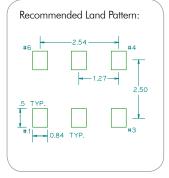
Enabling Serial Connectivity

Package:





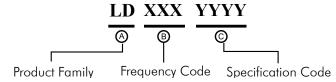




Pin Functions:

Pin	Function			
1	OE or NC			
2	NC or OE			
3	Ground			
4	Q Output			
5	Q Output			
6	V _{CC}			

Part Ordering Information:



Following the above format, Saronix-eCera part numbers will be assigned upon confirmation of exact customer requirements.

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2.5V LVDS Low Jitter XO LD

LD Series Crystal Clock Oscillator (XO) 5.0 x 3.2mm

Electrical Performance

Parameter	Min.	Тур.	Max.	Units	Notes
Output Frequency	38.88		162	MHz	As specified
Supply Voltage	2.25	2.50	2.75	V	
Supply Current, Enabled		35	47	mA	
Supply Current, Disabled			0.03	mA	
Frequency Stability			±20 to ±50	ppm	See Note 1 below
O	-20		+70	°C	Commercial (standard)
Operating Temperature Range	-40		+85		Industrial (standard)
Output Logic 0, V _{OL}	0.9	1.1		V	
Output Logic 1, V _{OH}		1.43	1.6	V	
Output Load	100Ω conr	100Ω connected between both outputs			output requires termination
Duty Cycle	45		55	%	measured 50% of waveform
Rise and Fall Time		500	850	ps	measured 20/80% of waveform
Jitter, Phase		0.5	1	ps RMS (1-σ)	12kHz to 20MHz frequency band
Jitter, Total			25	ps pk–pk	100,000 random periods

Notes:

1. Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), aging (5 year at 40°C average effective ambient temperature), shock and vibration.

2. For specifications othere than those listed, please contact sales.

Output Enable / Disable Function

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (pin OE), Output Enable	0.7			V	or open
Input Voltage (pin OE), Output Disable (low power standby)			0.3	V	Outpus disabled to Hi-Z
Internal Pullup Resistance	50			kΩ	
Output Disable Delay			200	ns	
Output Enable Delay			10	ms	

Absolute Maximum Ratings

Parameter	Min.	Тур.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: http://www.pericom.com/products/timing/oscillators/LD 2.5V/

For test circuit go to: <u>http://www.pericom.com/pdf/sre/tc_lvds.pdf</u>

For soldering reflow profile and reliability test ratings go to: <u>http://www.pericom.com/pdf/sre/reflow2.pdf</u>

For tape and reel information go to: http://www.pericom.com/pdf/sre/tr 5032 xo.pdf

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