

# PDG110003 Datasheet

www.digi-electronics.com



DiGi Electronics Part Number	PDG110003-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	PDG110003
Description	XTAL OSC XO 161.1330MHZ PECL SMD
Detailed Description	161.133 MHz XO (Standard) PECL Oscillator 3.3V En able/Disable 6-SMD, No Lead

https://www.DiGi-Electronics.com



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



# Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PDG110003	Diodes Incorporated
Series:	Product Status:
SaRonix-eCera™ PD	Active
Base Resonator:	Туре:
Crystal	XO (Standard)
Frequency:	Function:
161.133 MHz	Enable/Disable
Output:	Voltage - Supply:
PECL	3.3V
Frequency Stability:	Absolute Pull Range (APR):
±50ppm	
Operating Temperature:	Current - Supply (Max):
-20°C ~ 70°C	88mA
Ratings:	Mounting Type:
	Surface Mount
Package / Case:	Size / Dimension:
6-SMD, No Lead	0.197" L x 0.126" W (5.00mm x 3.20mm)
Height - Seated (Max):	Current - Supply (Disable) (Max):
0.051" (1.30mm)	30µA

# **Environmental & Export classification**

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	



# PD Series Crystal Clock Oscillator (XO)

# 5.0 x 3.2mm

# **3.3V PECL Low Jitter XO**





### **Product Features**

- 38.88 to 162 MHz Frequency Range
- <1 ps RMS jitter with non-PLL design
- Designed for standard reflow & washing techniques
- IBIS models available
- Pb-free & RoHS/Green compliant

## **Product Description**

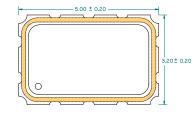
The PD Series 3.3V crystal clock oscillator achieves superb jitter and stability over a broad range of operating conditions and frequencies. The output clock signal, generated internally with a non-PLL oscillator design, is compatible with LVPECL logic levels. The device, available on tape and reel, is contained in a 5.0 x 3.2mm surface-mount ceramic package.

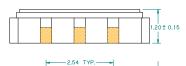
## **Applications**

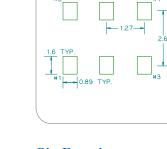
The PD Series is ideal for high-speed applications requiring low jitter, including:

- 1/10 Gigabit Ethernet
- 2/4/10G FibreChannel
- Serial Attached SCSI (SAS)
- Server & Storage platforms
- SONET/SDH linecards
- Passive Optical Network (PON) devices
- HD Video Systems

#### Package:







Recommended Land Pattern:

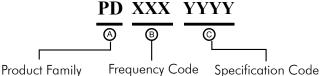
# **Pin Functions:**

• www.saronix-ecera.com

1

Pin	Function			
1	OE or NC			
2	NC or OE			
3	Ground			
4	Q Output			
5	$\overline{\mathbf{Q}}$ Output			
6	V <sub>CC</sub>			

#### **Part Ordering Information:**



- 0.64 TYE

20 TYP

Following the above format, Saronix-eCera part numbers will be assigned upon confirmation of exact customer requirements.

SaRonix-eCera™ is a Pericom® Semiconductor company • US: +1-408-435-0800 TW: +886-3-4518888



# 3.3V PECL Low Jitter XO PD



# **PD Series Crystal Clock Oscillator (XO)** 5.0 x 3.2mm

#### **Electrical Performance**

Parameter	Min.	Тур.	Max.	Units	Notes
Output Frequency	38.88		162	MHz	As specified
Supply Voltage	2.97	3.30	3.63	V	
Supply Current, enabled		55	88	mA	
Supply Current, Disabled			0.03	mA	
Frequency Stability			±20 to ±50	ppm	See Note 1 below
On anotice a Tarra anotaria Banas	-20		+70	°C	Commercial (standard)
Operating Temperature Range	-40		+85		Industrial (standard)
Output Logic 0, V <sub>OL</sub>			V <sub>CC</sub> - 1.62	V	
Output Logic 1, V <sub>OH</sub>	V <sub>CC</sub> - 1.025			V	
Output Load	50Ω connec	$50\Omega$ connected connected to V <sub>CC</sub> - 2 V			output requires termination
Duty Cycle	45		55	%	measured 50% of waveform
Rise and Fall Time		500	850	ps	measured 20/80% of waveform
Jitter, Phase		0.5	1	ps RMS (1-o)	12kHz to 20MHz frequency band
Jitter, Total			25	ps pk–pk	100,000 random periods

#### Notes:

Stability includes all combinations of operating temperature, load changes, rated input (supply) voltage changes, initial calibration tolerance (25°C), 1. aging (5 year at 40°C average effective ambient temperature), shock and vibration.

For specifications othere than those listed, please contact sales. 2.

#### **Output Enable / Disable Function**

Parameter	Min.	Тур.	Max.	Units	Notes
Input Voltage (pin OE), Output Enable	0.7			V	or open
Input Voltage (pin OE), Output Disable (low power standby)			0.3	V	Outpus disabled to Hi-Z
Internal Pullup Resistance	50			kΩ	
Output Disable Delay			200	ns	
Output Enable Delay			10	ms	

#### **Absolute Maximum Ratings**

Parameter	Min.	Тур.	Max.	Units	Notes
Storage Temperature	-55		+125	°C	

For the latest product information visit: http://www.pericom.com/products/crystals-and-crystal-oscillators/standard-xo/?part=PD+3.3V

For test circuit go to: http://www.pericom.com/assets/sre/tc pecl.pdf

For soldering reflow profile and reliability test ratings go to: <u>http://www.pericom.com/assets/sr</u>e/reflow2.pdf

For tape and reel information go to: http://www.pericom.com/assets/sre/tr 5032 xo.pdf

SaRonix-eCera<sup>™</sup> is a Pericom® Semiconductor company • US: +1-408-435-0800 TW: +886-3-4518888





# **OUR CERTIFICATE**

DiGi provide top-quality products and perfect service for customer worldwide through standardization, technological innovation and continuous improvement. DiGi through third-party certification, we striciy control the quality of products and services. Welcome your RFQ to Email: Info@DiGi-Electronics.com

	<section-header></section-header>		
Marginary Marginary   Marginary	Market M Market Market Mark	Marchine Marchine Image: Control of the sector of the sec	





Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.