

# PI49FCT32807QEX Datasheet

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 DiGi Electronics Part Number
 PI49FCT32807QEX-DG

 Manufacturer
 Diodes Incorporated

 Manufacturer Product Number
 PI49FCT32807QEX

 Description
 IC CLK BUFFER 1:10 133MHZ 20QSOP

 Detailed Description
 Clock Fanout Buffer (Distribution) IC 1:10 133 MHz 2 0-SSOP (0.154", 3.90mm Width)

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# Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PI49FCT32807QEX	Diodes Incorporated
Series:	Product Status:
49FCT	Active
Туре:	Number of Circuits:
Fanout Buffer (Distribution)	1
Ratio - Input:Output:	Differential - Input:Output:
1:10	No/No
Input:	Output:
CMOS	CMOS
Frequency - Max:	Voltage - Supply:
133 MHz	2.97V ~ 3.63V
Operating Temperature:	Mounting Type:
-40°C ~ 85°C	Surface Mount
Package / Case:	Supplier Device Package:
20-SSOP (0.154", 3.90mm Width)	20-QSOP
Base Product Number:	
PI49FCT32807	

# **Environmental & Export classification**

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	





#### 3.3V 1:10 CMOS Clock Driver

### **Features**

- → Low skew: < 200ps
- Fast switching frequency >133 MHz →
- Fast output rise/fall time < 1.5ns →
- Low propagation delay < 2.5ns →
- Low input capacitance < 6.0pF →
- 5V I/O Tolerant input →
- Rail-to-Rail CMOS outputs →
- Industrial Temperature: -40°C to +85°C →
- 3.3V ±10% operation →
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) →
- Halogen and Antimony Free. "Green" Device (Note 3) →
- For automotive applications requiring specific change control → (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

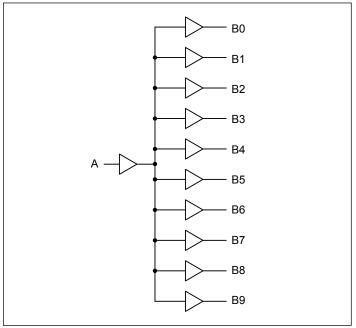
- Packaging (Pb-free & Green): →
  - 20-pin 150-mil wide QSOP (Q)

## Description

Diodes' PI49FCT32807 is a 3.3V very low-skew clock buffer that produces ten outputs from a single low-capacitance input. Excellent output signals to power and ground ratio minimize power and ground noise, and also improves output performance.

The PI49FCT32807 integrates series damping resistors on all outputs.

## **Block Diagram**



Notes:

<sup>1.</sup> No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

<sup>2.</sup> See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





# **Pin Configuration**

АС		20 🗆 V <sub>CC</sub>
GND 🗆	2 .	19 🗆 B9
во 🗆	3	18 🗆 B8
V <sub>CC</sub> [	4 <sup>.</sup>	
B1 🗆	5	16 🗆 B7
gnd E	6 <sup>.</sup>	15 🛛 V <sub>CC</sub>
B2 🗆	7	14 🛛 B6
V <sub>CC</sub> [	8	
вз 🗆	9	12 🗆 B5
gnd E	10 <sup>·</sup>	11 🗆 B4

# **Pin Description**

Pin Name	Description
Α	Input
B <sub>0</sub> -B <sub>9</sub>	Outputs
GND	Ground
V <sub>CC</sub>	Power



Note:



PI49FCT32807

## **Maximum Ratings**

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	2
Supply Voltage to Ground Potential (Inputs & $V_{CC}$ Only)–0.5V to +7.0V	7
Supply Voltage to Ground Potential (Outputs & D/O Only) $-0.5V$ to $+7.0V$	7
DC Input Voltage0.5V to +7.0V	7
DC Output Current	ł
Power Dissipation	Ι
Latch up 200mA	ł
ESD Protection (Input)	)
Junction Temperature	

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

#### DC Electrical Characteristics (Over the Operating Range)

Symbol	Parameter	Test Condition <sup>(1</sup>	)	Min.	Тур.	Max.	Units
V <sub>OH</sub>	Output High Voltage	$V_{CC} = 3V$ , $V_{IN} = V_{IH}$ or $V_{IL}$	I <sub>OH</sub> = -8mA	2.4	3.0		
V <sub>OL</sub>	Output Low Voltage	$V_{CC} = 3V V_{IH} \text{ or } V_{IL}$	$I_{OL} = 12mA$		0.4	0.5	V
V <sub>IH</sub>	Input High Voltage	Guaranteed Logic HIGH Level (	Input pins)	2.0		505	v
V <sub>IL</sub>	Input Low Voltage	Guaranteed Logic LOW Level (I	nput pins)	-0.5		0.8	
I <sub>IH</sub>	Input High Current	$V_{CC} = 3.6 V$	$V_{IN} = 3.6V$			1	A
I <sub>IL</sub>	Input Low Current	$V_{CC} = 3.6 V$	$V_{IN} = 0V$			-1	μA
V <sub>IK</sub>	Clamp Diode Voltage	$V_{CC}$ = Min., $I_{IN}$ = -18mA			-0.7	-1.2	V
I <sub>OH</sub>	Output HIGH Current	$V_{CC} = 3.3V$ , $V_{IN} = V_{IL}$ or $V_{IH}$ , $V$	$OUT = 1.5V^{(5)}$	-25	-45	-80	
I <sub>OL</sub>	Output LOW Current	$V_{CC}$ = 3.3V, $V_{IN}$ = $V_{IL}$ or $V_{IH}$ , V	$OUT = 1.5V^{(5)}$	25	45	90	mA
I <sub>OS</sub>	Short Circuit <sup>(5)</sup> Current	$V_{CC} = Max., V_{OUT} = GND^{(5)}$		-50	-100	-180	
V <sub>H</sub>	Input Hysteresis				150		mV
R <sub>S</sub>	Internal Series Resistor				22		Ω

Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type. 1.

Typical values are at  $V_{CC} = 3.3V$ ,  $+25^{\circ}C$  ambient and maximum loading. 2.

3.  $V_{OH} = V_{CC} - 0.6V$  at rated current.

This parameter is determined by device characterization but is not production tested. 4.

Not more than one output should be shorted at one time. Duration of the test should not exceed one second. 5.





#### **Power Supply Characteristics**

Parameters	Description	Test Condi	tions <sup>(1)</sup>	Min.	<b>Typ</b> <sup>(2)</sup>	Max.	Units
I <sub>CC</sub>	Quiescent Power Supply Current	V <sub>CC</sub> = Max.	$V_{IN} = GND \text{ or } V_{CC}$	_	0.1	30	
$\Delta I_{CC}$	Supply Current per Inputs @ TTL HIGH	V <sub>CC</sub> = Max.	$V_{\rm IN} = V_{\rm CC} - 0.6 V^{(3)}$	_	47	300	μA
I <sub>CCD</sub>	Supply Current per Input per MHz <sup>(4)</sup>	V <sub>CC</sub> = Max., Outputs Open Per Output Toggling 50% Duty Cycle	$V_{IN} = V_{CC}$ $V_{IN} = GND$		0.08	0.16	mA/ MHz

Notes:

For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device. 1

2. Typical values are at  $V_{CC} = 3.3V$ ,  $+25^{\circ}C$  ambient.

Per TTL driven input ( $V_{IN} = V_{CC} - 0.6V$ ); all other inputs at  $V_{CC}$  or GND. 3.

This parameter is not directly testable, but is derived for use in Total Power Supply Calculations. 4.

5. Values for these conditions are examples of the I<sub>C</sub> formula. These limits are guaranteed but not tested.

#### **Capacitance** ( $T_A = 25^{\circ}C$ , f = 1 MHz)

Parameters <sup>(1)</sup>	Description	Test Conditions	Тур	Max.	Units
C <sub>IN</sub>	Input Capacitance	$V_{IN} = 0V$	3.0	4	пE
C <sub>OUT</sub>	Output Capacitance	$V_{OUT} = 0V$		6	pF

Notes:

This parameter is determined by device characterization but is not production tested. 1

#### Maximum Switching Characteristics (Over operating range)

Symbol	Description	Condition	Max.	Units <sup>(3)</sup>
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay A to $B_N^{(3)}$	$C_{\rm L} = 15 \mathrm{pF}$	2.5	
t <sub>R</sub> /t <sub>F</sub>	Rise/Fall Time <sup>(2)</sup>	0.8V - 2.0V	1.5	
t <sub>SK(P)</sub>	Pulse Skew, same package <sup>(1,2)</sup>		0.35	ns
t <sub>SK(O)</sub>	Output Skew, same package <sup>(1,2)</sup>	C 15-E	0.20	
t <sub>SK(I)</sub>	Package Skew, different package <sup>(1,2)</sup>	$C_L = 15 pF$	0.55	
F <sub>IN</sub>	Input Frequency <sup>(1,2)</sup>		133	MHz

Notes:

Other loading condition is described on page 4, "Test Circuits for All Outputs." 1.

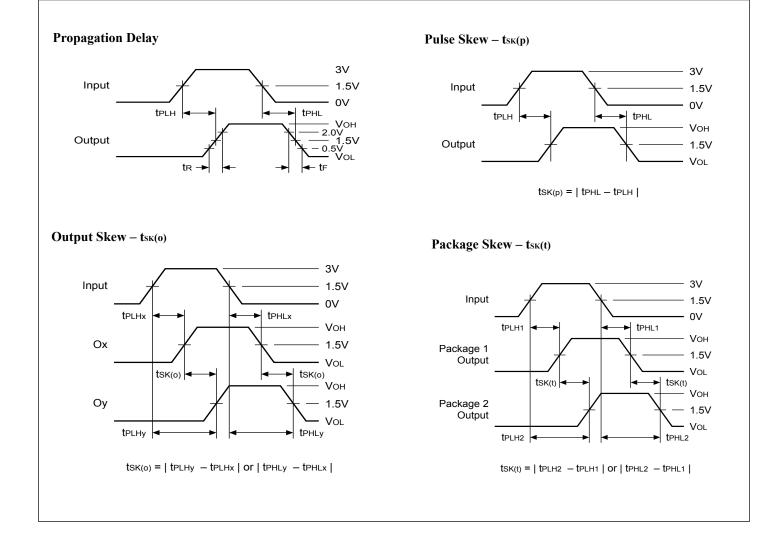
2. These parameters are guaranteed by design.

3. Minimum propagation delay of 1.5ns is guaranteed by design.





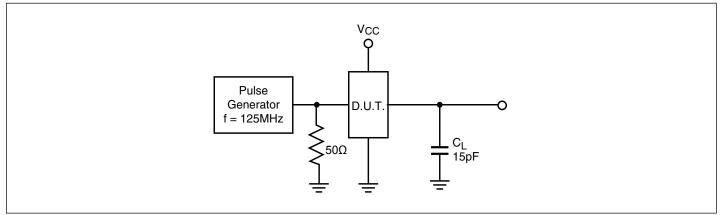
# **Switching Waveforms**







# **Tests Circuits for All Outputs**



# **Part Marking**

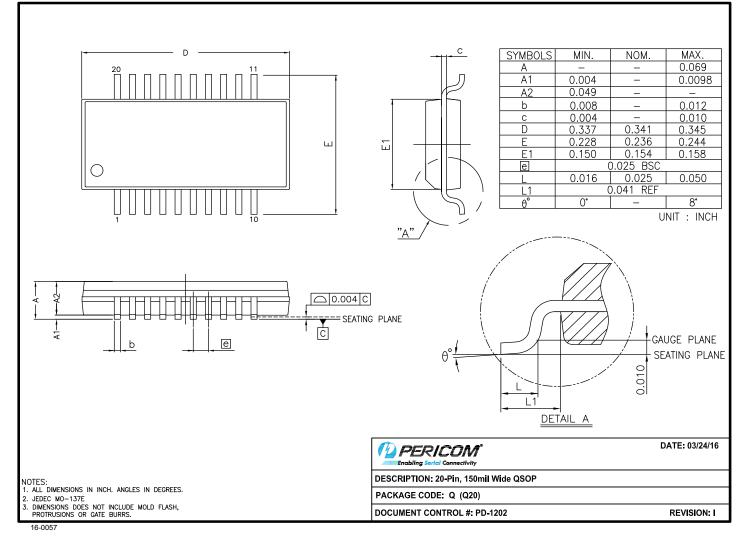


YY: Year WW: Workweek 1st X: Assembly Code 2nd X: Fab Code





# Packaging Mechanical: 20-QSOP (Q)



#### For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

# **Ordering Information**

Ordering Code	Package Code	Package Description
PI49FCT32807QEX	Q	20-pin, 150-mil Wide (QSOP)

#### Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm

antimony compounds.

4. E = Pb-free and Green

5. X suffix = Tape/Reel





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