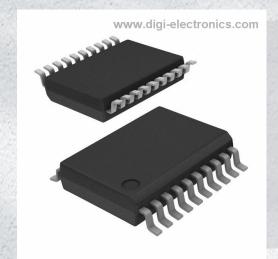


PI49FCT3805DQEX Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number PI49FCT3805DQEX-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number PI49FCT3805DQEX

Description IC CLK BUFFER 1:5 133MHZ 20QSOP

Detailed Description Clock Fanout Buffer (Distribution) IC 1:5 133 MHz 20

-SSOP (0.154", 3.90mm Width)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PI49FCT3805DQEX	Diodes Incorporated
Series:	Product Status:
49FCT	Active
Type:	Number of Circuits:
Fanout Buffer (Distribution)	2
Ratio - Input:Output:	Differential - Input:Output:
1:5	No/No
Input:	Output:
CMOS, TTL	CMOS
Frequency - Max:	Voltage - Supply:
133 MHz	3V ~ 3.6V
Operating Temperature:	Mounting Type:
-40°C ~ 85°C	Surface Mount
Package / Case:	Supplier Device Package:
20-SSOP (0.154", 3.90mm Width)	20-QSOP
Base Product Number:	
PI49FCT3805	

Environmental & Export classification

8542.39.0001

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	





3.3V, 2 x 1:5 CMOS Clock Driver

Features

- → Low output skew: <200ps
- → Switching frequency up to 166 MHz
- → Fast output rise/fall time: <1.0ns
- → Low propagation delay: <2.5ns
- → Low input capacitance: <6.0pF
- → Balanced CMOS outputs
- → Industrial Temperature: -40°C to +85°C
- → 3.3V ±10% operation, 5V Input Tolerant
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

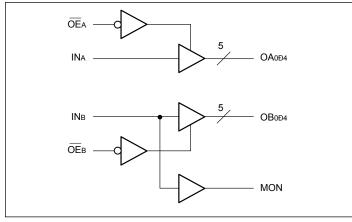
https://www.diodes.com/quality/product-definitions/

- → Packaging (Pb-free & Green available):
 - 20-pin, 150-mil wide QSOP (Q)
 - 20-pin, 209-mil wide SSOP (H)

Description

Diodes' PI49FCT3805D is composed of non-inverting drivers. The outputs are configured into 2 groups of one-in, five-out with independent output enable. Group B has an extra MON output. Excellent output signals to power and ground ratio minimize power and ground noise and also improves output performance.

Block Diagram



Truth Table⁽¹⁾

Inputs		Outputs		
\overline{OE}_{X}	IN_X	OA _X MO		
L	L	L	L	
L	Н	Н	Н	
Н	L	Z	L	
Н	Н	Z	Н	

Note:

1. H = High Voltage Level, L = Low Voltage Level, Z = High Impedance

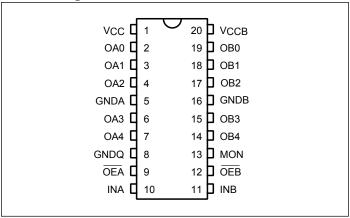
Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.





Pin Configuration



Pin Description

Pin Name	Description	
\overline{OE}_X	Hi-Z State Output Enable Inputs (Active Low)	
IN_X	Clock Inputs	
OA _N , OB _N	Clock Outputs	
MON	Monitor Output	
GND	Ground	
V _{CC}	Power	





Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature—65°C to +150°C
Ambient Temperature with Power Applied—40°C to +85°C
Input Voltage to GND Potential (Inputs & V_{CC} Only) $-0.5V$ to $5.5V$
Output Voltage to GND Potential (Outputs & I/O Only)–0.5V to $+V_{CC}$ +0.5V
V_{CC} Input Voltage $-0.5V$ to $+4.6V$

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics ($T_A = -40^{\circ}\text{C to} + 85^{\circ}\text{C}$, $V_{CC} = 3.3\text{V} \pm 0.3\text{V}$)

Symbol	Parameters	Test Conditions ⁽¹⁾	Min.	Тур.	Max.	Units
V _{OH}	Output High Voltage V_{CC} = Min., V_{IN} = V_{IL} or V_{IH}	$I_{OH} = -0.1 \text{mA}$ $I_{OH} = -8 \text{mA}$ $I_{OH} = -12 \text{mA}$	V _{CC} -0.2 2.4 ⁽³⁾ 2.4 ⁽³⁾	3.0 3.0		
V _{OL}		$I_{OH} = 0.1 \text{mA}$ $I_{OH} = 8 \text{mA}$ $I_{OH} = 12 \text{mA}$		0.2 0.3	0.2 0.4 0.4	V
V _{IH}	Input High Voltage	Low Logic	2.0		5.5	
$V_{\rm IL}$	Input Low Voltage	High Logic	-0.5		0.8	
I_{IH}	Input High Current	$V_{CC} = Max., V_{IN} = 5.5V$			1	
I _{IL}	Input Low Current	V _{CC} = Max., V _{IN} = GND			-1	μΑ
I _{OZH} I _{OZL}	High Impedance output current	$egin{array}{ll} V_{CC} = Max., all & V_{OUT} = V_{CC} \\ outputs \ disabled & V_{OUT} = GND \\ \end{array}$			1 -1	pri i
V _{IK}	Clamp Diode Voltage	V _{CC} = Min., I _{IN} = -18mA		-0.7	-1.2	V
I _{ODH}	Output High Current ^(4, 5)	$V_{OUT} = 1.5V$, $V_{IN} = V_{IL}$ or V_{IH} , $V_{CC} = 3.3V$	-40	-74	-100	
I _{ODL}	Output Low Current ^(4, 5)	V_{OUT} = 1.5V, V_{IN} = V_{IL} or V_{IH} , V_{CC} = 3.3V	50	90	130	mA
I _{OS}	Short Circuit Current ^(4, 5)	V _{CC} = Max., V _{OUT} = GND	-60	-100	-120	

Notes:

- 1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- 2. Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient and maximum loading.
- 3. $V_{OH} = V_{CC} 0.6V$ at rated current.
- 4. This parameter is determined by device characterization but is not production tested.
- 5. Not more than one output should be shorted at one time. Duration of the test should not exceed one second.

Capacitance ($T_A = 25$ °C, f = 1 MHz)

Parameters ⁽¹⁾	Description	Test Conditions	Тур	Max.	Units
C _{IN}	Input Capacitance	$V_{IN} = 0V$	3.0	4	T
C _{OUT}	Output Capacitance	$V_{OUT} = 0V$	_	6	pF

Note:

This parameter is determined by device characterization but is not production tested.





Power Supply Characteristics

Parameters	Description	Test Co	onditions ⁽¹⁾	Min.	Typ. (2)	Max.	Units
I_{CC}	Quiescent Power Supply Current	V _{CC} = Max.	$V_{IN} = GND \text{ or } V_{DD}$		0.1	30	
I_{DD}	Dynamic Supply Current per Output	$V_{CC} = 3.6V,$ $C_L = 15pF,$ All Outputs Toggling			80	120	μΑ
	$V_{CC} = 3.6V,$ $C_{L} = 15pF,$	$V_{IN} = V_{CC}$ or GND		100	135		
	Total Power Supply	All Outputs Toggling, f _i = 133 MHz	$V_{IN} = V_{CC}$ -0.6V or GND		100	135	mA/
I _C	Current	$V_{CC} = 3.6V,$ $C_{L} = 15pF,$	$V_{IN} = V_{CC}$ or GND		120	160	MHz
	All Outputs Toggling, $f_i = 166 \text{ MHz}$	$V_{\rm IN} = V_{\rm CC}$ -0.6V or GND		120	160		
ΔI_{CC}	Supply Current per inputs @ TTL High	V _{CC} = Max.	$V_{IN} = V_{CC} - 0.6V^{(3)}$		45	300	μΑ

4

Notes:

- For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device. 1.
- Typical values are at $V_{CC} = 3.3V$, $+25^{\circ}C$ ambient. 2.
- Per TTL driven input ($V_{IN} = V_{CC} 0.6V$); all other inputs at V_{CC} or GND.





Switching Characteristics over Operating Range

D	December 1	m . c . t (1)	3805D	TT. *4 .
Parameters	Description	Test Conditions ⁽¹⁾	Max.	Units
t _{PLH} t _{PHL}	Propagation Delay IN_N to O_N			ns
t _R /t _F	CLKn Rist/Fall Time 0.8V ~ 2.0V		1.5	ns
$t_{SK(o)}^{(3)}$	Pulse Skew	$C_{L} = 15pF,$	270	
$t_{SK(p)}^{(3)}$	Output Skew	133 MHz (3805D)	270	ps
$t_{SK(t)}^{(3)}$	Package Skew		550	
t_{ZL} , t_{ZH} , t_{LZ} , t_{HZ}	Enable/Disable Time		5.2	ns
F _{MAX}	Input Frequency		133	MHz

Note:

- 1. These parameters are guaranteed by design
- 2. Series Resistor loading = 33Ω (See Test Circuit)

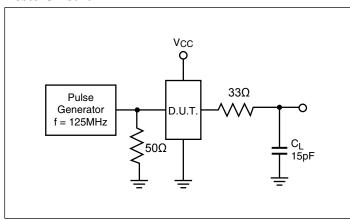
Switch Position

Test	Switch
Disable LOW Enable LOW	6V
Disable HIGH Enable HIGH	GND
All Other Inputs	Open

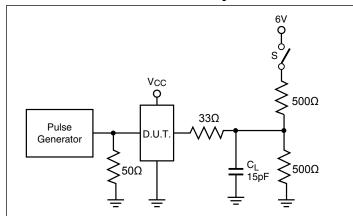
Definitions:

- 1. C_L = Load capacitance: includes jig and probe capacitance.
- 2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator.

Tests Circuit



Enable/Disable Time Test Set-Up

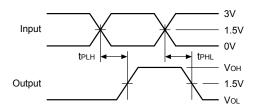




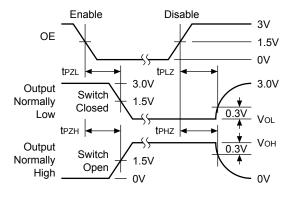


Switching Waveforms

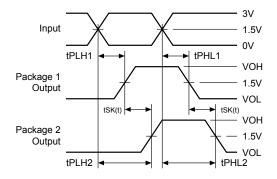
Propagation Delay



Enable and Disable Times

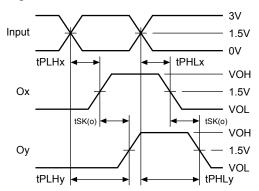


Package Skew - tsk(t)



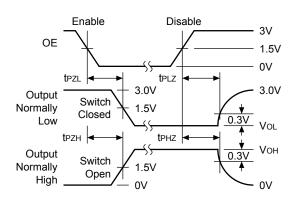
tSK(t) = | tPLH2 D tPLH1 | or | tPHL2 D tPHL1 |

Output Skew - tsk(o)



tSK(o) = | tPLHy D tPLHx | or | tPHLy D tPHLx |

Pulse Skew – tsk(p)







Part Marking H Package



YY: Year WW: Workweek 1st X: Assembly Code 2nd X: Fab Code

Q Package



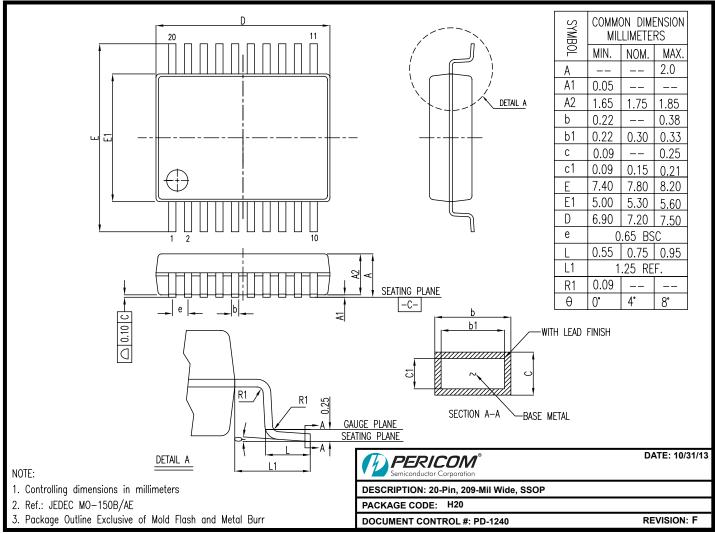
YY: Year WW: Workweek

1st X: Assembly Code 2nd X: Fab Code





Packaging Mechanical: 20-SSOP (H)

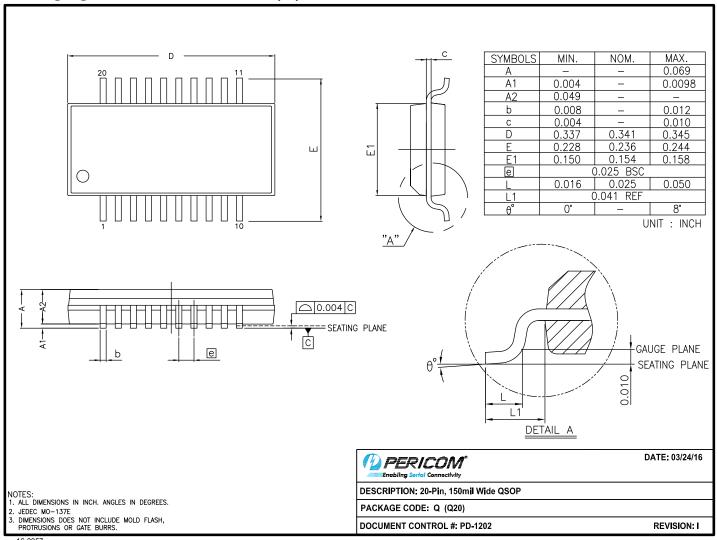


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Packaging Mechanical: 20-QSOP (Q)



For latest package info.

 $please\ check:\ http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/packaging-mechanical-and-thermal-characteristics/packaging-mech$

Ordering Information

Ordering Code	Package Code	Package Description
PI49FCT3805DHEX	Н	20-pin, 209-mil Wide (SSOP)
PI49FCT3805DQEX	Q	20-pin, 150-mil Wide (QSOP)

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- $2. \ \ See \ https://www.diodes.com/quality/lead-free/\ for\ more\ information\ about\ Diodes\ Incorporated's\ definitions\ of\ Halogen-\ and\ Antimony-free,\ "Green"\ and\ Lead-free.$
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. E = Pb-free and Green
- 5. X suffix = Tape/Reel





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