PI6C10806BLEX Datasheet

DiGi Electronics Part Number	PI6C10806BLEX-DG
Manufacturer	Diodes Incorporated
Manufacturer Product Number	PI6C10806BLEX
Description	IC CLK BUFFER 1:6 160MHZ 16TSSOP
Detailed Description	Clock Fanout Buffer (Distribution) IC 1:6 160 MHz 16 -TSSOP (0.173", 4.40mm Width)

https://www.DiGi-Electronics.com

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PI6C10806BLEX	Diodes Incorporated
Series:	Product Status:
-	Active
Type:	Number of Circuits:
Fanout Buffer (Distribution)	1
Ratio - Input:Output:	Differential - Input:Output:
1:6	No/No
Input:	Output:
Crystal	LVCMOS
Frequency - Max:	Voltage - Supply:
160 MHz	1.425V ~ 3.465V
Operating Temperature:	Mounting Type:
-40°C ~ 85°C	Surface Mount
Package / Case:	Supplier Device Package:
16-TSSOP (0.173", 4.40mm Width)	16-TSSOP
Base Product Number:	
PI6C10806	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	

A Product Line of
Diodes Incorporated

PI6C10806B

1.5V/1.8V/2.5V/3.3V, 160MHz, Low Skew 1:6 Crystal to LVCMOS Clock Buffer

Features

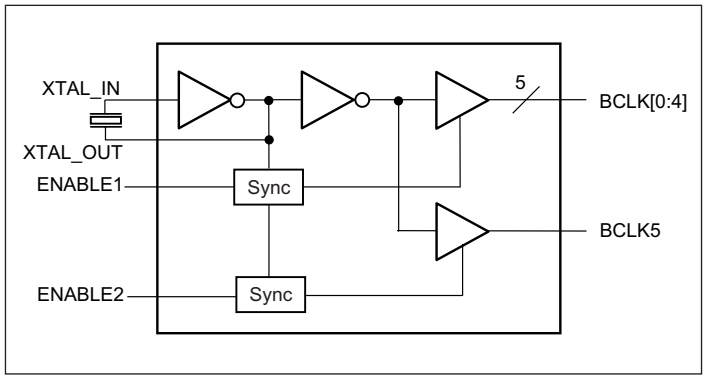
- Six low skew outputs: < 80ps
- Crystal oscillator input: 10MHz to 50MHz
- Switching frequency up to 160 MHz
- Fast output rise/fall time (down to 1.8V): < 800ps
- Synchronous output enables
- 1.5V, 1.8V, 2.5V, and 3.3V operation
- Industrial Temperature range: –40°C to +85°C
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.
  https://www.diodes.com/quality/product-definitions/
- Packaging (Pb-free & Green available):
  - 16-pin, 173-mil wide TSSOP (L)

Description

Diodes' PI6C10806B is a low skew, six output, crystal oscillator driver. The device's input range is from 10MHz to 50MHz. If the XTAL_IN is driven with a signal source, then the input frequency can be as high as 160MHz. The outputs are configured into two groups: a five output and a single output, each with an independent output enable.

The PI6C10806B has a wide range of operating voltages: 1.5V, 1.8V, 2.5V, and 3.3V. This feature, paired with the low output-to-output and part-to-part skew, makes the device ideal for low voltage, low power, high frequency, single ended applications; such as in networking.

Block Diagram



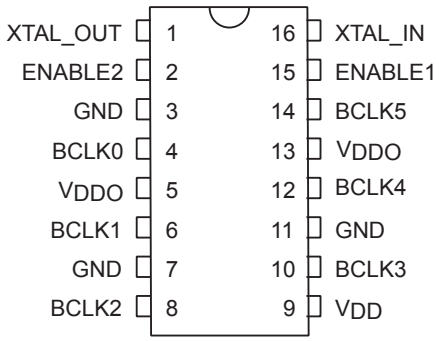
Truth Table(1)

Inputs		Outputs	
ENABLE1	ENABLE2	BCLK[0:4]	BCLK5
L	L	L	L
L	H	L	Switching
H	L	Switching	L
H	H	Switching	Switching

Note:
1. H = High Voltage Level, L = Low Voltage Level

Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

Pin Configuration



Pin Description

Pin#	Pin Name	Description
15, 2	ENABLE1, ENABLE2	Active High Output Enable Inputs
16	XTAL_IN	Crystal interface
1	XTAL_OUT	Crystal interface
4, 6, 8, 10, 12, 14	BCLK[0:5]	Clock Outputs
3, 7, 11	GND	Ground
9	V_{DD}	Core Power
5, 13	V_{DDO}	Output Power

Absolute Maximum Ratings (Above which the useful life may be impaired. For user guidelines only, not tested.)

Storage Temperature.....–65°C to +150°C
V_{DD}, V_{DDO} Voltage .....–0.5V to +3.6V
Output Voltage (max. 4.6V) ..... –0.5V to V_{DD}+0.5V
Input Voltage (max 4.6V)..... –0.5V to V_{DD}+0.5V
Junction Temperature ..... Max. 125°C

Note:
Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Power Supply DC Characteristics (V_{DD}/V_{DDO} = 3.3V ± 5%, T_A = -40°C to 85°C)

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
V_{DD}	Core Supply Voltage		3.135	3.3	3.465	V
V_{DDO}	Output Supply Voltage		3.135	3.3	3.465	V
I_{DD}	Power Supply Current	ENABLE1:2 = '00'			10	mA
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'			5	mA

Power Supply DC Characteristics (V_{DD}/V_{DDO} = 2.5V ± 5%, T_A = -40°C to 85°C)

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
V_{DD}	Core Supply Voltage		2.375	2.5	2.625	V
V_{DDO}	Output Supply Voltage		2.375	2.5	2.625	V
I_{DD}	Power Supply Current	ENABLE1:2 = '00'			8	mA
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'			4	mA

Power Supply DC Characteristics (V_{DD}/V_{DDO} = 1.8V ± 0.2V, T_A = -40°C to 85°C)

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
V_{DD}	Core Supply Voltage		1.6	1.8	2.0	V
V_{DDO}	Output Supply Voltage		1.6	1.8	2.0	V
I_{DD}	Power Supply Current	ENABLE1:2 = '00'			5	mA
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'			3	mA

Power Supply DC Characteristics (V_{DD}/V_{DDO} = 1.5V ± 5%, T_A = -40°C to 85°C)

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
V_{DD}	Core Supply Voltage		1.425	1.5	1.575	V
V_{DDO}	Output Supply Voltage		1.425	1.5	1.575	V
I_{DD}	Power Supply Current	ENABLE1:2 = '00'			5	mA
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'			3	mA

Power Supply DC Characteristics (V_{DD} = 3.3V ± 5%, T_A = -40°C to 85°C)

Symbols	Parameters		Test Conditions	Min.	Typ	Max.	Units
I_{DD}	Power Supply Current	ENABLE1:2 = '00'	V_{DDO} = 2.5V ± 5%			10	mA
			V_{DDO} = 1.8V ± 0.2V				
			V_{DDO} = 1.5V ± 5%				

Power Supply DC Characteristics Cont.

Symbols	Parameters		Test Conditions	Min.	Typ	Max.	Units
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'	V_{DDO} = 2.5V ± 5%			4	mA
			V_{DDO} = 1.8V ± 0.2V			3	
			V_{DDO} = 1.5V ± 5%			3	

Power Supply DC Characteristics (V_{DD} = 2.5V ± 5%, T_A = -40°C to 85°C)

Symbols	Parameters		Test Conditions	Min.	Typ	Max.	Units
I_{DD}	Power Supply Current	ENABLE1:2 = '00'	V_{DDO} = 1.8V ± 0.2V			8	mA
			V_{DDO} = 1.5V ± 5%				
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'	V_{DDO} = 1.8V ± 0.2V			3	
			V_{DDO} = 1.5V ± 5%				

Power Supply DC Characteristics (V_{DD} = 1.8V ± 0.2V, T_A = -40°C to 85°C)

Symbols	Parameters		Test Conditions	Min.	Typ	Max.	Units
I_{DD}	Power Supply Current	ENABLE1:2 = '00'	V_{DDO} = 1.5V ± 5%			5	mA
I_{DDO}	Output Supply Current	ENABLE1:2 = '00'	V_{DDO} = 1.5V ± 5%			3	

I/O DC Characteristics (T_A = -40°C to 85°C)

Symbols	Parameters		Test Conditions	Min.	Typ	Max.	Units
V_{IH}	Input High Voltage	ENABLE 1, ENABLE 2	V_{DD} = 3.3V ± 5%	2		V_{DDO} + 0.3	V
			V_{DD} = 2.5V ± 5%	1.7		V_{DDO} + 0.3	V
			V_{DD} = 1.8V ± 0.2V	0.65* V_{DDO}		V_{DDO} + 0.3	V
			V_{DD} = 1.5V ± 5%	0.65* V_{DDO}		V_{DDO} + 0.3	V
V_{IL}	Input Low Voltage	ENABLE 1, ENABLE 2	V_{DD} = 3.3V ± 5%	-0.3		0.8	V
			V_{DD} = 2.5V ± 5%	-0.3		0.7	V
			V_{DD} = 1.8V ± 0.2V	-0.3		0.35* V_{DDO}	V
			V_{DD} = 1.5V ± 5%	-0.3		0.35* V_{DDO}	V
V_{OH}	Output High Voltage		V_{DDO} = 3.3V ± 5% (1)	2.6			V
			V_{DDO} = 2.5V ± 5%; I_{OH} = -1mA	2			V
			V_{DDO} = 2.5V ± 5% (1)	1.8			V
			V_{DDO} = 1.8V ± 0.2V(1)	V_{DDO} - 0.3			V
			V_{DDO} = 1.5V ± 5% (1)	V_{DDO} - 0.3			V

I/O DC Characteristics Cont.

Symbols	Parameters	Test Conditions	Min.	Typ	Max.	Units
V_{OL}	Output Low Voltage	V_{DD} = 3.3V ± 5% (1)			0.5	V
		V_{DDO} = 2.5V ± 5%; I_{OL} = 1mA			0.4	V
		V_{DDO} = 2.5V ± 5% (1)			0.45	V
		V_{DDO} = 1.8V ± 0.2V (1)			0.35	V
		V_{DDO} = 1.5V ± 5% (1)			0.3	V
R_{OUT}	Output Impedance	V_{DDO} = 3.3 V		7		Ω
		V_{DDO} = 2.5 V		8		Ω
		V_{DDO} = 1.8 V		13		Ω
		V_{DDO} = 1.5 V		20		Ω

Notes: 1. I_{OH} = -8mA, I_{OL} = 8mA.

AC Characteristics (Over Operating Range: V_{DD} = 3.3V ± 5%, T_A = -40° to 85°C)

Parameters	Description	Test Conditions(1)		Min.	Typ	Max.	Units
f_{OUT}	Output Frequency	Using Crystal		10		50	MHz
		External Clock(2)	V_{DDO} = 3.3V±5%	0		160	
			V_{DDO} = 2.5V±5%				
			V_{DDO} = 1.8V±0.2V				
			V_{DDO} = 1.5V±5%	0		100	
t_{DC}	Output Duty Cycle	@ V_{DDO}/2	V_{DDO} = 3.3V±5%	47		53	%
			V_{DDO} = 2.5V±5%				
			V_{DDO} = 1.8V±0.2V				
			V_{DDO} = 1.5V±5%	45		55	
t_R/t_F	CLKn Rise/Fall Time	20% to 80%	V_{DDO} = 3.3V±5%	150		800	ps
			V_{DDO} = 2.5V±5%	200		800	
			V_{DDO} = 1.8V±0.2V	200		800	
			V_{DDO} = 1.5V±5%	600		1300	
RMS	Random RMS Phase Jitter	25MHz @ Integration Range 100Hz - 1MHz	V_{DDO} = 3.3V±5%		0.098		ps
			V_{DDO} = 2.5V±5%		0.112		
			V_{DDO} = 1.8V±0.2V		0.233		
			V_{DDO} = 1.5V±5%		0.277		
$t_{SK(O)}$(3)	Output to Output Skew between any two outputs of the same device @ same transition	@V_{DDO}/2				80	ps
t_{DIS},t_{EN}(4)	Output Enable/Disable	@V_{DDO}/2				4	cycles

Notes:

1. Unless noted otherwise, all parameters are tested with xtal @ f <= Fxtal_max,; outputs are terminated @ 50Ω to V_{DDO}/2, see waveforms.
2. External clock source is driving XTAL_IN input
3. Identical conditions: loading, transitions, supply voltage, temperature, package type and speed grade.
4. These parameters are guaranteed, but not tested. Max delay is 4 cycles. Min. setup time = 3ns.

AC Characteristics (V_{DD} = 2.5V±5%, T_A = -40°C to 85°C)

Parameters	Description	Test Conditions(1)		Min.	Typ	Max.	Units
f_{OUT}	Output Frequency	Using Crystal		10		50	MHz
		External Clock(2)	V_{DDO} = 2.5V±5%	0		160	
			V_{DDO} = 1.8V±0.2V	0		160	
			V_{DDO} = 1.5V±5%	0		100	
t_{DC}	Output Duty Cycle	@ V_{DDO}/2	V_{DDO} = 2.5V±5%	47		53	%
			V_{DDO} = 1.8V±0.2V				
			V_{DDO} = 1.5V±5%	45		55	
t_R/t_F	CLKn Rise/Fall Time	20% to 80%	V_{DDO} = 2.5V±5%	150		800	ps
			V_{DDO} = 1.8V±0.2V	200		900	
			V_{DDO} = 1.5V±5%	700		1400	
RMS	Random RMS Phase Jitter	25MHz @ Integration Range 100Hz - 1MHz	V_{DDO} = 2.5V±5%		0.112		ps
			V_{DDO} = 1.8V±0.2V		0.233		
			V_{DDO} = 1.5V±5%		0.277		
$t_{SK(O)}$(3)	Output to Output Skew between any two outputs of the same device @ same transition	@V_{DDO}/2				80	ps
t_{DIS},t_{EN}(4)	Output Enable/Disable	@V_{DDO}/2				4	cycles

Notes:

1. Unless noted otherwise, all parameters are tested with xtal @ f <= Fxtal_max,; outputs are terminated @ 50Ω to V_{DDO}/2, see waveforms.
2. External clock source is driving XTAL_IN input
3. Identical conditions: loading, transitions, supply voltage, temperature, package type and speed grade.
4. These parameters are guaranteed, but not tested. Max delay is 4 cycles. Min. setup time = 3ns.

AC Characteristics (V_{DD} = 1.8V±0.2V, T_A = -40°C to 85°C)

Parameters	Description	Test Conditions(1)		Min.	Typ	Max.	Units
f_{OUT}	Output Frequency	Using Crystal		10		50	MHz
		External Clock(2)	V_{DDO} = 1.8V±0.2V	0		160	
			V_{DDO} = 1.5V±5%	0		100	
t_{DC}	Output Duty Cycle	@ V_{DDO}/2	V_{DDO} = 1.8V±0.2V	47		53	%
			V_{DDO} = 1.5V±5%	45		55	
t_R/t_F	CLKn Rise/Fall Time	20% to 80%	V_{DDO} = 1.8V±0.2V	150		800	ps
			V_{DDO} = 1.5V±5%	800		1500	
RMS	Random RMS Phase Jitter	25MHz @ Integration Range 100Hz - 1MHz	V_{DDO} = 1.8V±0.2V		0.233		ps
			V_{DDO} = 1.5V±5%		0.277		
$t_{SK(O)}$(3)	Output to Output Skew between any two outputs of the same device @ same transition	@V_{DDO}/2				80	ps
t_{DIS},t_{EN}(4)	Output Enable/Disable	@V_{DDO}/2				4	cycles

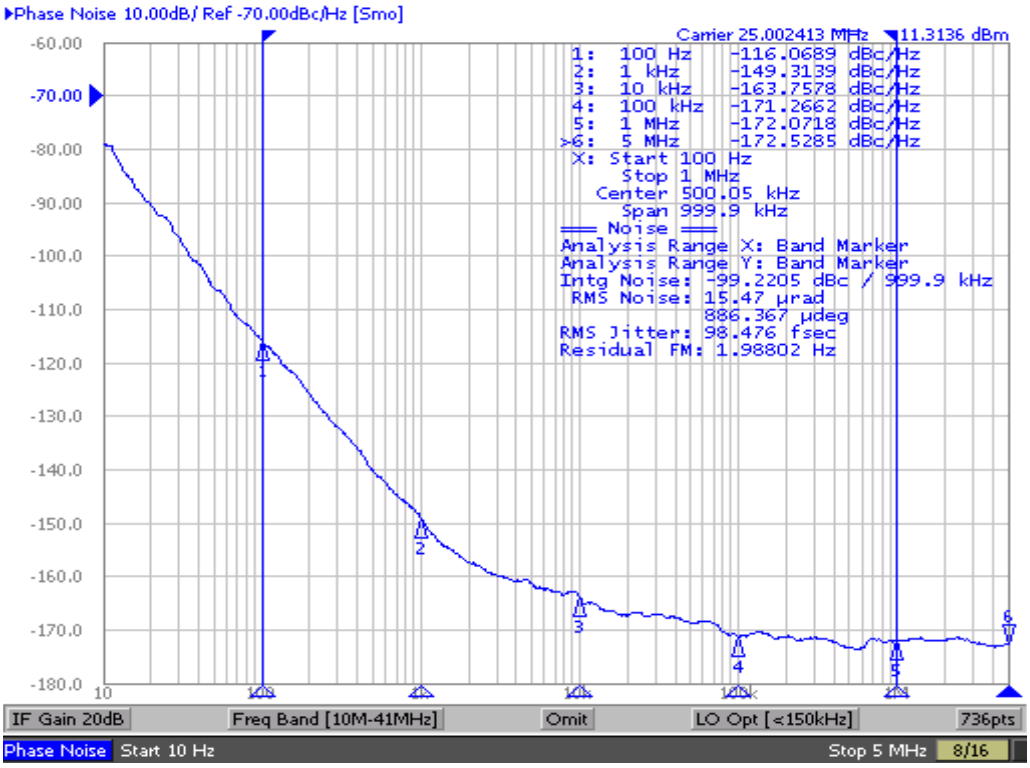
Notes:
All parameters measured at f=f_{MAX} using a crystal input unless noted otherwise.
Outputs are terminated at 50Ω to V_{DDO} /2.
1. XTAL_IN can be overdriven relatively to a signal a crystal provides.
2. Defined as skew between outputs at the same supply voltage and with equal load conditions. Measured at V_{DDO} /2.
3. These parameters are guaranteed, but not tested.
4. This parameter is defined in accordance with JEDEC Standard 65.

AC Characteristics (V_{DD} = 1.5V±5%, T_A = -40°C to 85°C)

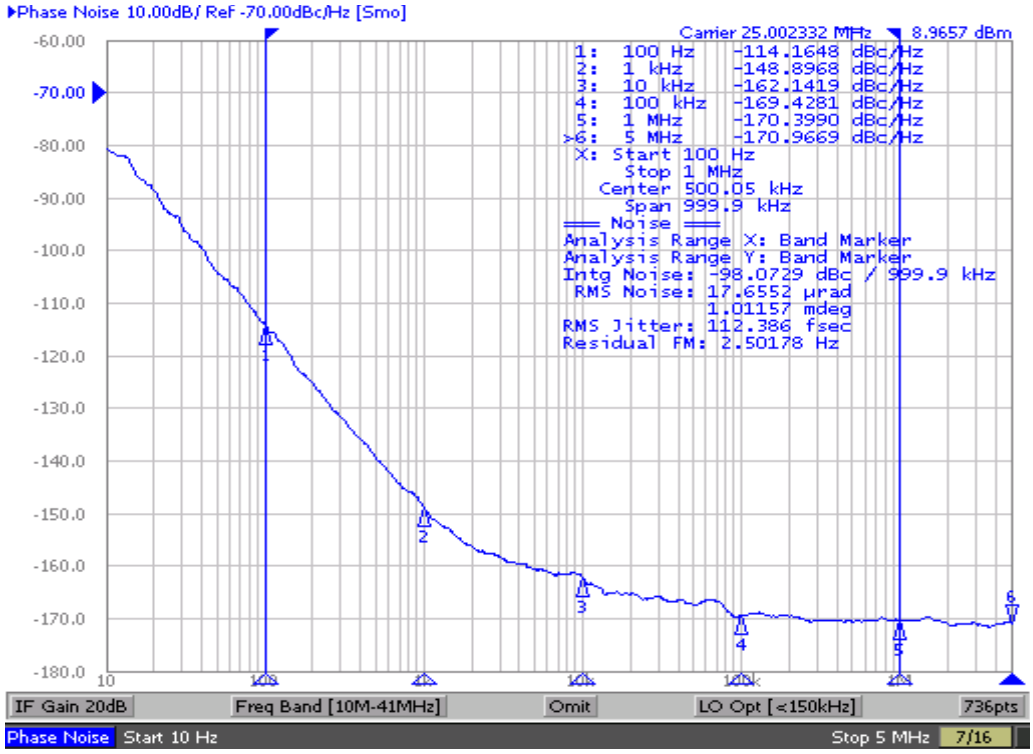
Parameters	Description	Test Conditions(1)		Min.	Typ	Max.	Units
f_{OUT}	Output Frequency	Using Crystal		10		50	MHz
		External Clock(2)		0		100	
t_{DC}	Output Duty Cycle	@ V_{DDO}/2		45		55	%
t_R/t_F	CLKn Rise/Fall Time	20% to 80%	V_{DDO} = 1.5V±5%	800		1500	ps
RMS	Random RMS Phase Jitter	25MHz @ Integration Range 100Hz - 1MHz	V_{DDO} = 1.5V±5%		0.277		ps
$t_{SK(O)}$(3)	Output to Output Skew between any two outputs of the same device @ same transition	@V_{DDO}/2				80	ps
t_{DIS},t_{EN}(4)	Output Enable/Disable	@V_{DDO}/2				4	cycles

Notes:
All parameters measured at f=f_{MAX} using a crystal input unless noted otherwise.
Outputs are terminated at 50Ω to V_{DDO} /2.
1. XTAL_IN can be overdriven relatively to a signal a crystal provides.
2. Defined as skew between outputs at the same supply voltage and with equal load conditions. Measured at V_{DDO} /2.
3. These parameters are guaranteed, but not tested.
4. This parameter is defined in accordance with JEDEC Standard 65.

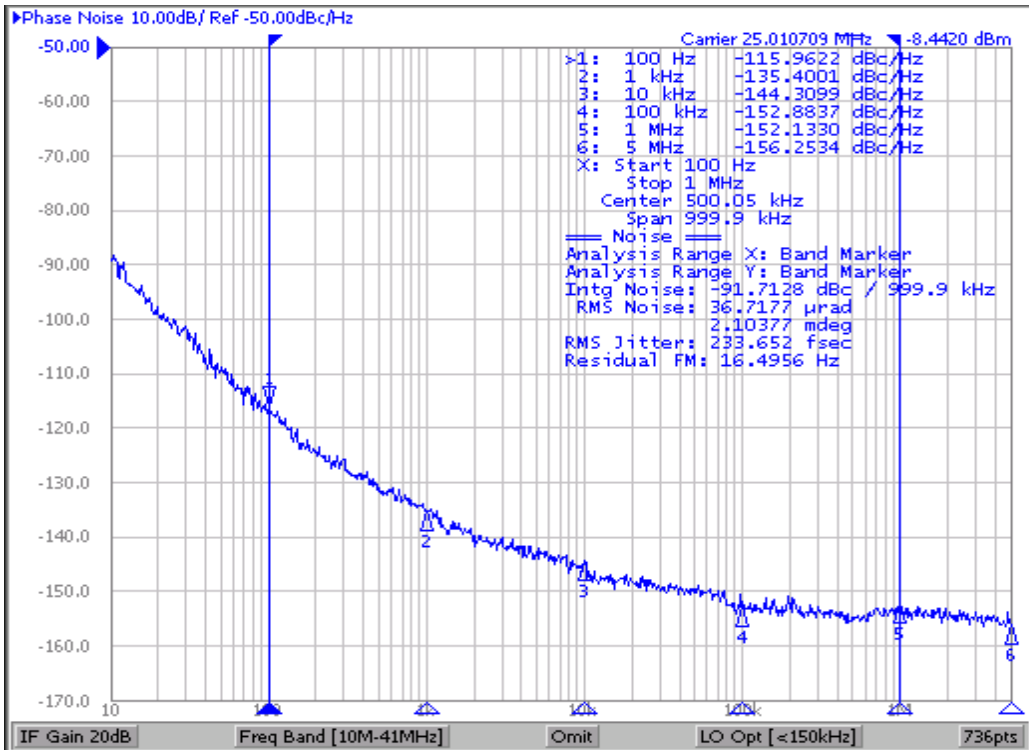
Jitter (typical phase noise at 25MHz)



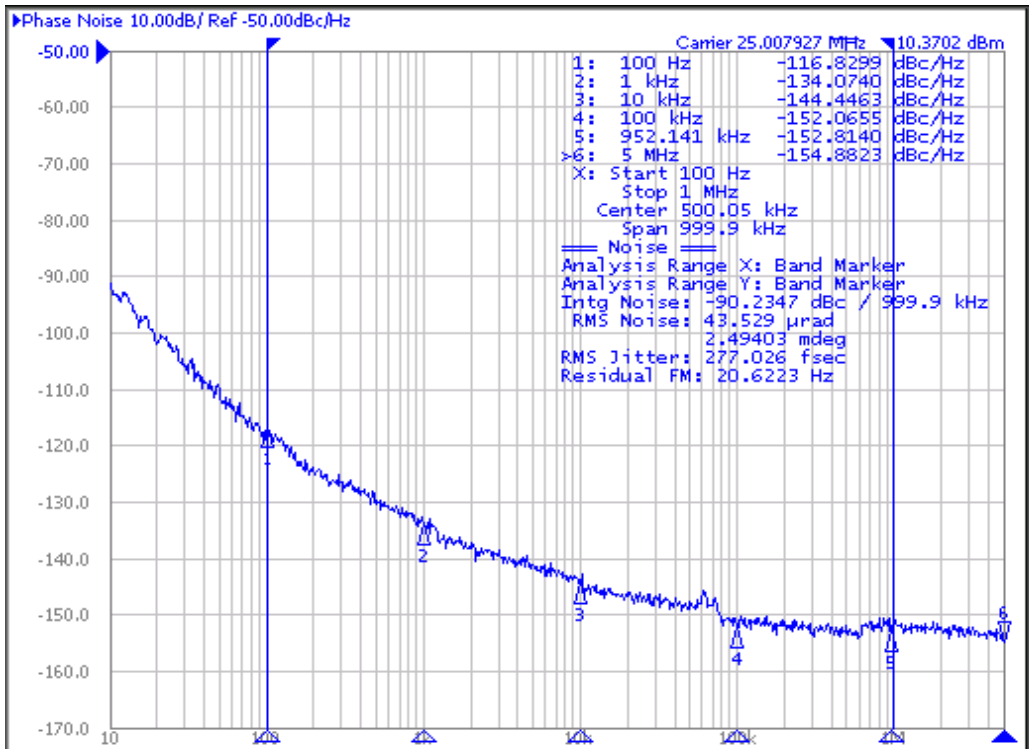
3.3V Core/3.3V Output
RMS phase jitter (Random)
100Hz to 1MHz =0.098ps (typical)



2.5V Core/2.5V Output
RMS phase jitter (Random)
100Hz to 1MHz =0.112ps (typical)



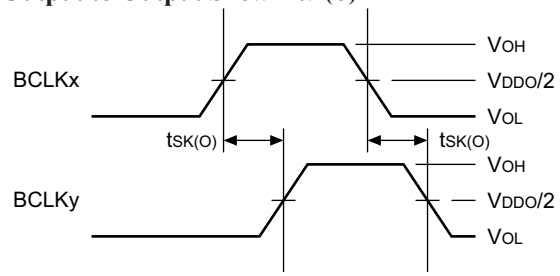
1.8V Core/1.8V Output
RS phase jitter (Random)
100Hz to 1MHz =0.233ps (typical)



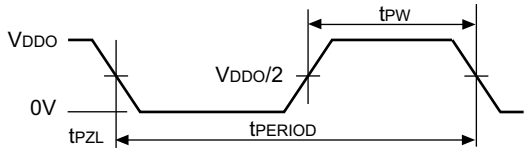
1.5V Core/1.5V Output
RMS phase jitter (Random)
100Hz to 1MHz =0.277ps (typical)

Waveforms

Output to Output Skew – $t_{SK(O)}$

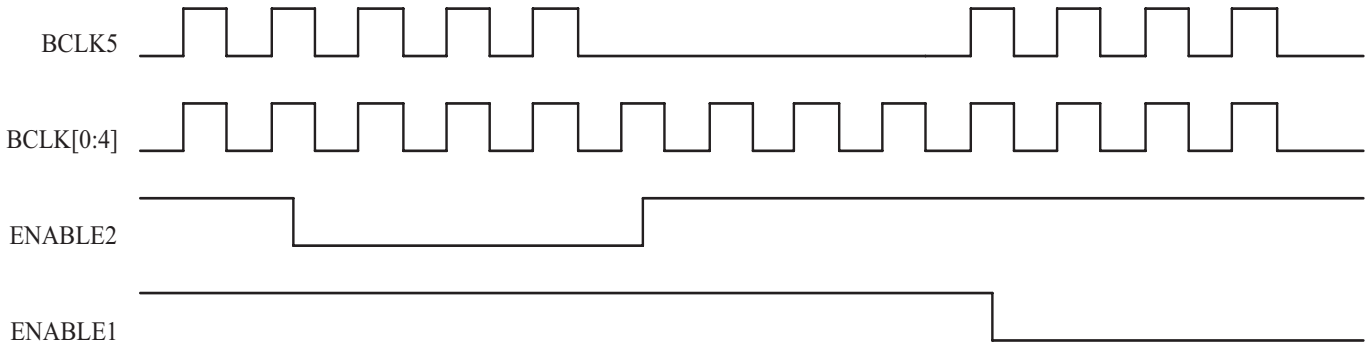


Duty Cycle – t_{DC}

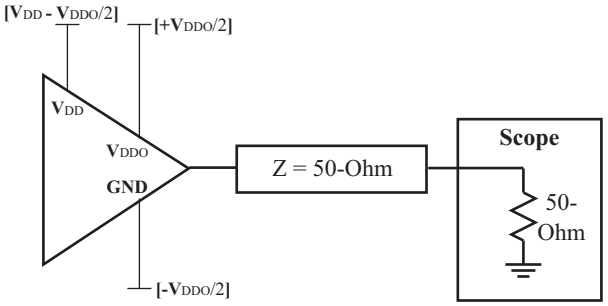


$t_{DC} = (t_{PW} / t_{PERIOD}) \times 100\%$

ENABLE1, ENABLE2 Timing Diagram



AC Test Circuit Load



Note:
V_{DD}/V_{DDO} = 1.5V ± 5%,
1.8V ± 0.2V,
2.5V ± 5%,
3.3V ± 5%

Crystal Characteristic

Parameters	Description	Min	Typ	Max.	Units
OSC_{MODE}	Mode of Oscillation		Fundamental		
FREQ	Frequency	10	25	50	MHz
ESR(1)	Equivalent Series Resistance	30		50	Ohm
C_{LOAD}	Load Capacitance		18		pF
C_{SHUNT}	Shunt Capacitance			7	pF
DRIVE level				1	mW

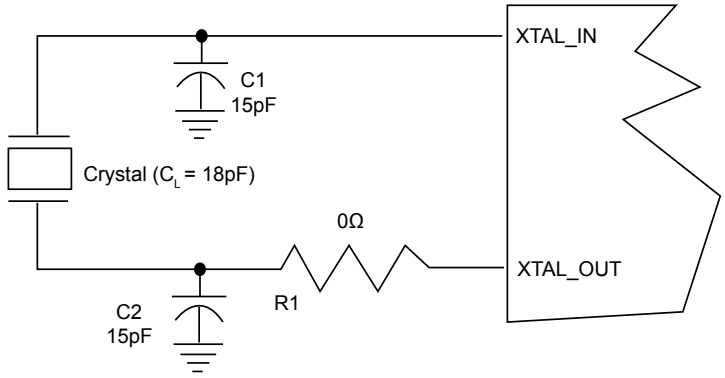
Note: 1. ESR value is dependent upon frequency of oscillation

Application Notes

Crystal Circuit Connection

The following diagram shows PI6C10806B crystal circuit connection with a parallel crystal. For the C_L=18pF crystal, it is suggested to use C1=15pF, C2=15pF. C1 and C2 can be adjusted to fine tune to the target ppm of crystal oscillator according to different board layouts. R1 is not recommended.

Crystal Oscillator Circuit



Part Marking

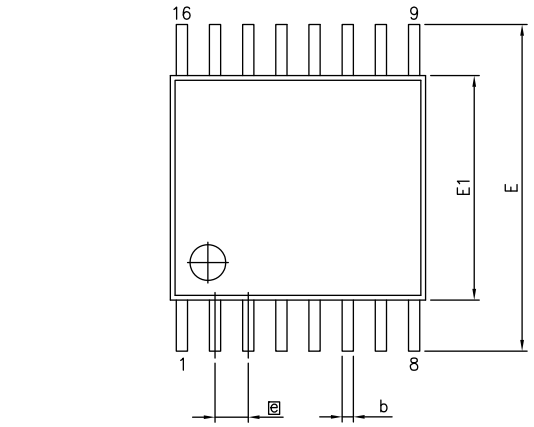
PI6C
10806BLE
ZYWXX̄

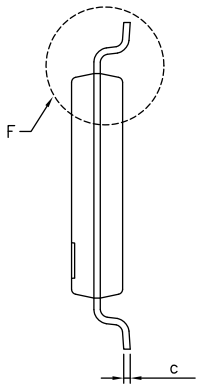
Z: Die Rev
Y: Year
W: Workweek
1st X: Assembly Code
2nd X: Fab Code

A Product Line of
Diodes Incorporated

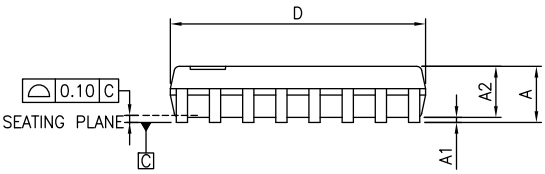
PI6C10806B

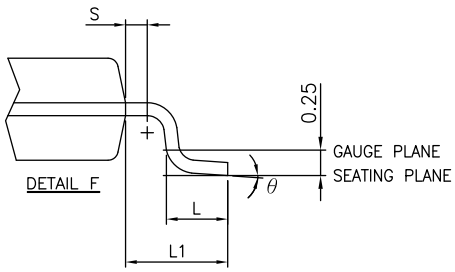
Packaging Mechanical: 16-TSSOP (L)





SYMBOLS	MIN.	NOM.	MAX.
A	–	–	1.20
A1	0.05	–	0.15
A2	0.80	1.00	1.05
b	0.19	–	0.30
c	0.09	–	0.20
D	4.90	5.00	5.10
E1	4.30	4.40	4.50
E	6.20	6.40	6.60
e	0.65 BSC		
L1	1.00 REF		
L	0.45	0.60	0.75
S	0.20	–	–
θ	0°	–	8°





NOTES:
1. ALL DIMENSIONS IN MILLIMETERS. ANGLES IN DEGREES.
2. JEDEC MO–153F
3. DIMENSIONS DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

PERICOM Enabling Serial Connectivity

DATE: 03/24/16

DESCRIPTION: 16-Pin, 173mil Wide TSSOP

PACKAGE CODE: L (L16)

DOCUMENT CONTROL #: PD-1310 REVISION: G

16-0061

For latest package info.
please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description
PI6C10806BLEX	L	16-Pin, 173mil Wide (TSSOP)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
4. E = Pb-free and Green
5. X suffix = Tape/Reel

A Product Line of
Diodes Incorporated

PI6C10806B

IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.

3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.

4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.

5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.

6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.

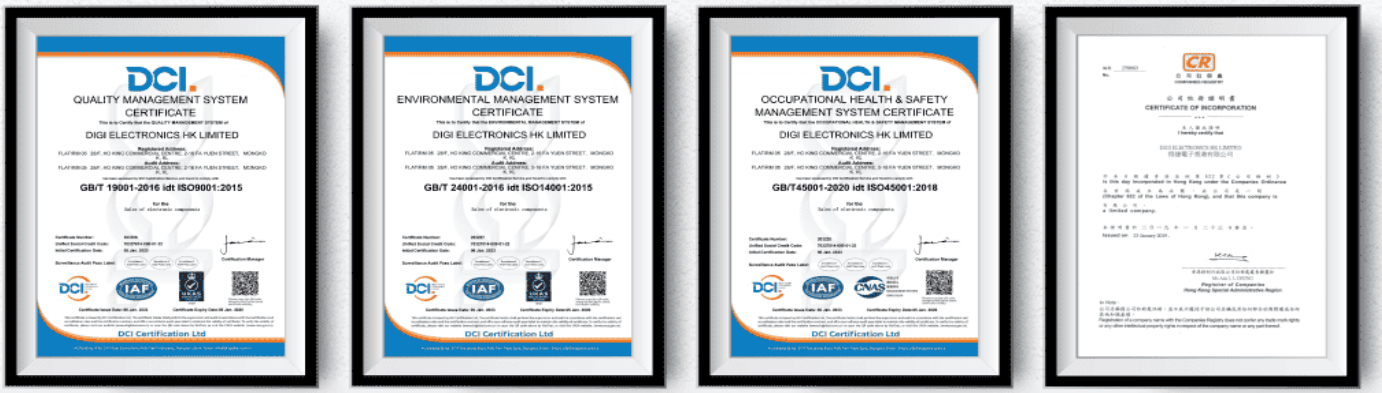
7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.

8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

www.diodes.com

OUR CERTIFICATE

DiGi provide top-quality products and perfect service for customer worldwide through standardization, technological innovation and continuous improvement. DiGi through third-party certification, we striciy control the quality of products and services. Welcome your RFQ to Email: Info@DiGi-Electronics.com



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.