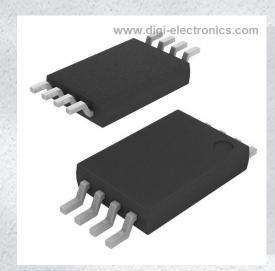


# PI6C2405A-1LEX Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number PI6C2405A-1LEX-DG

Manufacturer Diodes Incorporated

Manufacturer Product Number PI6C2405A-1LEX

Description IC ZD BUFFER 8TSSOP

Detailed Description Zero Delay Buffer IC 133MHz 1 8-TSSOP (0.173", 4.4

0mm Width)



Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

DiGi is a global authorized distributor of electronic components.



## **Purchase and inquiry**

Manufacturer Product Number:	Manufacturer:
PI6C2405A-1LEX	Diodes Incorporated
Series:	Product Status:
	Last Time Buy
DiGi-Electronics Programmable:	Type:
Not Verified	Zero Delay Buffer
PLL:	Input:
Yes	πι
Output:	Number of Circuits:
TTL	1
Ratio - Input:Output:	Differential - Input:Output:
1:5	No/No
1:5	No/No
1:5 Frequency - Max:	No/No Divider/Multiplier:
1:5 Frequency - Max: 133MHz	No/No Divider/Multiplier: No/No
1:5 Frequency - Max: 133MHz Voltage - Supply:	No/No Divider/Multiplier: No/No Operating Temperature:
1:5 Frequency - Max:  133MHz  Voltage - Supply:  3V ~ 3.6V	No/No Divider/Multiplier: No/No Operating Temperature: 0°C ~ 70°C
1:5 Frequency - Max: 133MHz Voltage - Supply: 3V ~ 3.6V Mounting Type:	No/No Divider/Multiplier: No/No Operating Temperature: 0°C ~ 70°C Package / Case:

## **Environmental & Export classification**

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	

8542.39.0001





**Zero-Delay Clock Buffer** 

#### **Features**

- → Maximum rated frequency: 133 MHz
- → Low cycle-to-cycle jitter
- → Input to output delay, less than 300ps
- → Internal feedback allows outputs to be synchronized to the clock input
- → 5V tolerant input\*
- → Spread spectrum clock ready
- → Operates at 3.3V V<sub>DD</sub>
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- → For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

- → Packaging (Pb-free & Green available):
  - -8-pin, 150-mil SOIC (W)
  - -8-pin, 173-mil TSSOP (L)
- \* CLKIN must reference the same voltage thresholds for the PLL to deliver zero delay skewing

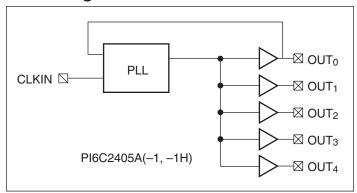
## **Description**

The PI6C2405A-1/PI6C2405A-1H is a PLL based, zero-delay buffer, with the ability to distribute five outputs of up to 133MHz at 3.3V. All the outputs are distributed from a single clock input CLKIN and output OUT0 performs zero delay by connecting a feedback to PLL.

An internal feedback on OUT0 is used to synchronize the outputs to the input; the relationship between loading of this signal and the outputs determines the input-output delay. PI6C2405A-1/PI6C2405A-1H is able to track spread spectrum clocking for EMI reduction. PI6C2405A-1/PI6C2405A-1H is characterized for both commercial and industrial operation.

PI6C2405A-1H is a high-drive version of PI6C2405A-1.

#### **Block Diagram**

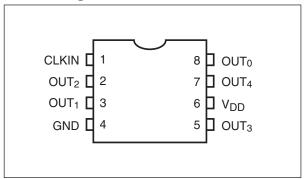


#### Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.



## **Pin Configuration**



#### **Pin Description**

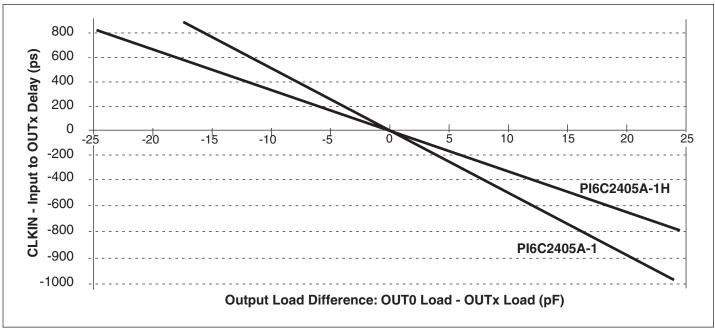
Pin#	Pin Name	Description	
1	CLKIN	nput clock reference frequency (weak pull-down)	
2, 3, 5, 7	OUT[1-4]	ock Outputs	
4	GND	Ground	
6	$V_{\mathrm{DD}}$	3.3V Supply	
8	OUT0	Clock output, internal PLL feedback (weak pull-down)	





#### **Zero Delay and Skew Control**

CLKIN Input to OUTx Delay vs. Difference in Loading between OUT0 pin and OUTx pins



The relationship between loading of the OUT0 signal and other outputs determines the input-output delay. Zero delay is achieved when all outputs, including feedback, are loaded equally.



#### **Maximum Ratings**

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature65°C to +150°	,C
Junction Temperature+125°C Ma	ıX.
Supply Voltage to Ground Potential0.5V to +4.6	
DC Input Voltage (Except CLKIN)0.5V to V <sub>DD</sub> +5.5	
ESD Protection (Input)	

#### Note:

Stresses greater than those listed under MAXIMUM RAT-INGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

#### **Operating Conditions** ( $V_{CC} = 3.3V \pm 0.3V$ )

Parameter	Description		Max.	Units
$V_{\mathrm{DD}}$	Supply Voltage		3.6	V
T	Commercial Operating Temperature	0	70	96
T <sub>A</sub>	Industrial Operating Temperature -40 85		85	°C
C	Load Capacitance, below 100 MHz		30	
$C_{\rm L}$	Load Capacitance, from 100 MHz to 133		15	pF
C <sub>IN</sub>	Input Capacitance 7			

#### **DC Electrical Characteristics for Industrial Temperature Devices**

Parameter	Description	Test Conditions		Max.	Units
$V_{\rm IL}$	Input LOW Voltage			0.8	V
$V_{\mathrm{IH}}$	Input HIGH Voltage		2.0		V
$I_{IL}$	Input LOW Current	$V_{IN} = 0V$		50	4
I <sub>IH</sub>	Input HIGH Current	$V_{IN} = V_{DD}$		125	μΑ
V <sub>OL</sub>	Output LOW Voltage	$I_{OL} = 8mA(-1); I_{OL} = 12mA(-1H)$		0.4	V
V <sub>OH</sub>	Output HIGH Voltage	$I_{OH} = -8mA(-1); I_{OH} = -12mA(-1H)$	2.4		V
т	Summly Cumment	Unloaded outputs 100 MHz, Select inputs at $\mathrm{V}_{\mathrm{DD}}$ or GND		54	A
$I_{\mathrm{DD}}$	Supply Current	Unloaded outputs 66 MHz, CLKIN		39	mA





## **AC Electrical Characteristics for Industrial Temperature Devices**

Parameter	Description	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
F <sub>O</sub> Output Frequency		30pF load	10		100	MIL
		15pF load			133	MHz
	D ( C 1 (1) (1)	Measured at V <sub>DD</sub> /2, F <sub>OUT</sub> < 66.67MHz, 30pF load	40		60	
	Duty Cycle <sup>(1)</sup> (-1)	Measured at V <sub>DD</sub> /2, F <sub>OUT</sub> < 45MHz 15pF load	45	50	55	0/
$t_{DC}$	D + C 1 (1) (111)	Measured at V <sub>DD</sub> /2, F <sub>OUT</sub> < 100MHz 15pF load	40	50	60	%
	Duty Cycle <sup>(1)</sup> (-1H)	Measured at V <sub>DD</sub> /2, F <sub>OUT</sub> < 45MHz 30pF load	45		55	
	D: T: (1)(1)	Measured between 0.8V and 2.0V, 30pF load			2.2	
$t_R$	Rise Time <sup>(1)</sup> (-1)	Measured between 0.8V and 2.0V, 15pF load			1.5	
	Rise Time <sup>(1)</sup> (-1H)	Measured between 0.8V and 2.0V, 30pF load			1.7	
	_ 11 (1) ( )	Measured between 0.8V and 2.0V, 30pF load			2.2	ns
$t_{\mathrm{F}}$	Fall Time <sup>(1)</sup> (-1)	Measured between 0.8V and 2.0V, 15pF load			1.5	
Fall Time <sup>(1)</sup> (-1H)		Measured between 0.8V and 2.0V, 30pF load			1.5	
t <sub>sk(o)</sub>	Output to Output skew (-1, -1H) <sup>(1)</sup>	All outputs equally loaded			200	
$t_0$	Delay, CLKIN Rising Edge to OUT0 Rising Edge <sup>(1)</sup>	Measured at $V_{\rm DD}/2$		0	±300	ps
t <sub>SK(D)</sub>	Device-to-device skew <sup>(1)</sup>	Measured at $V_{\rm DD}/2$ on OUT0 pins of device	Measured at $V_{DD}/2$ on OUT0 pins of device $0$		600	
t <sub>SLEW</sub>	Output slew rate (1)	Measured between 0.8V and 2.0V on -1H device using Test Circuit #2				V/ns
t <sub>JIT</sub>	Cycle-to-Cycle Jitter (-1, -1H)	Measured at 66.67 MHz, loaded 30pF load			200	ps
$t_{LOCK}$	PLL Lock time (1)	Stable power supply, valid clocks presented on CLKIN pin			1.0	ms

#### Notes:

#### **DC Electrical Characteristics for Commercial Temperature Devices**

Parameter	Description	Test Conditions		Max.	Units
$V_{IL}$	Input LOW Voltage			0.8	V
$V_{IH}$	Input HIGH Voltage		2.0		V
I <sub>IL</sub>	Input LOW Current	$V_{IN} = 0V$		50	4
$I_{IH}$	Input HIGH Current	$V_{IN} = V_{DD}$		125	μΑ
V <sub>OL</sub>	Output LOW Voltage	$I_{OL} = -8mA(-1); I_{OL} = 12mA(-1H)$		0.4	V
V <sub>OH</sub>	Output HIGH Voltage	$I_{OH} = -8mA(-1); I_{OH} = -12mA(-1H)$	2.4		V
т	C1 C	Unloaded outputs 100 MHz, Select inputs at $V_{\mathrm{DD}}$ or GND		54	A
$I_{DD}$	Supply Current	Unloaded outputs 66.67 MHz, select inputs at V <sub>DD</sub> or GND		39	mA

<sup>1.</sup> See Switching Waveforms on page 6.





## **AC Electrical Characteristics for Commercial Temperature Devices**

Parameter	Description	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
Г	Outrot Francisco	30pF load	10		100	MHz
F <sub>O</sub>	Output Frequency	15pF load 10			133	MHZ
	Duty Cycle <sup>(1)</sup> (-1)	Measured at $V_{DD}/2$ , $F_O < 66$ MHz, 30pF load	40	50	60	0/
$t_{DC}$	Duty Cycle <sup>(1)</sup> (-1H)	Measured at $V_{DD}/2$ , $F_O < 66$ MHz, 30pF load	45	50	55	- %
	Rise Time <sup>(1)</sup> @ 30pF				2.2	
$t_{R}$	Rise Time <sup>(1)</sup> @ 15pF	Measured between 0.8V and 2.0V			1.5	
	Rise Time <sup>(1)</sup> @ 30pF (-1H)				1.5	
	Fall Time <sup>(1)</sup> @ 30pF				2.2	ns
$t_{\mathrm{F}}$	Fall Time <sup>(1)</sup> @ 15pF	Measured between 0.8V and 2.0V			1.5	
	Fall Time <sup>(1)</sup> @ 30pF (-1H)	-			1.5	
t <sub>sk(o)</sub>	Output to Output skew (-1, -1H) <sup>(1)</sup>	All outputs equally loaded			200	
t <sub>0</sub>	Input to output delay, CLKIN Rising Edge to OUT0 Rising Edge <sup>(1)</sup>	Measured at $V_{\mathrm{DD}}/2$		0	±300	ps
t <sub>SK(D)</sub>	Device-to-device skew <sup>(1)</sup>	Measured at V <sub>DD</sub> /2 on OUT0 pins of device		0	600	
t <sub>SLEW</sub>	Output slew rate (1)	Measured between 0.8V and 2.0V on -1H device using Test Circuit #2				V/ns
t <sub>JIT</sub>	Cycle-to-Cycle Jitter (-1, -1H)	Measured at 66.67 MHz, loaded 30pF load			200	ps
t <sub>LOCK</sub>	PLL Lock time (1)	Stable power supply, valid clocks presented on CLKIN pin			1.0	ms

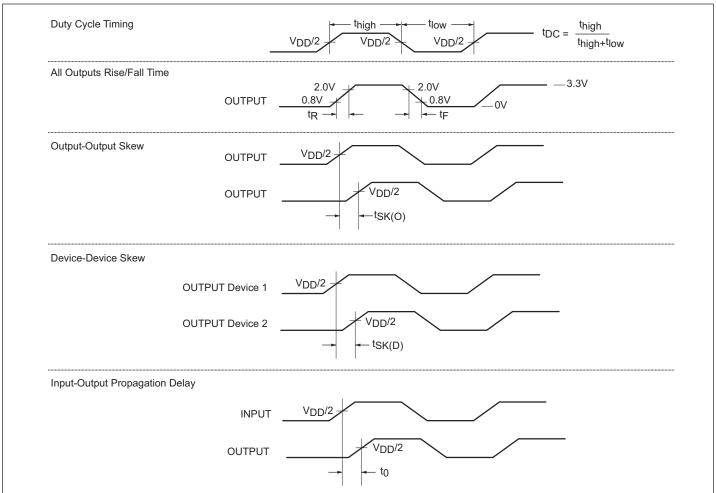
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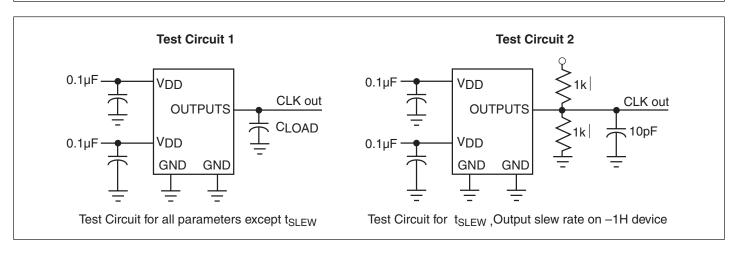
See Switching Waveforms on page 6.





## **Switching Waveforms**







## **Part Marking**

L Package

P6C240 5A-1L BYWXX

PI6C2405A-1LE B: Fab Port Code

Y: Year

W: Workweek

1st X: Assembly Code 2nd X: Fab Code

PC2405 A-1HL BYWXX

PI6C2405A-1HLE B: Fab Port Code

Y: Year

W: Workweek

1st X: Assembly Code 2nd X: Fab Code

#### W Package

PI6C2405 A-1WE BYYWWXX

PI6C2405A-1WE B: Fab Port Code

YY: Year

WW: Workweek

1st X: Assembly Code

2nd X: FabCode

PI6C2405 A-1HWE BYYWWXX

PI6C2405A-1HWE B: Fab Port Code

YY: Year

WW: Workweek

1st X: Assembly Code

2nd X: FabCode

PI6C2405 A-1HWIE BYYWWXX

PI6C2405A-1HWIE B: Fab Port Code

YY: Year

WW: Workweek

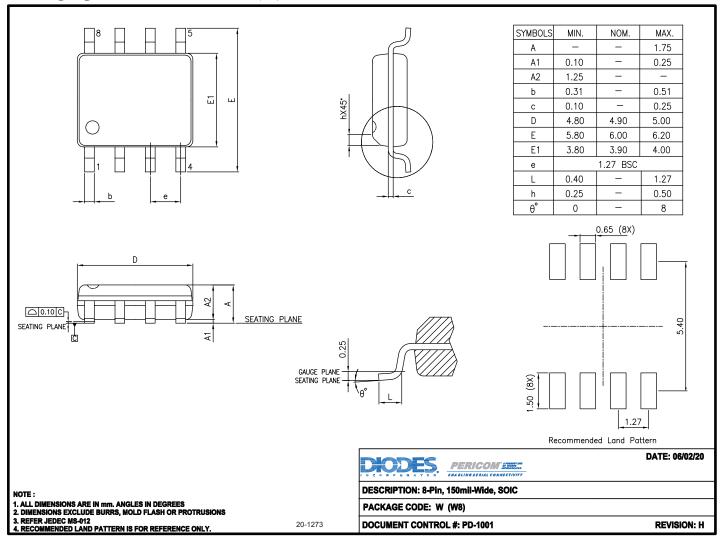
1st X: Assembly Code

2nd X: FabCode





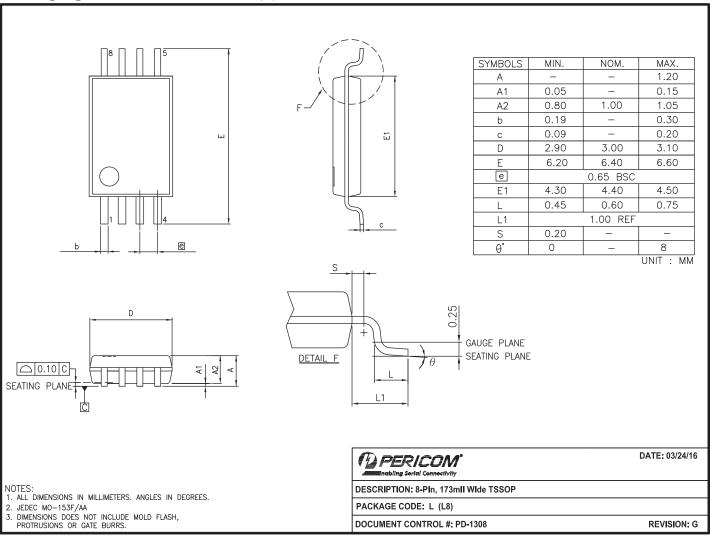
## Packaging Mechanical: 8-SOIC (W)







## Packaging Mechanical: 8-TSSOP(L)



16-0062

#### For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/





## **Ordering Information**

Ordering Code	Package Code	Package Description
PI6C2405A-1HWEX	W	8-pin, 150-mil wide (SOIC)
PI6C2405A-1HLEX	L	8-pin, 173-mil wide (TSSOP)
PI6C2405A-1WEX	W	8-pin, 150-mil wide (SOIC)
PI6C2405A-1LEX	L	8-pin, 173-mil wide (TSSOP)
PI6C2405A-1HWIEX	W	8-pin, 150-mil wide (SOIC)

#### Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- $2. \ See \ https://www.diodes.com/quality/lead-free/\ for\ more\ information\ about\ Diodes\ Incorporated's\ definitions\ of\ Halogen-\ and\ Antimony-free,\ "Green"\ and\ Lead-free.$
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. I = Industrial
- 5. E = Pb-free and Green
- 6. X suffix = Tape/Reel



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