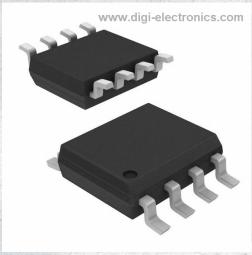


PI6C2405A-1WE Datasheet

Man



DiGi Electronics Part Number	PI6C2405A-1WE-DG
Manufacturer	Diodes Incorporated
Nanufacturer Product Number	PI6C2405A-1WE
Description	IC ZD BUFFER 8SOIC
Detailed Description	Zero Delay Buffer IC 133MHz 1 8-SOIC (0.154", 3. mm Width)

3.90

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PI6C2405A-1WE	Diodes Incorporated
Series:	Product Status:
	Last Time Buy
DiGi-Electronics Programmable:	Type:
Not Verified	Zero Delay Buffer
PLL:	Input:
Yes	πι
Output:	Number of Circuits:
TTL	1
Ratio - Input:Output:	Differential - Input:Output:
1:5	No/No
Frequency - Max:	Divider/Multiplier:
133MHz	No/No
Voltage - Supply:	Operating Temperature:
3V ~ 3.6V	0°C ~ 70°C
Mounting Type:	Package / Case:
Surface Mount	8-SOIC (0.154", 3.90mm Width)
Supplier Device Package:	Base Product Number:
8-SOIC	PI6C2405

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	



A Product Line of Diodes Incorporated



PI6C2405A-1/PI6C2405A-1H

Zero-Delay Clock Buffer

Features

- → Maximum rated frequency: 133 MHz
- → Low cycle-to-cycle jitter
- \rightarrow Input to output delay, less than 300ps
- \rightarrow Internal feedback allows outputs to be synchronized to the clock input
- → 5V tolerant input*
- \rightarrow Spread spectrum clock ready
- → Operates at 3.3V V_{DD}
- → Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- → Halogen and Antimony Free. "Green" Device (Note 3)
- \rightarrow For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.

https://www.diodes.com/quality/product-definitions/

- → Packaging (Pb-free & Green available): -8-pin, 150-mil SOIC (W)
 - -8-pin, 173-mil TSSOP (L)
- * CLKIN must reference the same voltage thresholds for the PLL to deliver zero delay skewing

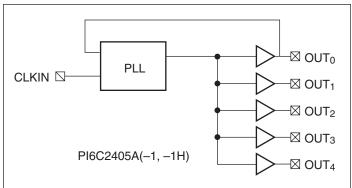
Description

The PI6C2405A-1/PI6C2405A-1H is a PLL based, zero-delay buffer, with the ability to distribute five outputs of up to 133MHz at 3.3V. All the outputs are distributed from a single clock input CLKIN and output OUT0 performs zero delay by connecting a feedback to PLL.

An internal feedback on OUT0 is used to synchronize the outputs to the input; the relationship between loading of this signal and the outputs determines the input-output delay. PI6C2405A-1/PI6C2405A-1H is able to track spread spectrum clocking for EMI reduction. PI6C2405A-1/PI6C2405A-1H is characterized for both commercial and industrial operation.

PI6C2405A-1H is a high-drive version of PI6C2405A-1.

Block Diagram



Notes:

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

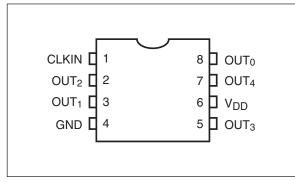
June 2021

^{1.} No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.





Pin Configuration



Pin Description

Pin#	Pin Name	Description
1	CLKIN	Input clock reference frequency (weak pull-down)
2, 3, 5, 7	OUT[1-4]	Clock Outputs
4	GND	Ground
6	V _{DD}	3.3V Supply
8	OUT0	Clock output, internal PLL feedback (weak pull-down)



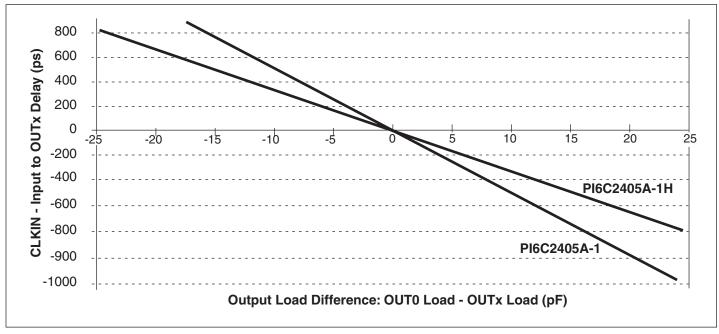
A Product Line of Diodes Incorporated



PI6C2405A-1/PI6C2405A-1H

Zero Delay and Skew Control

CLKIN Input to OUTx Delay vs. Difference in Loading between OUT0 pin and OUTx pins



The relationship between loading of the OUT0 signal and other outputs determines the input-output delay. Zero delay is achieved when all outputs, including feedback, are loaded equally.



Note:



PI6C2405A-1/PI6C2405A-1H

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	65°C to +150°C
Junction Temperature	+125°C Max.
Supply Voltage to Ground Potential	-0.5V to +4.6V
DC Input Voltage (Except CLKIN)	0.5V to V _{DD} +5.5V
ESD Protection (Input)	
_	

Stresses greater than those listed under MAXIMUM RAT-INGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

Operating Conditions ($V_{CC} = 3.3V \pm 0.3V$)

Parameter	Description	Min.	Max.	Units
V _{DD}	Supply Voltage	3.0	3.6	V
T _A	Commercial Operating Temperature	0 70		
	Industrial Operating Temperature	-40	85	°C
C	Load Capacitance, below 100 MHz		30	
CL	Load Capacitance, from 100 MHz to 133		15	pF
C _{IN}	Input Capacitance		7]

DC Electrical Characteristics for Industrial Temperature Devices

Parameter	Description	Test Conditions	Min.	Max.	Units	
V _{IL}	Input LOW Voltage			0.8	V	
V _{IH}	Input HIGH Voltage		2.0		v	
I _{IL}	Input LOW Current	$V_{IN} = 0V$		50		
I _{IH}	Input HIGH Current	$V_{IN} = V_{DD}$		125	μΑ	
V _{OL}	Output LOW Voltage	$I_{OL} = 8mA(-1); I_{OL} = 12mA(-1H)$		0.4	V	
V _{OH}	Output HIGH Voltage	I _{OH} = -8mA(-1); I _{OH} = -12mA(-1H)	2.4		v	
т	Sumply Cumont	Unloaded outputs 100 MHz, Select inputs at V_{DD} or GND		54		
I _{DD}	Supply Current	Unloaded outputs 66 MHz, CLKIN	39 mA	IIIA		





AC Electrical Characteristics for Industrial Temperature Devices

Parameter	Description	Test Conditions		Тур.	Max.	Units	
Г		30pF load	10		100	MIL	
F _O Output Frequency		15pF load	10		133	MHz	
	Duty Cycle ⁽¹⁾ (-1)	Measured at V _{DD} /2, F _{OUT} < 66.67MHz, 30pF load	40	40		-	
	Duty Cycle ⁽⁻⁾ (-1)	Measured at $V_{DD}/2$, $F_{OUT} < 45$ MHz 15pF load	45	50	55	0/	
t _{DC}	$\mathbf{D} \in \mathbf{C} \setminus \{\mathbf{l}\} \cup \{\mathbf{u}\}$	Measured at V _{DD} /2, F _{OUT} < 100MHz 15pF load	40	50	60	%	
	Duty Cycle ⁽¹⁾ (-1H)	Measured at V _{DD} /2, F _{OUT} < 45MHz 30pF load	45		55		
	D: T: (1)(1)	Measured between 0.8V and 2.0V, 30pF load			2.2		
t _R	Rise Time ^{(1)} (-1)	Measured between 0.8V and 2.0V, 15pF load			1.5		
	Rise Time ⁽¹⁾ (-1H)	Measured between 0.8V and 2.0V, 30pF load			1.7	ns	
	D 11 (1) (1)	Measured between 0.8V and 2.0V, 30pF load			2.2		
t _F	Fall Time ⁽¹⁾ (-1)	Measured between 0.8V and 2.0V, 15pF load			1.5	.5	
	Fall Time ⁽¹⁾ (-1H)	Measured between 0.8V and 2.0V, 30pF load			1.5	1	
t _{sk(o)}	Output to Output skew (-1, -1H) ⁽¹⁾	All outputs equally loaded			200		
t ₀	Delay, CLKIN Rising Edge to OUT0 Rising Edge ⁽¹⁾	Measured at $V_{DD}/2$		0	±300 ps		
t _{SK(D)}	Device-to-device skew ⁽¹⁾	Measured at $V_{DD}/2$ on OUT0 pins of device		0	600		
t _{SLEW}	Output slew rate ⁽¹⁾	Measured between 0.8V and 2.0V on -1H device us- ing Test Circuit #2	1			V/ns	
t _{JIT}	Cycle-to-Cycle Jitter (-1, -1H)	Measured at 66.67 MHz, loaded 30pF load			200	ps	
t _{LOCK}	PLL Lock time ⁽¹⁾	Stable power supply, valid clocks presented on CLKIN pin			1.0	ms	

Notes:

See Switching Waveforms on page 6. 1.

DC Electrical Characteristics for Commercial Temperature Devices

Parameter	Description	Test Conditions		Max.	Units	
V _{IL}	Input LOW Voltage			0.8	V	
V _{IH}	Input HIGH Voltage		2.0			
I _{IL}	Input LOW Current	$V_{IN} = 0V$		50		
I _{IH}	Input HIGH Current	$V_{IN} = V_{DD}$		125	5 μA	
V _{OL}	Output LOW Voltage	$I_{OL} = -8mA(-1); I_{OL} = 12mA(-1H)$		0.4	V	
V _{OH}	Output HIGH Voltage	$I_{OH} = -8mA(-1); I_{OH} = -12mA(-1H)$	2.4			
I _{DD} Supply Current	Course los Courses et	Unloaded outputs 100 MHz, Select inputs at V_{DD} or GND		54		
	Unloaded outputs 66.67 MHz, select inputs at V_{DD} or GND		39	mA		





AC Electrical Characteristics for Commercial Temperature Devices

Parameter	Description Test Conditions		Min.	Тур.	Max.	Units	
Г	Output Erequency 30pF load		10		100	N411-	
FO	Output Frequency	15pF load	10		133	MHz	
1	Duty Cycle ⁽¹⁾ (-1)	Measured at $V_{DD}/2$, $F_O < 66$ MHz, 30pF load	40	50	60	- %	
t _{DC}	Duty Cycle ⁽¹⁾ (-1H)	Measured at $V_{DD}/2$, $F_O < 66$ MHz, 30pF load	45	50	55	%	
	Rise Time ⁽¹⁾ @ 30pF				2.2		
t _R	Rise Time ⁽¹⁾ @ 15pF	Measured between 0.8V and 2.0V			1.5		
	Rise Time ⁽¹⁾ @ 30pF (-1H)				1.5		
	Fall Time ⁽¹⁾ @ 30pF				2.2	ns	
t _F	Fall Time ⁽¹⁾ @ 15pF	Measured between 0.8V and 2.0V			1.5		
	Fall Time ⁽¹⁾ @ 30pF (-1H)				1.5		
t _{sk(o)}	Output to Output skew (-1, -1H) ⁽¹⁾	All outputs equally loaded			200		
t ₀	Input to output delay, CLKIN Rising Edge to OUT0 Rising Edge ⁽¹⁾	Measured at $V_{DD}/2$		0	±300	0 ps	
t _{SK(D)}	Device-to-device skew ⁽¹⁾	Measured at $V_{DD}/2$ on OUT0 pins of device		0	600]	
t _{SLEW}	Output slew rate ⁽¹⁾	Measured between 0.8V and 2.0V on -1H device using Test Circuit #2	1			V/ns	
t _{JIT}	Cycle-to-Cycle Jitter (-1, -1H)	Measured at 66.67 MHz, loaded 30pF load			200	ps	
t _{LOCK}	PLL Lock time ⁽¹⁾	Stable power supply, valid clocks presented on CLKIN pin			1.0	ms	

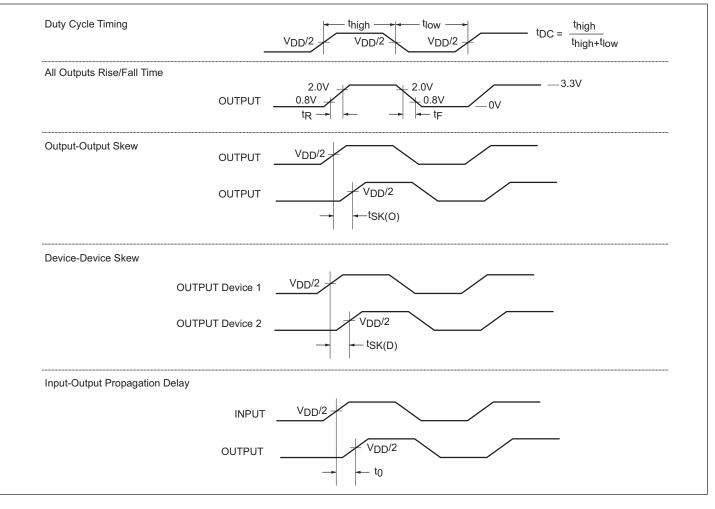
Notes:

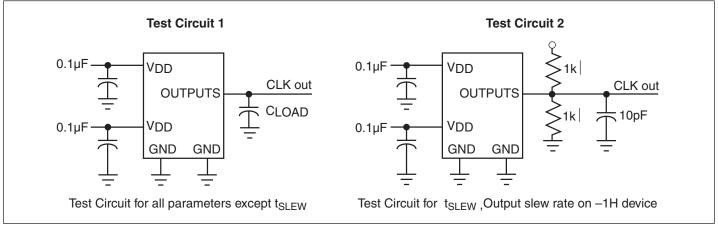
1. See Switching Waveforms on page 6.





Switching Waveforms







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PI6C2405A-1/PI6C2405A-1H

Part Marking

L Package



PI6C2405A-1LE B: Fab Port Code Y: Year W: Workweek 1st X: Assembly Code 2nd X: Fab Code



PI6C2405A-1HLE B: Fab Port Code Y: Year W: Workweek 1st X: Assembly Code 2nd X: Fab Code

W Package



PI6C2405A-1WE B: Fab Port Code YY: Year WW: Workweek 1st X: Assembly Code 2nd X: FabCode



PI6C2405A-1HWE B: Fab Port Code YY: Year WW: Workweek 1st X: Assembly Code 2nd X: FabCode

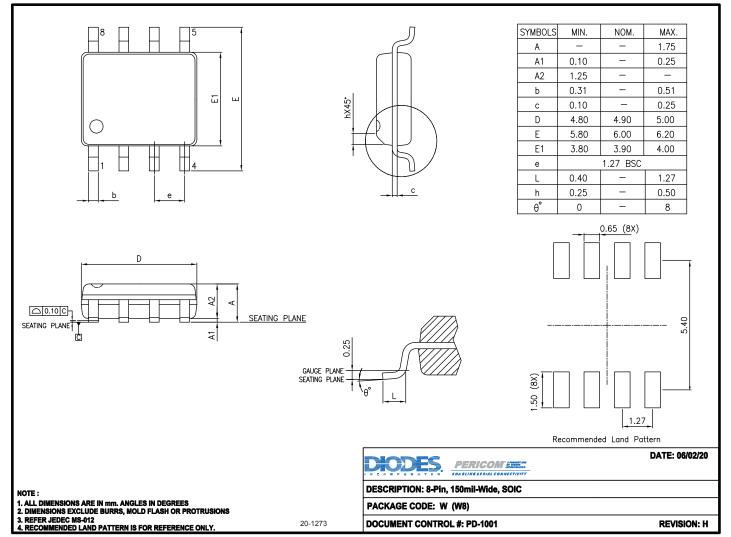


PI6C2405A-1HWIE B: Fab Port Code YY: Year WW: Workweek 1st X: Assembly Code 2nd X: FabCode





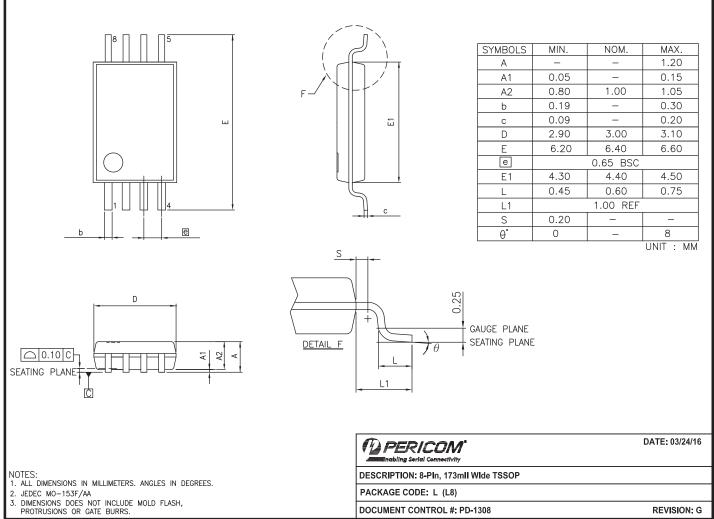
Packaging Mechanical: 8-SOIC (W)







Packaging Mechanical: 8-TSSOP(L)



16-0062

For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/





Ordering Information

Ordering Code	Package Code	Package Description
PI6C2405A-1HWEX	W	8-pin, 150-mil wide (SOIC)
PI6C2405A-1HLEX	L	8-pin, 173-mil wide (TSSOP)
PI6C2405A-1WEX	W	8-pin, 150-mil wide (SOIC)
PI6C2405A-1LEX	L	8-pin, 173-mil wide (TSSOP)
PI6C2405A-1HWIEX	W	8-pin, 150-mil wide (SOIC)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. I = Industrial

5. E = Pb-free and Green

6. X suffix = Tape/Reel





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