

PI6C49X0202WIEX Datasheet

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DiGi Electronics Part Number	PI6C49X0202WIEX-DG
Manufacturer	Diodes Incorporated
anufacturer Product Number	PI6C49X0202WIEX
Description	IC CLK BUFFER 1:2 250MHZ 8SOIC
Detailed Description	Clock Fanout Buffer (Distribution) IC 1:2 250 MHz 8- SOIC (0.154", 3.90mm Width)

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Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
PI6C49X0202WIEX	Diodes Incorporated
Series:	Product Status:
	Active
Type:	Number of Circuits:
Fanout Buffer (Distribution)	1
Ratio - Input:Output:	Differential - Input:Output:
1:2	No/No
Input:	Output:
LVCMOS, LVTTL	LVCMOS, LVTTL
Frequency - Max:	Voltage - Supply:
250 MHz	2.375V ~ 3.465V
Operating Temperature:	Mounting Type:
-40°C ~ 85°C	Surface Mount
Package / Case:	Supplier Device Package:
8-SOIC (0.154", 3.90mm Width)	8-SOIC
Base Product Number:	
PI6C49	

Environmental & Export classification

RoHS Status:	Moisture Sensitivity Level (MSL):
ROHS3 Compliant	1 (Unlimited)
REACH Status:	ECCN:
REACH Unaffected	EAR99
HTSUS:	
8542.39.0001	





Low Skew, 1-To-2 LVCMOS/LVTTL Fanout Buffer

Description

The DIODES PI6C49X0202 is a low skew, 1-to-2 LVCMOS/ LVTTL High Performance Fanout Buffer. The PI6C49X0202 has a single ended clock input. The single ended clock input accepts LVCMOS or LVTTL input levels. The PI6C49X0202 features a pair of LVCMOS/LVTTL outputs. Guaranteed output and partto-part skew characteristics make the PI6C49X0202 ideal for clock distribution applications demanding well defined performance and repeatability.

Block Diagram



Features

- 2 LVCMOS/LVTTL Outputs
- LVCMOS/LVTTL Clock Input Accepts LVCMOS or LVTTL Input Levels
- Maximum Output Frequency: 250MHz ٠
- Output Skew: 25ps (typical)
- Part-to-Part Skew: 250ps (typical) •
- Full 3.3V, 2.5V Operation Modes •
- Ambient Operating Temperature: -40°C to 85°C •
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2) •
- Halogen and Antimony Free. "Green" Device (Note 3)
- For automotive applications requiring specific change control • (i.e. parts qualified to AEC-Q100/101/104/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please contact us or your local Diodes representative.
 - https://www.diodes.com/quality/product-definitions/
- Packaging (Pb-free & Green): • • 8-pin, SOIC (W)





Pin Configuration



Pin Descriptions

Pin#	Pin Name	Туре		Pin Description
1, 6	VDDO	Power		Output supply pins.
2	VDD	Power		Core supply pin.
3	CLK	Input	Pulldown	LVCMOS/LVTTL clock input.
4,7	GND	Power		Power supply ground.
5	Q1	Output		Single clock output. LVCMOS/LVTTL interface levels.
8	Q0	Output		Single clock output. LVCMOS/LVTTL interface levels.

Note: Pulldown refer to internal input resistors, typical values in Pin Characteristics table.

Pin Characteristics

Symbol	Parameter	Min.	Тур.	Max.	Units
C _N	Capacitance		4		pF
R _{PULLDOWN}	Input Pulldown Resistor		51		kΩ
R _{OUT}	Output Impedance	5	7	12	Ω





Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)	
	Maximum Supply Voltage, VDD, VDDO. 4.6V Inputs, V1. -0.5V to VDD +0.5V Output, V0. -0.5V to VDDO +0.5V Storage Temperature -65°C to 150°C ESD Protection (HBM) 2000V	Note: Stresses beyond those listed under Abs cause permanent damage to the device. fications only. Functional operation of any conditions beyond those listed in the Characteristics is not implied. Exposur conditions for extended periods may affe

solute Maximum Ratings may These ratings are stress speciproduct at these conditions or the DC Characteristics or AC e to absolute maximum rating ect product reliability.

Power Supply DC Characteristics

$T_A = -40^{\circ}C$ to $85^{\circ}C$

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Units
VDD	Com Seconda Valta as	3.3V Operation	3.135	3.3	3.465	17
VDD	Core Supply Voltage	2.5V Operation	2.375	2.5	2.625	V
VDDO	Outrast Descent Control to Welter an	3.3V Supply	3.135	3.3	3.465	17
VDDO	Output Power Supply Voltage	2.5V Supply	2.375	2.5	2.625	V
IDD	Power Supply Current				5	mA
IDDO	Output Supply Current	Unloaded, 25MHz			6.5	mA

Note: Parameters measured up to f_{max} unless otherwise noted.

LVCMOS/LVTTL DC Characteristics

$T_A = -40^{\circ}C$ to	85°C						
Symbol	Parameter	Conditions		Min.	Тур.	Max.	Units
37		VDD = 3.3V		2		VDD+0.3	3.7
V _{IH}	Input High voltage	VDD = 2.5V		1.7		VDD+0.3	V
V	Lenut Lovy Waltage	VDD = 3.3V		-0.3		0.8	N7
V IL	Input Low Voltage	VDD = 2.5V		-0.3		0.8	v
т	Input High Current	$VDD = V_{IN} = 3.465V$				100	A
I _{IH}	Input Fign Current	$VDD = V_{IN} = 2.6$	25V			80	μΑ
т			$VDD = 3.465V, V_{IN} = 0V$				A
IIL	IL Input Low Current		$VDD = 2.625V, V_{IN} = 0V$				μΑ
V	Output High Voltage	VDDO = 3.3V	$I_{\rm OH}=-100\mu A$	2.9			V
V OH	Output High Voltage	VDDO = 2.5V	$I_{\rm OH}=-100\mu A$	2.2			V
V _{OL}	Output Low Voltage	VDDO = 3.3V	$I_{\rm OL}=100\mu A$			0.2	V
	Output Low Voltage	VDDO = 2.5V	$I_{\rm OL}=100\mu A$			0.2	V





AC Characteristics

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0=00

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Units
C		VDDO = 3.3V	4		250	MIL
I _{MAX}	Output Frequency	VDDO = 2.5V	4		250	MHZ
		VDDO = 3.3 V, $f \le 250$ MHz	1.4		2.2	
tр _{LH}	Propagation Delay, Low-to-High	VDDO = 2.5V, $f \le 250$ MHz	1.5		3.0	ns
<i>tsk</i> (0)	Output Skew ⁽²⁾			25	80	ps
<i>tsk</i> (pp)	Part-to-Part Skew ⁽³⁾			250	800	ps
4	Outrast Disc Time (4)	VDDO = 3.3V	100	300	400	
t_R	Output Rise Time	VDDO = 2.5V	100	350	500	ps
		VDDO = 3.3V	100	300	400	
$\tau_{\rm F}$	Output Fall Time	VDDO = 2.5V	100	350	500	ps
		$f \le 133 \text{MHz}$	48		52	%
odc	Output Duty Cycle ⁽⁵⁾	133 MHz < $f \le 200$ MHz	47		53	%
		$200 \text{MHz} < f \le 250 \text{MHz}$	47		53	%
1	Addition DMC little	156.25MHz (@12kHz to 20MHz)		0.1		ps
ljit	Additive KINS Jitter	125MHz (@12kHz to 20MHz)		0.07		ps

Note:

Parameters measured at f $_{\rm MAX}$ unless otherwise noted.

- 1. Measured from VDD /2 of the input to VDDO /2 of the output.
- 2. Defined as skew between outputs at the same supply voltage and with equal load conditions. Measured at VDDO /2.

Defined as skew between outputs on different devices operating at the same supply voltages and with equal load conditions. Using the same type of inputs on 3. each device, the outputs are measured at VDDO /2.

4. Defined from 20% to 80%

Measured at VDDO /2 5





AC Characteristics

$VDD = 2.5V \pm$	5%,	$T_A =$	-40°C	to	85°C	
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Symbol	Parameter Conditions		Min.	Тур.	Max.	Units
f _{MAX}	Output Frequency	VDDO = 2.5V	4		250	MHz
tp_{LH}	Propagation Delay, Low-to-High ⁽¹⁾	VDDO = 2.5V, $f \le 250$ MHz	1.5		2.8	ns
tsk(0)	Output Skew ⁽²⁾			25	75	ps
<i>tsk</i> (pp)	Part-to-Part Skew ⁽³⁾			250	800	ps
t _R	Output Rise Time ⁽⁴⁾	VDDO = 2.5V	100	350	500	ps
t _F	Output Fall Time ⁽⁴⁾	VDDO = 2.5V	100	350	500	ps
		$f \le 133 \text{MHz}$	48		52	%
odc	Output Duty Cycle ⁽⁵⁾	133 MHz < $f \le 200$ MHz	47		53	%
		$200 \text{MHz} < f \le 250 \text{MHz}$	42		58	%
+	Additive DMS litter	156.25MHz (@12kHz to 20MHz)		0.1		ps
Ljit	Additive Kivis Juter	125MHz (@12kHz to 20MHz)		0.07		ps

Note:

Parameters measured at f_{MAX} unless otherwise noted.

- Measured from VDD /2 of the input to VDDO /2 of the output. 1.
- Defined as skew between outputs at the same supply voltage and with equal load conditions. Measured at VDDO /2. 2.
- Defined as skew between outputs on different devices operating at the same supply voltages and with equal load conditions. Using the same type of inputs on 3. each device, the outputs are measured at VDDO /2.
- 4. Defined from 20% to 80%
- 5. Measured at VDDO /2

AC Test Circuit Load







Part Marking

PI6C49X 0202WIE YYYWWXX 0

Y: Die Rev YY: Year WW: Workweek 1st X: Assembly Code 2nd X: Fab Code





Packaging Mechanical

8-SOIC (W)



For latest package info.

please check: http://www.diodes.com/design/support/packaging/pericom-packaging/packaging-mechanicals-and-thermal-characteristics/

Ordering Information

Ordering Code	Package Code	Package Description
PI6C49X0202WIEX	W	8-pin, 150mil-Wide (SOIC)

Notes:

1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.

2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free. 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

4. I = Industrial

5. E = Pb-free and Green

6. X suffix = Tape/Reel





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