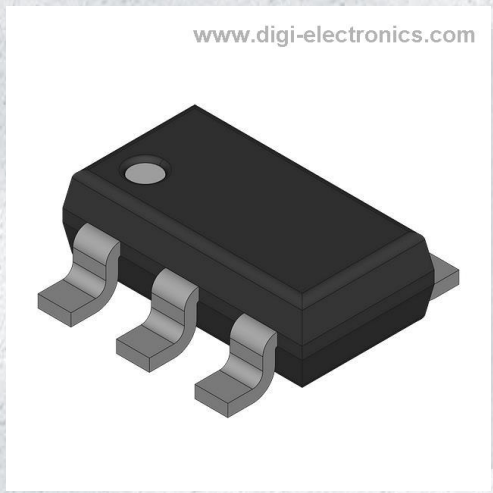
CPH6604-TL-E Datasheet



https://www.DiGi-Electronics.com

DiGi Electronics Part Number	CPH6604-TL-E-DG
Manufacturer	Sanyo
Manufacturer Product Number	CPH6604-TL-E
Description	N-CHANNEL SILICON MOSFET
Detailed Description	

Tel: +00 852-30501935

RFQ Email: Info@DiGi-Electronics.com

Purchase and inquiry

Manufacturer Product Number:	Manufacturer:
CPH6604-TL-E	Sanyo
Series:	Product Status:
*	Active

Environmental & Export classification

Moisture Sensitivity Level (MSL):	REACH Status:
Vendor Undefined	Vendor Undefined

Ordering number : ENN7147

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DATA SHEET

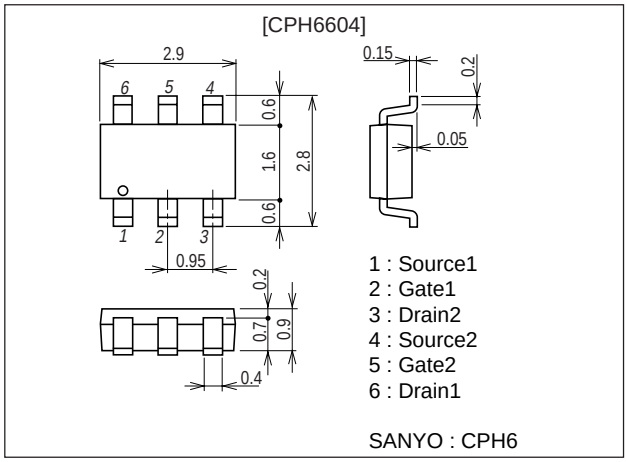
CPH6604 — N-Channel Silicon MOSFET Ultrahigh-Speed Switching Applications

Features

- Low ON-resistance.
- Ultrahigh-speed switching.
- 4V drive.

Package Dimensions

unit : mm
2202



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		30	V
Gate-to-Source Voltage	V_{GSS}		±20	V
Drain Current (DC)	I_D		2.0	A
Drain Current (Pulse)	I_{DP}	PW≤10μs, duty cycle≤1%	8.0	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board (900mm²×0.8mm)1unit	0.9	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	I_D=1mA, V_{GS}=0	30			V
Zero-Gate Voltage Drain Current	I_{DSS}	V_{DS}=30V, V_{GS}=0			1	μA
Gate-to-Source Leakage Current	I_{GSS}	V_{GS}=±16V, V_{DS}=0			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS}=10V, I_D=1mA	1.2		2.6	V
Forward Transfer Admittance	$\|yfs\|$	V_{DS}=10V, I_D=1A	1.3	1.8		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	I_D=1A, V_{GS}=10V		115	150	mΩ
	$R_{DS(on)2}$	I_D=0.5A, V_{GS}=4V		190	270	mΩ

Marking : FP

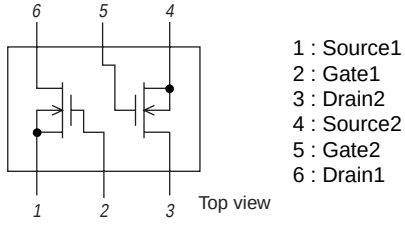
Continued on next page.

CPH6604

Continued from preceding page.

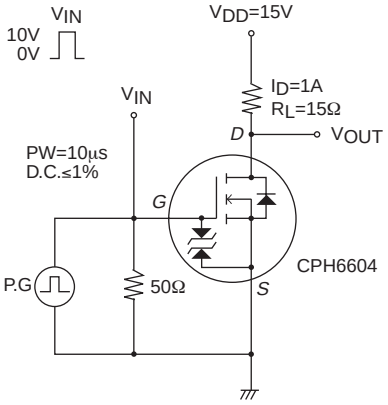
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	Ciss	V_{DS}=10V, f=1MHz		120		pF
Output Capacitance	Coss	V_{DS}=10V, f=1MHz		30		pF
Reverse Transfer Capacitance	Crss	V_{DS}=10V, f=1MHz		15		pF
Turn-ON Delay Time	t_{d}(on)	See specified Test Circuit.		6		ns
Rise Time	t_{r}	See specified Test Circuit.		4		ns
Turn-OFF Delay Time	t_{d}(off)	See specified Test Circuit.		17		ns
Fall Time	t_{f}	See specified Test Circuit.		5		ns
Total Gate Charge	Qg	V_{DS}=10V, V_{GS}=10V, I_{D}=2.0A		3.6		nC
Gate-to-Source Charge	Qgs	V_{DS}=10V, V_{GS}=10V, I_{D}=2.0A		0.6		nC
Gate-to-Drain “Miller” Charge	Qgd	V_{DS}=10V, V_{GS}=10V, I_{D}=2.0A		0.5		nC
Diode Forward Voltage	V_{SD}	I_{S}=2.0A, V_{GS}=0		0.87	1.2	V

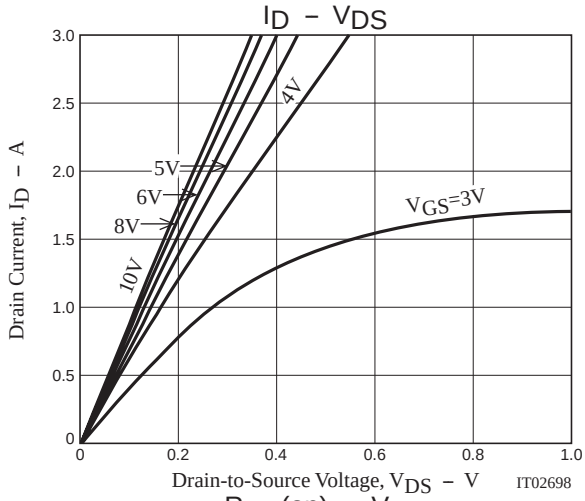
Electrical Connection

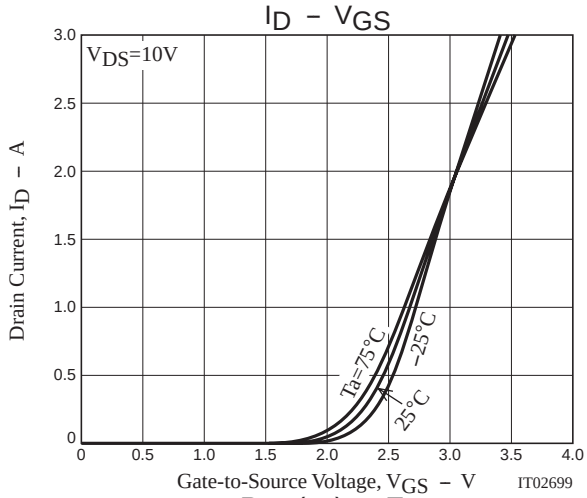


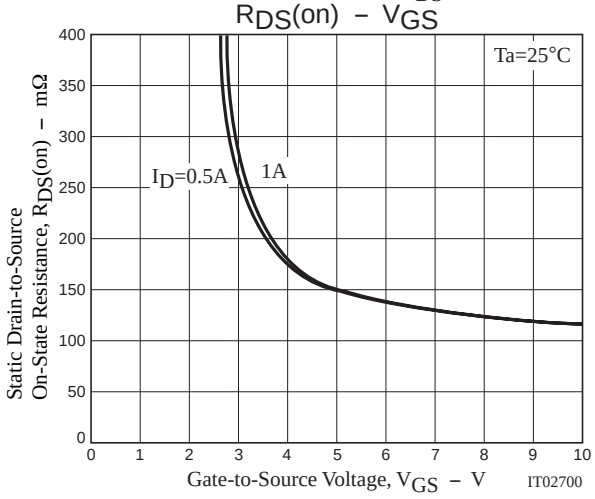
1 : Source1
2 : Gate1
3 : Drain2
4 : Source2
5 : Gate2
6 : Drain1

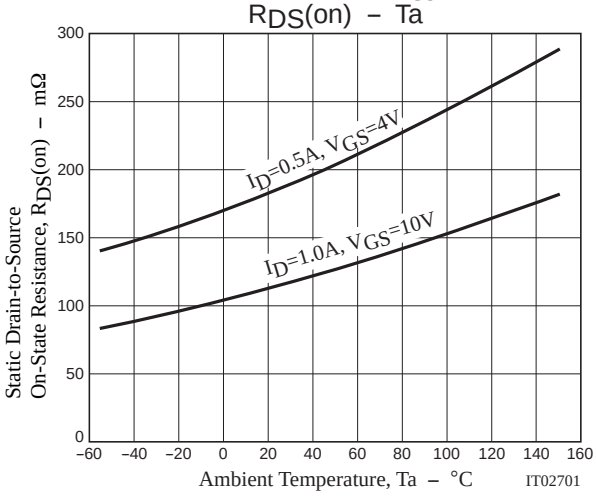
Switching Time Test Circuit











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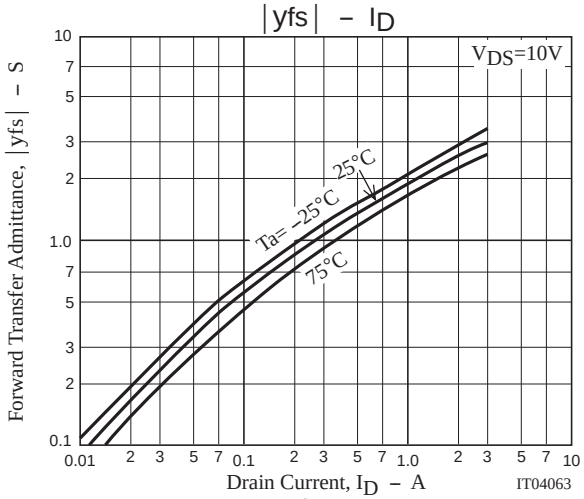
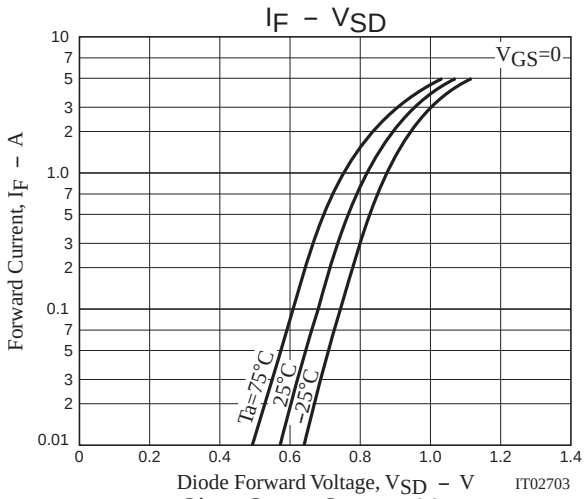
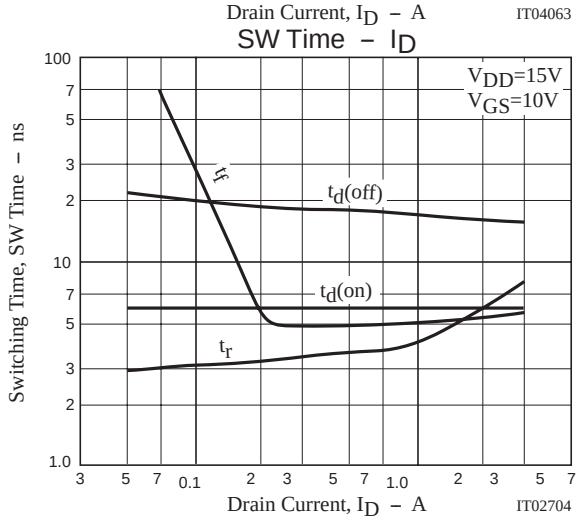
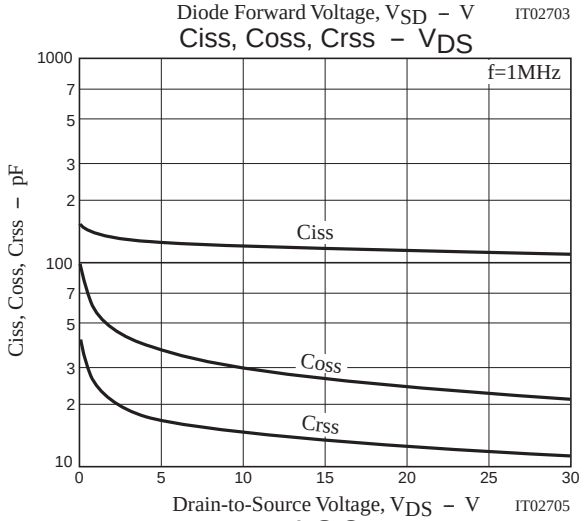
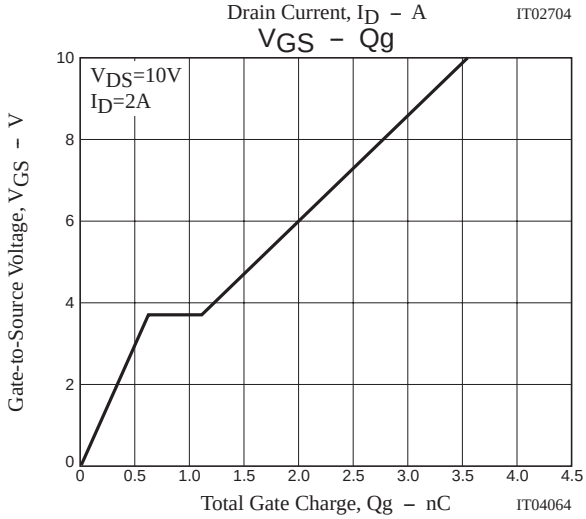
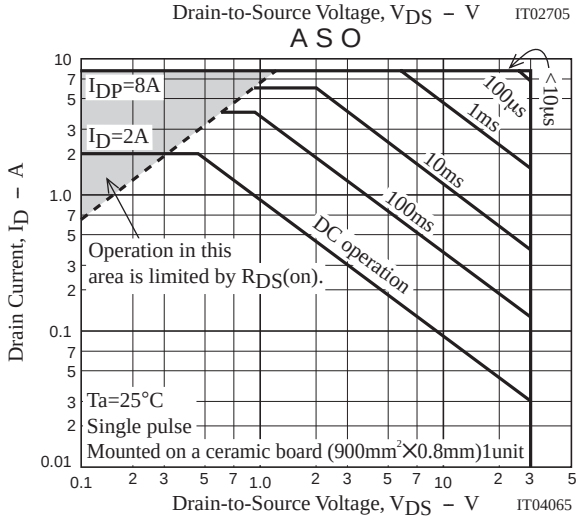

|yfs| – ID
VDS=10V
25°C
Ta= –25°C
75°C
Forward Transfer Admittance, |yfs| – S
Drain Current, ID – A
IT04063

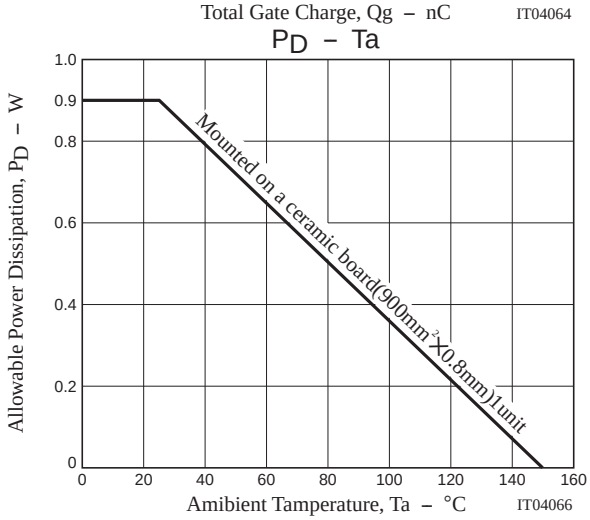

IF – VSD
VGS=0
Ta=75°C
25°C
–25°C
Forward Current, IF – A
Diode Forward Voltage, VSD – V
IT02703


SW Time – ID
VDD=15V
VGS=10V
tf
td(off)
td(on)
tr
Switching Time, SW Time – ns
Drain Current, ID – A
IT02704


Ciss, Coss, Crss – VDS
f=1MHz
Ciss
Coss
Crss
Ciss, Coss, Crss – pF
Drain-to-Source Voltage, VDS – V
IT02705


VGS – Qg
VDS=10V
ID=2A
Gate-to-Source Voltage, VGS – V
Total Gate Charge, Qg – nC
IT04064


A S O
IDP=8A
ID=2A
100µs
1ms
10ms
100ms
DC operation
Operation in this
area is limited by RDS(on).
Ta=25°C
Single pulse
Mounted on a ceramic board (900mm²×0.8mm)1unit
Drain Current, ID – A
Drain-to-Source Voltage, VDS – V
IT04065


PD – Ta
Mounted on a ceramic board(900mm²×0.8mm)1unit
Allowable Power Dissipation, PD – W
Amibient Tamperature, Ta – °C
IT04066

CPH6604

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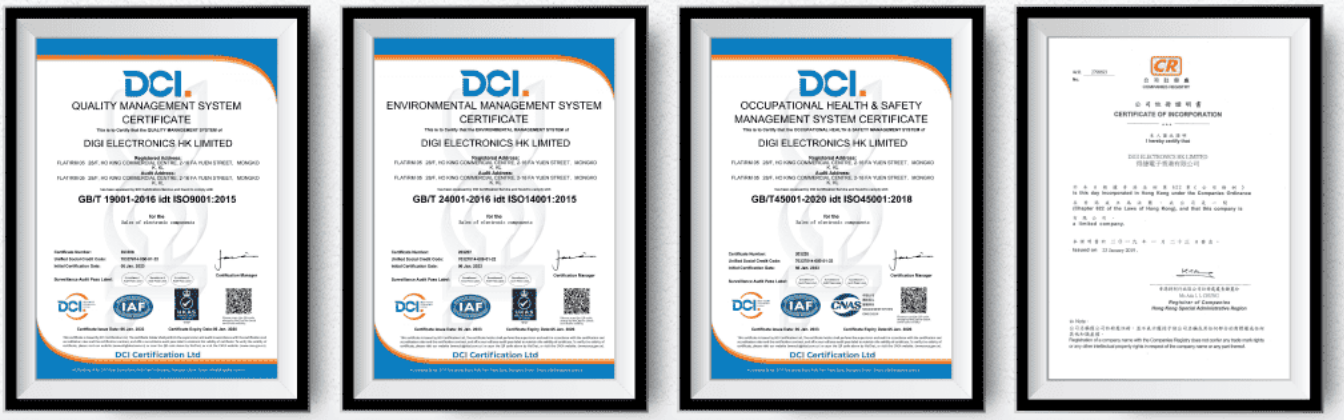

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